IJIBC 24-2-13

Design of a Converter with Anti-blinking Circuitry for T5 LED Indirect Lighting

Woo-young Kim¹, Quoc Cuong Nguyen², Seong-Kweon Kim³

 ¹Ph.D. Candidate, Department of Information Technology & Media Engineering, Seoul National University of Science and Technology, Korea
²Assistant Professor, Faculty of Engineering, Dong Nai Technology University, Bien Hoa city, Vietnam
³Professor, Department of Information Technology & Media Engineering, Seoul National University of Science and Technology, Korea

¹hero_486@hanmail.net, ²nguyenquoccuong@dntu.edu.vn, ³kim12632@seoultech.ac.kr

Abstract

We address the problematic issue of blinking in residential LED lighting systems—a phenomenon that has recently become a significant concern due to voltage sags caused by high-power household appliances. To combat this, we developed a two-stage LED converter with integrated anti-blinking circuitry, specifically designed for T5 LED indirect lighting fixtures. The first stage employs a Power Factor Correction (PFC) boost circuit to enhance voltage stability by aligning the voltage and current phases, thereby minimizing power losses. The second stage, a meticulously engineered DC-DC buck converter, ensures stable lighting despite electrical fluctuations. Rigorous testing has confirmed our converter's efficacy in maintaining consistent light output without blinking, thereby substantially improving user comfort and adhering to strict standards for harmonic distortion and electromagnetic compatibility. Our breakthrough provides a robust solution to a pressing issue, marking a significant advancement in LED lighting technology.

Keywords: LED, Luminaire, Converter, Voltage sag, Two-stage circuit, Blinking, Lighting

1. Introduction

In recent advancements within residential lighting technologies, LEDs have become central due to their energy efficiency, longevity, and environmental friendliness [1, 2]. Despite their widespread adoption, an increasingly significant issue is the occurrence of blinking in LED lighting systems. This problem, caused by abrupt and irregular light interruptions, has escalated into a notable concern due to its potential to induce stress and health issues in users, thereby necessitating prompt and effective solutions [3].

Until recently, the blinking issue in LED lights has been prematurely attributed to faults within the LEDs or their converters. Efforts to mitigate these disruptions typically involved superficial remedies such as

Manuscript Received: March. 25, 2024 / Revised: April. 2, 2024 / Accepted: April. 8, 2024

Tel: +82-2-970-6432,

Corresponding Author: kim12632@seoultech.ac.kr

Professor, Department of Information Technology & Media Engineering, Seoul National University of Science and Technology, Korea

modifying the converter's PCB layout or replacing the driver ICs without a true understanding of the underlying causes. Recent investigations have revealed that the primary cause of blinking is voltage sags initiated by the use of high-power household appliances. Devices requiring significant power loads, such as electric bidet seats, heaters, induction cookers, and instant water heaters, contribute to these voltage sags. These appliances provoke sudden voltage fluctuations at the input of LED converters, leading directly to blinking. Such voltage fluctuations often result from the rapid on/off cycling of these high-demand appliances, culminating in converter malfunctions that manifest as blinking [4-6].

This issue affects not only LED lighting but also other electronic devices, although it is most visually noticeable in LED lights, significantly inconveniencing users. Consequently, major construction companies have started to mandate the incorporation of converters that are resilient to external disturbances in their lighting fixture specifications, actively combating the blinking issue [7].

Given these challenges, there is a clear demand for a reevaluation of power network designs and power usage management in residential settings. This encompasses not only technological enhancements but also a deeper comprehension of household power consumption patterns and thorough analyses of the interactions between electrical appliances and lighting systems. This paper proposes the integration of an anti-blinking circuit within a newly designed, compact, and highly efficient LED converter. Specifically developed for T5 LED fixtures, which are increasingly popular in indirect lighting applications, this solution is aimed at enhancing the usability of LED lighting and ultimately safeguarding user health and comfort.

2. Design Structure

2.1 Definition of Voltage Sag

According to IEEE-1159, a voltage sag is characterized by a decrease in RMS voltage to between 0.9 PU and 0.1 PU over durations ranging from half a cycle to one minute. This phenomenon is classified into three types based on duration: instantaneous, momentary, and temporary, as detailed in the table below [8].

Table 1. Classification of instantaneous voltage sags

Voltage Dips Classification	Instantaneous	Momentary	Temporary		
Duration	0.5 ~ 30 cycles	0.5 ~ 30 cycles	3 sec. ~ 1 min.		

2.2 Configuration of the Two-stage LED Converter

To address voltage sags and meet the standards of KS C IEC 61347-2-13 and KS C IEC 62384, the converter is designed as a two-stage circuit

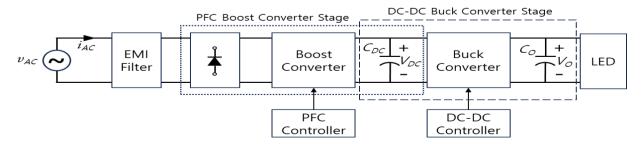


Figure 1. The conceptual diagram of the double-stage LED-driving power converter

The first stage uses a Power Factor Correction (PFC) boost circuit to align the phase angle close to zero degrees, minimizing the phase difference between voltage and current, thus approaching real power while meeting international standards for maximum allowable harmonic current [9, 10].

The second stage consists of a DC-DC buck converter that receives the boosted voltage to supply a stable output to the LED module. Given the specifications of T5 fluorescent lamp fixtures, which typically measure about 16 mm in diameter, the recent transition to LED equivalents does not demand exact dimension compliance but does require a simplified circuit due to the confined space within the converter housing [11]. Consequently, a non-isolated buck converter is designed to fit within these compact dimensions [12].

2.3 Circuit Design and Component Configuration

The control ICs selected for assembling the two-stage circuitry include built-in FETs, which help reduce the size of the circuit board while ensuring heat generated by the circuit is managed efficiently. Figure 2 illustrates the schematic of the circuit using two FET-integrated controller ICs, based on which component values are determined and PCB artwork for converter sample production is planned.

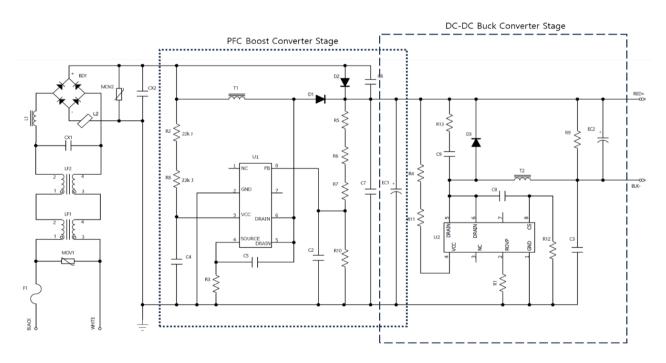


Figure 2. Schematic of a two-stage LED-driving power converter

In the AC input stage, the design incorporates an EMI filter to minimize electromagnetic interference, ensuring the converter meets electromagnetic compatibility standards. Following rectification, the voltage is boosted to 400V through the PFC controller(U1) and the switching transformer(T1). At this time, the PFC controller compares the output voltage through the FB pin to an internal high-precision 2.5 V reference to maintain a constant output voltage. In closed-loop control, the output voltage is determined by the following equation (1).

$$V_o(V) = \frac{V_{ref}}{R_{fb_2}} \times (R_{fb_1} + R_{fb_2})$$
(1)

In the equation above, R_{fb_1} means the Upper voltage divider resistor and R_{fb_1} means the resistor between FB pin and GND.

In the PFC stage, the rectified 400V DC voltage is fed into a DC-DC buck converter. It is then converted to a lower voltage suitable for the LED module through controller U1 and switching transformer(T2). T5 LED lighting fixtures of 1200mm length typically consume around 20 W of power. The DC-DC controller used in the buck circuit of the second stage is in QR-Buck mode, where the IC keeps the CS peak current constant and starts a new PWM cycle with valley switching. Thus, high-precision CC and high conversion efficiency can be achieved simultaneously. The average LED regulated output current is given by the following equation (2).

$$I_{cc_{out}}(mA) = \frac{1}{2} \cdot \frac{V_{cs(max)}}{R_{cs}} = \frac{300mV}{R_{cs}(\Omega)}$$
(2)

In the equation above, R_{CS} means the sensing resistor connected between the CS pin to IC GND. Utilizing this approach, the converter's output current was designed to maintain a constant current of 160 mA. When operating with a 114 V LED load, the converter aims for a power consumption of 20 W and an efficiency of approximately 91%. Over Voltage Protection (OVP) was set to a maximum of 155 V to accommodate voltage variations, and the design allows for a minimum power consumption of 12 W based on adjustments to the load voltage.

2.2 Artwork for the Converter PCB

Before designing the PCB, the structure of the T5 lighting fixture targeted for application was analyzed to select the appropriate size for the PCB and components, and to plan the artwork. Figure 3 is a drawing of the intended T5 fixture, which has a width of 22.5mm. However, due to the groove for the ceiling attachment bracket, the available width is confirmed to be 18.9mm, and there is a space of 17.3mm in height. Considering the cost-effectiveness and ease of obtaining product certification, an external converter design was required, and an extruded outer case made of 0.5mm thick Polypropylene (PP) material was selected. Thus, taking into account the clearance, the width was set to 16mm and the height to 14mm including the lead of the insert components on the bottom surface.

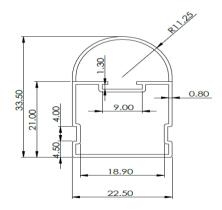


Figure 3. Structural diagram of the T5 LED fixture for the designed converter

Based on the design described above, the PCB for the Converter was created as shown in Figure 4. The material was set to FR-4 with a thickness of 1.2T and 1 oz of copper to satisfy both heat dissipation and

thickness requirements, and was designed as a double layer to mount all components on the top layer for improved productivity.

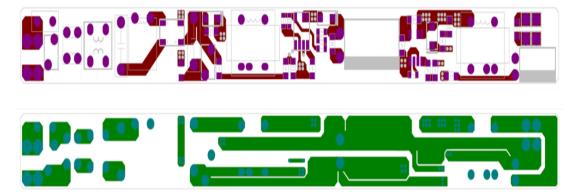


Figure 4. Top and bottom layers of the designed PCB for the converter

Considering the characteristics of T5 LED fixtures, which are connected to a single AC power source, the AC input line was designed to accommodate two sets of inserts. Since both the fixture and the converter housing are made of PP material, and the converter is mounted inside the housing, frame ground was not designed.

3. Result and Discussion

The images below showcase the top and bottom layers of the prototype converter. Within the 16 mm width of the PCB, all components were securely mounted and the spacing between patterns met all the necessary safety standards, as can be seen in Figure 5.

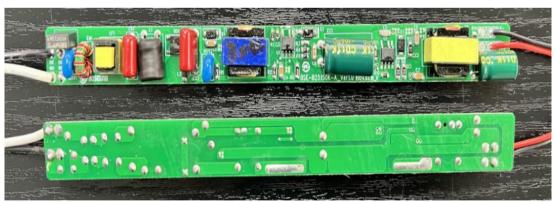


Figure 5. Top and bottom layers of the prototype converter

3.1 Voltage and Current Waveform Analysis

To verify the proper functioning of the anti-blinking circuitry targeted in this paper, we compared it with products using conventional isolated converters designed with typical flyback circuits. Notably, we tested a hot-plate known to induce blinking, which was purchased off the shelf. An oscilloscope was used to set up as depicted in Figure 6 and to measure the output current waveform with respect to Voltage sag.

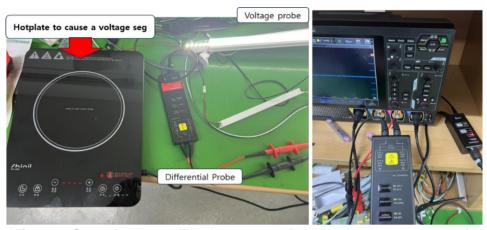


Figure 6. Setup for measuring the converter's voltage and current waveform

Figure 7 illustrates the changes in voltage/current waveforms of a typical converter before (left) and after (right) the operation of the hot plate. The yellow represents the voltage waveform, and the blue represents the current waveform. Even in a normal state, current flickering can be observed. On the right image, marked with red arrows, Voltage sag occurrence can be seen, which also leads to fluctuations in the current waveform. This results in an uneven current supply to the LEDs, thereby causing the blinking problem.



Figure 7. Changes in voltage waveform due to voltage sag

Figure 8 displays the voltage and current waveforms of the converter designed in this study. The left image shows the waveforms under typical conditions, where it can be observed that the two-stage circuit inherently does not produce flickering. The right image presents the waveforms after inducing a voltage sag. Here, distortion can be seen in the voltage waveform due to the voltage sag, yet the current waveform remains stable. Thus, it is confirmed that the designed converter effectively resolves both flickering and blinking issues.



Figure 8. Voltage and current waveform graphs of the developed converter

3.2 Electrical Characteristics Analysis

The designed converter successfully outputted the targeted constant current of 0.16 mA. However, due to voltage variations in the load LEDs, the output voltage was observed to be 106 V, lower than the targeted 114 V, consequently reducing the power consumption to 18.4 W. Despite this reduction, the converter achieved a high efficiency of 92.2% with an output power of 17 W relative to the input power. The power factor was measured at 0.98, meeting all the criteria of the KS C 7655 standard.

Figure 9 displays the harmonic content of the designed converter. The harmonic currents, expressed in mA, divided by the power consumption of 18.4 W, are detailed in Table 2. A comparison of these figures against the harmonic current standards specified in KS C IEC 61347-2-13 and KS C IEC 62384 for fixtures with a rated input power of 25 W or less confirms that the converter comfortably meets these standards.

Normal Me Store:Sto		Uover: Tover:		Scaling		LineFilt: FreqFilt:	NULL: CF:3	YOKOGAMA
			0					_Element1_
PLL Freq	U1 60.002 H	Iz Tot.	11 (A) 84.30m	hditizi		11 [A]	hd1[%]	U1 300V
Fred	60-002 H	12 110E.	84-30m 84-17m	95 852	40	0.06m	0.069	11 500mA
Urms1	220.04 V	1 3/	3.84	4 553	1	0.05m	0.059	
Long L	84.43		0.47		6	0.12	0.141	
P1	18.20 1		0.85		a a	0.05	0.063	
\$1	18.58 1		0.93		10	0.06	0.067	
01	-3.71		0-82	0.572	12	0.07	0.078	
	0-9798	1	1-04m		14	0.03m	0.038	
#1	D11.53 *		0.65m		16	0.09m	0.104	
Sector March	a strength of the second se	1	0.31		18	0.02m	0.025	
Uthd1	0.016 2		0.45		20 22	0.01m	0.013	
Pthd1	0.000 3		0.22		24	0.04m	0.043	
Pulei	0-000	2 2	0.25		26	0.00	0.068	
		27	0.71		28	8,116	0.133	
PAGE	1/2	25	0.55		38	0.134	0.150	
		31	0.25	0.146	32	0.14m	0.168	
		33	0.44m		34	0.02m	0.024	
		35	0.41m		36	0.07m	0.086	Integillerne_
		37	0-07		38		0.082	Time
		39	0-32	0.378	40		0.034	1:58:36
						EPAGE	1/2	

Figure 9. Measurement of harmonic content by order

Maximum Allowable Harmonic Current (mA)									
2nd	N/A	3rd	3.4 or less	5th	1.9 or less	7th	1 or less	9th	0.5 or less
11th	0.35 or less	13th	0.296 or less	15th	0.257 or less	17th	0.226 or less	19th	0.203 or less
21st	0.183 or less	23rd	0.167 or less	25th	0.154 or less	27th	0.143 or less	29th	0.133 or less
31st	0.124 or less	33rd	0.117 or less	35th	0.11 or less	37th	0.104 or less	39th	0.099 or less

3.3 Electromagnetic Disturbance Test

Figure 10 presents the graphs for the conducted emissions test (left) and the radiated emissions test (right). The tests were carried out inside a 10 m chamber at an accredited certification agency. Both tests were passed with a considerable margin, confirming that the product meets the standards and is clear for commercialization without any issues related to electromagnetic interference.

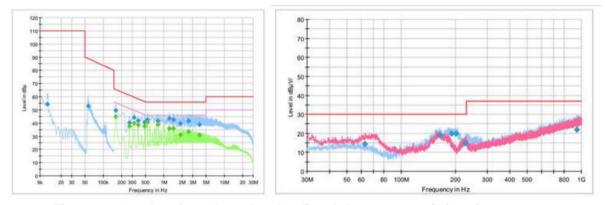


Figure 10. Graphs of conducted and radiated electromagnetic interference tests

4. Conclusion

We have presented a novel design and validation of a compact converter specifically engineered to address the pervasive issue of blinking in LED lighting, particularly suited for T5 LED indirect lighting applications. Through our development of a two-stage circuit, we have effectively countered the blinking effect that plagues many residential environments. Our approach integrates a PFC Boost circuit in the first stage, which not only adheres to the stringent power factor and harmonic standards set by KS criteria but also plays a crucial role in stabilizing voltage to prevent blinking. The second stage incorporates a DC-DC Buck converter that ensures a constant current flow, thereby mitigating the risk of flickering even during significant voltage sags.

Our extensive testing regime has demonstrated that our converter achieves an operational efficiency exceeding 92%, thereby meeting the high standards required for energy-efficient equipment certification. Moreover, the successful passing of all electromagnetic compatibility tests confirms that our converter is ready for commercialization, providing an optimal solution that enhances the quality and reliability of LED lighting systems. By applying this design across various LED fixture standards and configurations, we offer a highly effective remedy to the prevalent issue of LED light blinking, thereby enhancing user comfort and reducing energy consumption.

In conclusion, our work not only advances the technological application in lighting but also sets a new benchmark in the design of energy-efficient, stable LED lighting solutions that can significantly improve user experience and environmental impact.

Acknowledgement

This study was supported by the Research Program funded by the SeoulTech(Seoul National University of Science and Technology).

References

- [1] Kyu-Min Park, Sang-Kyoo Han, Sung-Soo Hong, Young-Gun Hong, Hyo-Bum Lee, Kwang-Il Lee and Chung-Wook Roh, "New High Efficiency LED Driver Circuit to Reduce Voltage Stress," The Transactions of the Korean Institute of Power Electronics, Vol. 14, No. 3, pp. 243-250, 2009.
- [2] Z. Ye, F. Greenfeld and Z. Liang, "Sing-Stage Offline SEPIC Converter with Power Factor Correction to Drive High Brightness LEDs," in Proceeding of the IEEE APEC'09, pp. 546-553, 2009. DOI: https://doi.org/10.1109/APEC.2009.4802711
- [3] Lena Lighting, "Why are the LEDs blinking? Flickering light effect in LED luminaires," https://lenalighting.com/company/knowledge-base/1714-why-are-the-leds-blinking-flickering-light-effect-in-ledluminaires#r1
- [4] Muhammad F., Rasheed H., Ali I., Alroobaea R. and Binmahfoudh, "A. Design and Control of Modular Multilevel Converter for Voltage Sag Mitigation," Energies 2022, 15, 1681. DOI: https://doi.org/10.3390/en15051681
- [5] Fan R., Li H., Zhang T., Wang H., Qi L. and Sun L., "Voltage Sag Causes Recognition with Fusion of Sparse Auto-Encoder and Attention Unet," Electronics 2022, 11, 3057. DOI: https://doi.org/10.3390/ electronics11193057
- [6] Jeong-il Yoon, "Causes and solutions for LED light blinking in apartments," Electric Times of Korea, https://www.electimes.com/news/articleView.html?idxno=217407
- [7] Jeong-il Yoon, "LED lighting 'blinking' costs to be borne by LED converter companies," Electric Times of Korea, https://www.electimes.com/news/articleView.html?idxno=221075
- [8] Myeong-Cheol Lee(2014), "A Study on the Assessment of Voltage Sags Considering the Protection Schemes in Transmission Systems," Master's thesis, Pukyong National University.
- [9] W. Yu, J. -S. Lai, and G. Lisi, etc., "High Efficiency DC-DC Converter with Twin-Bus for Dimmable LED Lighting," in Proceeding of the 2010 IEEE ECCE, pp. 457-462, 2010. DOI: https://doi.org/10.1109/TPEL.2011.2104368
- K. Schenk and S. Cuk, "A Single-Switch Single-Stage Active Power Factor Corrector with High Quality Input and Output," in Proceeding of the IEEE PESC'97, pp. 385-391, 1997.
 DOI: https://doi.org/10.1109/PESC.1997.616753
- [11] Byoung-Moon Yang and Woo-Jin Jang, "Development Electronic Ballast for 15mm T-5 FH Linear Lamps 28Watt," KIEE Spring Conference, pp. 99-102, Apr 2002.
- [12] Zhiyong Dong and Gyubum Joung, "Soft-Switched Synchronous Buck Converter for Battery Chargers," International Journal of Advanced Smart Convergence, Vol. 8, No. 4, pp. 138-146, 2019. DOI: http://dx.doi.org/10.7236/IJASC.2019.8.4.138