

Analysis and Optimal Design of Induction Heating Cookers

Muhammad Humza* and Byungtaek Kim[†]

Abstract – This paper deals with the optimal design of induction heating cooker starting from the analytical analysis of the cooker model. The analytical analysis method is based on the construction of the equivalent circuit of the cooker in which the circuit parameters are calculated from the geometries in a very simple way. By using the analysis method of equivalent circuit, the numerous combinations of primary winding coils for the specific rated power are obtained in fast computation time for a given secondary conductor. Through the calculation procedures, the optimal number of turns and their optimal positions can be obtained with which the cooker provides the highest efficiency at the rated power. The effectiveness and accuracy of the proposed analysis and design are verified through finite element analysis for practical and designed models.

Keywords: Analysis of induction cooker, Equivalent circuit, Optimal design of primary coil

1. Introduction

Traditional ways of cooking are hanging the pot on fire which is produced by burning of wood, coal or gas. These traditional ways have a lot of draw backs. The gas burner transfer only 35 % to 40 % heat to the pan, which means large portion of heat is wasted and these methods of cooking consumed a lot of time for cooking. The burning of gas also produces the CO₂ and other hazardous products which are unfriendly to the environment. On the other hand, induction heating cooker is friendly to environment and offers a lot of benefits such as high heating efficiency, fast, safe, clean, and cheap cooking. Induction heating is significantly energy and time saving [1]. Induction heating cooker consists of basically a heating coil (primary winding) and vessel or pot (secondary winding). The heating coil is placed underneath a conductive cooking vessel with a thermal insulator between them. A supply of medium frequency is applied to the heating coil, usually by means of power converter. It generates magnetic field which induces the current in the vessel which leads to the desired heating of vessel for the cooking purpose.

In the past, most designers have used FEM for design of the induction cooker which is accurate but much time consuming process [2-6]. This study proposes the novel method of analysis and optimal design of induction heating cooker by constricting the equivalent circuit. The equivalent circuit is constructed by using the analytical analysis from the geometries of a model. To get the circuit parameters of the secondary winding, it is divided into small elements to avoid from the skin and proximity effects. The depth of each element is selected less than the skin depth at desired

frequency. The self and mutual inductances of these elements are calculated by using the classical formulas obtained from the Biot-Savart law. The concrete equivalent circuit is constructed with the obtained parameters for the both primary and secondary windings. By solving the complicated coupled voltage circuit, a simplified equivalent circuit is obtained. The equivalent circuit provides an easy way to check the performance characteristics of induction heating cooker. This proposed designed method provides an easy and optimized way to calculate the number of turns of primary winding as well as the best positions for the insertion of coils on the primary side for the rated power. In this method the number of turns and their positions can be selected in such a way where it provides highest efficiency at the rated power.

Finally, FE analysis is performed for the previous and the optimally designed induction heating cookers to check characteristic performances. Various FEA results are compared and the effectiveness and accuracy of the proposed analysis and design method are verified.

2. Proposed Idea

Induction heating cooker heats the vessel by means of magnetic induction instead of thermal conduction from fire. The inductive heating has the capability to heat the vessel directly in a rapid way. Induction heating cooker uses input voltage V_{in} with the frequency f supplied from the drive circuit. The alternating magnetic field induces a current flow in the secondary winding (vessel) which heats the vessel. The practical induction heating cooker is shown in Fig. 1. In this study, the circuit parameters such as inductances and resistances can be calculated from the geometries. From these parameters equivalent circuit of induction heating cooker can be constructed easily to check the performance.

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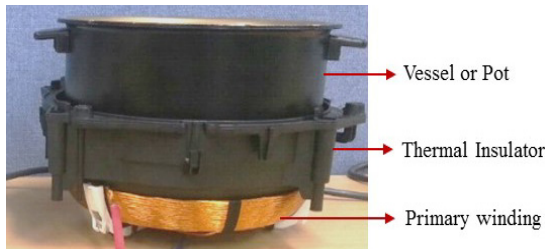


Fig. 1. Induction heating cooker model

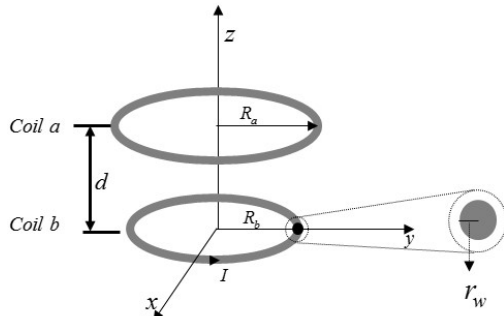


Fig. 2. Circular loop of wires

2.1 Calculation of inductances

In order to construct the equivalent circuit of induction heating cooker, it is necessary to calculate the resistances and inductances of the primary and secondary windings. For this purpose, the coaxial circular loop of wires is shown in Fig. 2.

The Biot-Savart law relates the magnetic fields to the currents which are their sources and it helps to calculate inductances for the given geometries of Fig. 2. But the Biot-Savart law’s equation requires a dense sampling to solve it which consumes a lot of time. By using the standard numerical integration techniques, the simple analytical equation for the self-inductance can be obtained which provide the same results as the results of the equation obtained from Biot-Savart law, in a very short time [7]. The analytical equation for the self-inductance for the circular loop of wire is given as in (1).

$$L_{aa} = \mu_0 R_a \left[\ln \left(\frac{8R_a}{r_w} \right) - 2 \right] \tag{1}$$

where r_w is the radius of wire, and R_a is the radius of circular loop of coil a .

Similarly, the mutual inductance of magnetically coupled circular loops of wires as shown in Fig. 2 can also be calculated by using the simplified form of equation of mutual inductance (2), where K and E are complete elliptic functions [8].

$$L_{ab} = 2\mu_0 \frac{\sqrt{a+b}}{b} \left[\left(1 - \frac{\beta^2}{2} \right) K(\beta^2) - E(\beta^2) \right] \tag{2}$$

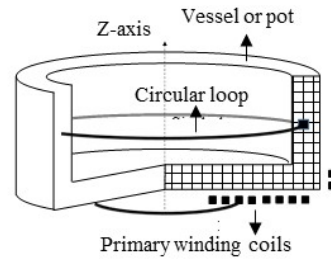


Fig. 3. Cut view of induction heating cooker

$$\text{where } a = \frac{R_a^2 + R_b^2 + d^2}{R_a R_b}, \quad b = \frac{2}{R_a R_b} \quad \text{and} \quad \beta = \sqrt{\frac{2b}{a+b}}$$

where R_a is the radius of one coil and R_b is the radius of other coil b , and d is the separation between these two coils.

2.2 Construction of an equivalent circuit

The equivalent circuit of induction heater is constructed with the resistances and inductances for the primary and secondary windings. To reduce the skin and proximity effect, primary winding coil is made of litz wire. The resistance of the primary winding can be calculated from its geometries by using common resistance law. The self and mutual inductances for the primary winding are calculated by using (1) and (2).

However, the secondary winding is the lump of solid conductor without any electric terminal, so it is impossible to use the previously established equations for the calculations of parameters. To obtain the parameters of the secondary winding, it is assumed that the secondary winding (vessel) is made of small pieces of slices which forms the circular loops of wire as shown in Fig 3. The depth of these circular loops of wires, is considered less than the skin depth to avoid the skin and proximity effects for the applied frequency. Consequently, the inductance equations (1) and (2) and the resistance law are applicable for each loop of wire to get the parameters of secondary windings.

2.2.1 Voltage Equations of induction heating cooker

For the given geometries of both primary and secondary windings, the resistance and inductance of each loop can be calculated. The voltage equation of n^{th} loop of wires is expressed in phasor form, as in (3).

$$\mathbf{V}_n = r_n \mathbf{i}_n + j\omega \sum_{k=1}^{N_{tot}} L_{nk} \mathbf{i}_k \quad (n \leq N_{tot}) \tag{3}$$

where N_{tot} is the sum of number of turns and L_{nk} is the mutual inductance between the n^{th} and k^{th} conducting loops.

For convenience, the numbers from 1 to N_p are assigned for the primary coils and the number from N_p+1 to N_{tot} are assigned for the elements of secondary winding. The

primary winding coils are connected in the series, so the currents of these circular loop of wires are same as \mathbf{I}_p . The voltage equations for the coils of primary winding is given as in (4).

$$\mathbf{V}_i = r_i \mathbf{I}_p + \left(j\omega \sum_{k=1}^{N_p} L_{ik} \right) \mathbf{I}_p + j\omega \sum_{k=N_p+1}^{N_{tot}} L_{ik} \mathbf{i}_k \quad (i \leq N_p) \quad (4)$$

The voltage applied to the primary winding is the sum of all coils voltages \mathbf{V}_i because the coils of the primary winding are connected in series, which is given by (5).

$$\begin{aligned} \mathbf{V}_p &= \sum_{i=1}^{N_p} \mathbf{v}_i \\ &= \sum_{i=1}^{N_p} \left(r_i + j\omega \sum_{k=1}^{N_p} L_{ik} \right) \mathbf{I}_p + j\omega \sum_{k=N_p+1}^{N_{tot}} \left(\sum_{i=1}^{N_p} L_{ik} \right) \mathbf{i}_k \\ &= \mathbf{Z}_{pp} \mathbf{I}_p + \mathbf{Z}_{ps} \mathbf{I}_s \end{aligned} \quad (5)$$

where the secondary winding has the circular loop of wire in parallel so the current of coils of secondary winding is $\mathbf{I}_s = [\mathbf{i}_{N_p+1} \ \mathbf{i}_{N_p+2} \ \dots \ \mathbf{i}_{N_{tot}}]^T$ and \mathbf{Z}_{ps} is the mutual impedance matrix between primary and secondary windings. \mathbf{Z}_{pp} is the impedance matrix of primary winding and it is given as in (6).

$$\mathbf{Z}_{pp} = R_p + jX_p = \sum_{i=1}^{N_p} r_i + j\omega \sum_{i=1}^{N_p} \sum_{k=1}^{N_p} L_{ik} \quad (6)$$

On the other hand, the secondary coils have the same voltage \mathbf{V}_s which is actually zero. The voltage equation for the elements of secondary winding is given as in (7).

$$\begin{aligned} \mathbf{V}_s &= v_j \quad (N_p + 1 < j \leq N_{tot}) \\ &= r_j \mathbf{i}_j + j\omega \sum_{k=1}^{N_p} L_{jk} \mathbf{I}_p + j\omega \sum_{k=N_p+1}^{N_{tot}} L_{jk} \mathbf{i}_k \end{aligned} \quad (7)$$

The combined equation in the form of impedance matrices for the elements of secondary winding is given as in (8), [9].

$$\mathbf{V}_s = \mathbf{Z}_{sp}^T \mathbf{I}_p + \mathbf{Z}_{ss} \mathbf{I}_s \quad (8)$$

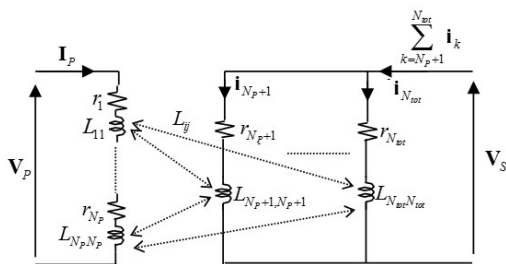


Fig. 4. Equivalent circuit of induction cooker for N_{tot} turns

where $\mathbf{V}_s = [\mathbf{v}_{N_p+1} \ \mathbf{v}_{N_p+2} \ \dots \ \mathbf{v}_{N_{tot}}]^T$.

Consequently, the equivalent circuit of induction heating cooker can be constructed with the derived equations (5) ~ (8) as shown in Fig. 4.

By combining the (5) and (8) and with $\mathbf{V}_s=0$, one can be obtained as in (9).

$$\begin{aligned} \mathbf{V}_p &= \left(-\mathbf{Z}_{ps}^T \mathbf{Z}_s^{-1} \mathbf{Z}_{ps} + \mathbf{Z}_{pp} \right) \mathbf{I}_p \\ &= \mathbf{Z}_{EQ} \mathbf{I}_p \end{aligned} \quad (9)$$

where \mathbf{Z}_{EQ} is the total impedance which can be seen from the primary side. The secondary impedance transferred to the primary side is also identified as in (10).

$$\begin{aligned} \mathbf{Z}_{EQ} &= \text{Re} \left(-\mathbf{Z}_{ps}^T \mathbf{Z}_s^{-1} \mathbf{Z}_{ps} + \mathbf{Z}_{pp} \right) + j \text{Im} \left(-\mathbf{Z}_{ps}^T \mathbf{Z}_s^{-1} \mathbf{Z}_{ps} + \mathbf{Z}_{pp} \right) \\ &= \underbrace{\text{Re} \left(-\mathbf{Z}_{ps}^T \mathbf{Z}_s^{-1} \mathbf{Z}_{ps} \right)}_{R_{EQ}} + \underbrace{\text{Re} \left(\mathbf{Z}_{pp} \right)}_{R_p} + j \underbrace{\text{Im} \left(-\mathbf{Z}_{ps}^T \mathbf{Z}_s^{-1} \mathbf{Z}_{ps} \right)}_{X_{EQ}} + j \underbrace{\text{Im} \left(\mathbf{Z}_{pp} \right)}_{X_p} \end{aligned} \quad (10)$$

where R_{EQ} and X_{EQ} are the equivalent resistance and reactance of induction cooker which can be seen from the primary side and R_s and X_s are the resistance and inductance of the secondary winding referred to the primary side.

Thus the final simplified equivalent circuit of induction heating cooker with secondary transferred to the primary side can be depicted as shown in Fig. 5.

2.2.2 Efficiency of induction heating cooker

In order to get the efficiency of the induction heating cooker, it is necessary to calculate the total input and output powers of the induction heating cooker. By using the constructed equivalent circuit, its characteristic analysis is very easy to check the performance. The input power of induction heating cooker can be obtained by (11).

$$P_{in} = \text{Re} \left(\mathbf{V}_p \mathbf{I}_p^* \right) = \text{Re} \left(\mathbf{Z}_{EQ} \mathbf{I}_p \mathbf{I}_p^* \right) \quad (11)$$

where \mathbf{V}_p is input supply voltage and \mathbf{I}_p^* is the conjugate of primary winding current \mathbf{I}_p which can be obtained from (9). The power delivered to the secondary winding can be calculated as in (12).

$$P_s = P_{in} - P_{loss} = \text{Re} \left(\mathbf{V}_p \mathbf{I}_p^* \right) - R_p I_p^2 = R_s I_p^2 \quad (12)$$

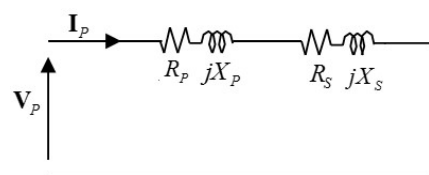


Fig. 5. Simplified equivalent circuit of cooker

The efficiency is the ratio between input and output powers of the induction heating cooker and it is given as in (13).

$$\eta = \frac{P_{out}}{P_{in}} = \frac{R_S I_P^2}{\text{Re}(\mathbf{V}_P \mathbf{I}_P^*)} = \frac{R_S I_P^2}{\text{Re}(\mathbf{Z}_{EQ} \mathbf{I}_P \mathbf{I}_P^*)} = \frac{R_S}{R_{EQ}} \quad (13)$$

All the above discussion described the simple method of analysis of induction heating cooker for the construction of equivalent circuit which is useful to find the performance of induction heating cooker in a very easy way.

3. Optimization of Primary Winding Configuration

The dimensions of the practical previous model which is to be optimized are shown in Fig. 7 and its specifications in detail are given in Table 1. In this study, with the given secondary (vessel) and the litz wire with given diameter, the primary winding is optimized to determine the best number of turns and their best positions. Due to the thermal insulator construction, maximum 22 conductors can be inserted at RA area beneath the vessel and maximally 19 conductors can be inserted at RB area to the side of the vessel as shown in Fig. 6, so maximum 41 turns in total can be inserted on the primary side for this model. An additional constraint for design is that all conductors in an area are contiguous together for manufacturing convenience.

Under these constraints, the number of total possible combinations $N_{C,tot}$ of primary winding coils for the available area is given by (14).

$$N_{C,tot} = \sum_{i=1}^{N_{a,max}} (N_{a,max} - i + 1) C_1 \times \sum_{j=1}^{N_{b,max}} (N_{b,max} - j + 1) C_1 \quad (14)$$

$$= 57684$$

where C denotes combination and $N_{a,max}$ and $N_{b,max}$ mean the maximal number of conductors in RA and RB area respectively, that is, 22 and 19. From the results of (14), it apparent that it is almost impossible to analyze all combinations with the FEM. However, with the proposed method the characteristics of induction cooker for each



Fig. 6. Primary winding and thermal insulator structure

combination can be obtained easily much faster than FE simulations.

The flow chart for the optimal designed of the primary winding coils is shown in Fig. 8. It is supposed that N_a are the number of turns inserted in area RA and N_b are the number of turns inserted at RB area of the primary side of cooker. This flow chart shows that at the start secondary winding parameters such as resistance and inductances can be calculated from the given geometries. The minimum number of turns ($N_a=2$, $N_b=2$) and their positions can be selected on RA and RB areas of the primary side. With the specific geometry of primary winding coils the secondary winding power and efficiency can be calculated. In this way the different number of combinations of primary winding can be obtained. The power and efficiency for each combination can be calculated. From those calculations the combination which has highest efficiency at rated power is selected as an optimized model which provide the number turns and their positions. The rated power for the design was selected as 1300 W. In this paper the power for optimization is selected as in a very narrow range of $P_{out} \leq P_{rated} \pm 2.5\%$ of P_{rated} . Among the combinations satisfying this range of power, the combination with the highest efficiency is selected. This is the simple analytical method which can be performed by using matlab program for simulation.

Fig. 9 and 10 show the power and efficiency graphs

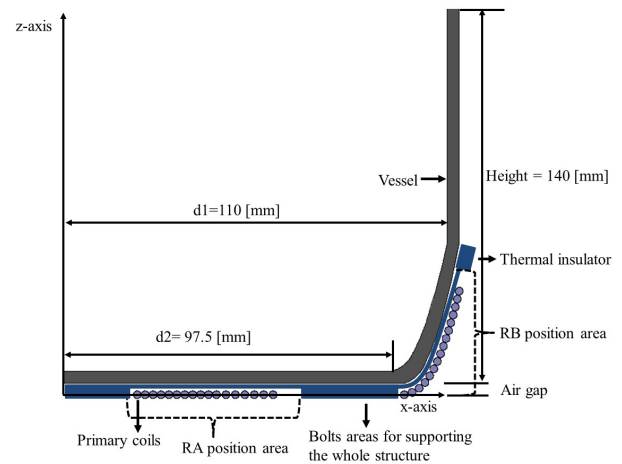


Fig. 7. Induction heating cooker with dimensions

Table 1. Specifications of analysis model

Input supply Voltage V_{in}		177 V_{rms}
Input supply frequency f		20 kHz
Gap between primary and secondary		3.2 mm
Primary winding	Material	Copper
	Diameter of litz wire	2.2 mm
	No. of strands	23
	N_a	17 turns
	N_b	16 turns
Secondary vessel	Material	Aluminum
	Thickness of vessel	3.5 mm

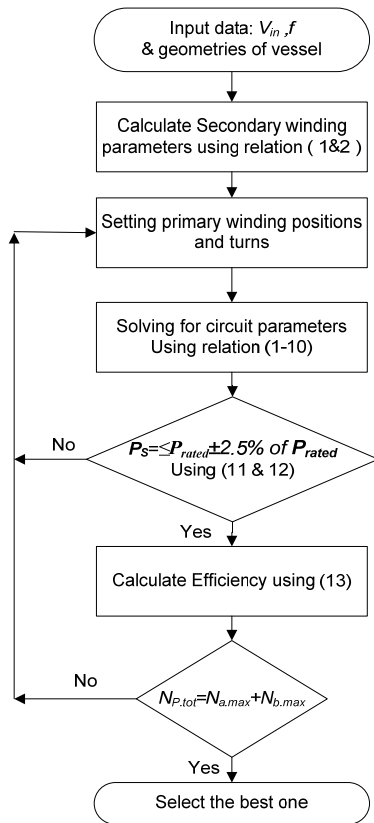


Fig. 8. Flow chart for optimization

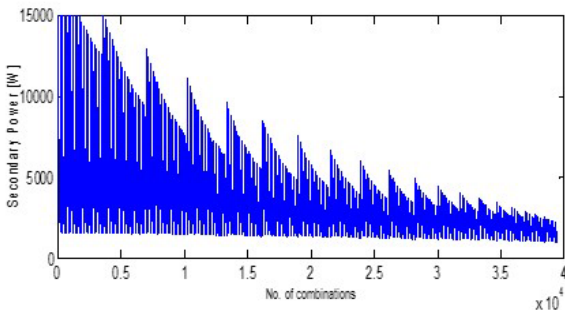


Fig. 9. Power vs no. of combinations

along the number of combinations of primary winding coils. The number of combinations is shown in these figures are less than the total number of combinations of (14) because the optimization is started from minimum 2 conductors ($N_a=2, N_b=2$) for both RA and RB areas. The power and efficiency is varying along different combinations but there were only 416 combinations which satisfy the selected range of rated power.

The rated power was 1300 W but the power of optimized combination for the proposed design which has highest efficiency is 1329 W. The proposed design and optimized method provide the information about the number of turns and their positions inserted at the primary side.

The total number of turns of primary winding for the optimized design is 30, where $N_a=11$ and $N_b=19$ turns are

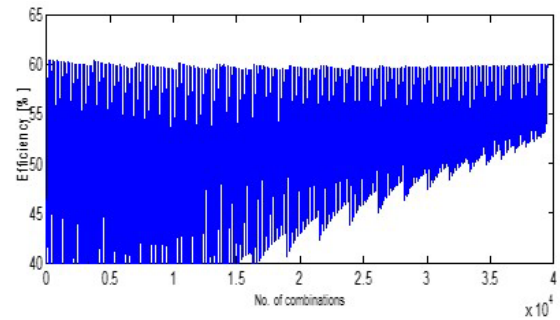
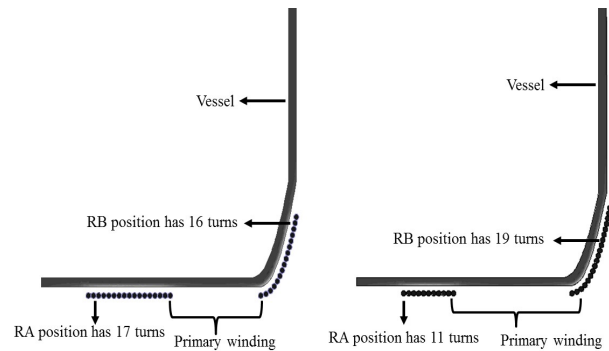


Fig. 10. Efficiency vs no. of combinations



(a) Practical model (b) Optimized model

Fig. 11. FEM models of induction heating cooker

inserted at the positions RA and RB respectively. The practical and optimized model are shown in Fig. 11.

4. Verification Through FE-analysis

The FE analysis is done with the RMS value of input supply 177 V at frequency of 20 kHz for the optimized model which has the highest efficiency at rated power. The solution type of FE analysis is 2D time-transient with cylindrical coordination. The step size for the solution is kept very small as $(1/100 \times 1/20000)$ sec. The Fig. 12 shows the flux lines, flux and current density distribution in the vessel and winding by using FE analysis. These figures clearly shows the magnetic induction current in the secondary conductor.

The input voltage, currents of primary and secondary winding are shown in Fig. 13 and 14 respectively. The input power is calculated from the waveforms of input voltage and current obtained from FE analysis as shown in Fig. 13. The RMS values of currents and voltage are also obtained from their waveforms. Apparent power is the multiplication of these RMS values of voltage and current. The input power can be extracted by taking the average of the point to point multiplication of current and voltage of the wave forms. The equivalent impedance from these waveform can be obtained as in (15)



(a) flux density and lines (b) current density

Fig. 12. Flux density and flux lines analysis

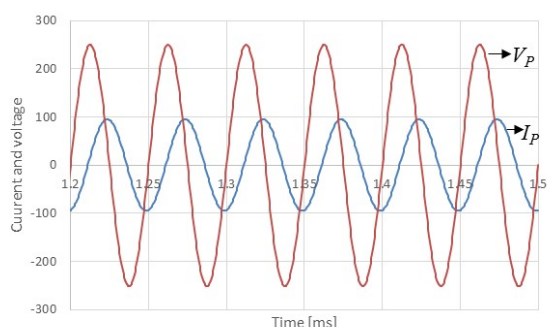


Fig. 13. Input voltage and current of primary winding

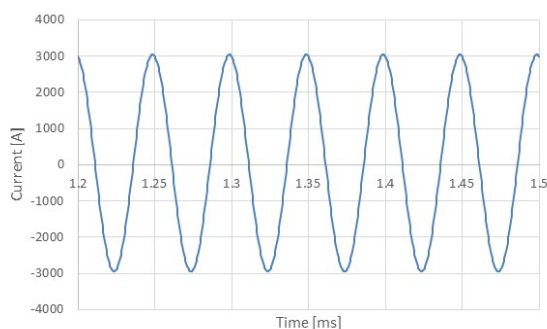


Fig. 14. Secondary winding current

$$Z_{eq} = \frac{P_{appant}}{I_{rms}^2} (\cos \theta + j \sin \theta) \quad (15)$$

where $\theta = \cos^{-1} \left(\frac{P_{real}}{P_{appr}} \right)$, θ is the impedance phase angle,

P_{real} is the real power and P_{appr} is the apparent power. The secondary power can be calculated by subtracting the primary winding copper loss from input power.

The same FE analysis and characteristic analysis were carried out for the previous model, and the comparison of the analysis results for the both previous and optimized models, are given in the Table 2 and 3 respectively. From the results, it is shown that the error between analytical and FE analysis results is less than 5% which might be caused by time step approximation and mesh qualities of FE

Table 2. Analysis results of practical model

Parameters	Analytical results	FEM Results
I_P	69.3 A	68.9 A
I_S	2103 A	2083 A
P_S	1300 W	1321 W
efficiency	66.1 %	66.3 %

Table 3. Analysis results of the optimized model

Parameters	Analytical results	FEM Results
I_P	68.6 A	67.4 A
I_S	2084 A	2067 A
P_S	1329 W	1341 W
efficiency	67.0%	67.5 %

analysis. The efficiency of optimized model is about 1% higher and number of turns are decreased to 30 from 33, which shows the validity of proposed idea.

5. Conclusion

In this paper, the novel method of analysis and optimal design of induction cooker is investigated in detail which is useful and time saving. The proposed method is started from the equivalent circuit analysis to find the electrical parameters and then the primary winding coils are designed with the simple optimized method.

Finally, FE analysis is performed for the best optimized combination of the primary winding coils. From the results it can be concluded that rated power satisfy with the analytically calculated one as well as the number of primary winding and their position of insertion is also obtained to get the maximum efficiency at rated power. The proposed method can be implemented by the designers easily for the optimal design of induction heating cooker of different ratings.

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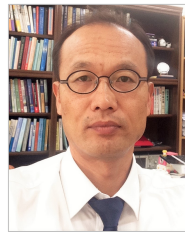
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