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# Performance of Solution Processed Zn-Sn-O Thin-film Transistors Depending on Annealing Conditions

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We have investigated zinc tin oxide (ZTO) thin films under various silicon ratios. ZTO TFTs were fabricated by solution processing with the bottom gate structure. Furthermore, annealing process was performed at different temperatures in various annealing conditions, such as air, vacuum and wet ambient. Completed fabrication of ZTO TFT, and the performance of TFT has been compared depending on the annealing conditions by measuring the transfer curve. In addition, structure in ZTO thin films has been investigated by X-ray diffraction spectroscopy (XRD) and Scanning electron microscope (SEM). It is confirmed that the electrical performance of ZTO TFTs are improved by adopting optimized annealing conditions. Optimized annealing condition has been found for obtaining high mobility.

Keywords: Oxide TFT, ZTO, Thin film transistor, N2, Vacuum, Wet, Annealing

## **1. INTRODUCTION**

Oxide semiconductor which has been studied extensively as a semiconductor material has a wide band gap and high mobility. Amorphous oxide semiconductors (AOSs) such as ZnO-based thin film transistors (TFTs) have attracted much attention for their application to integrated circuits used in backplane of active matrix displays, because of their higher electrical and optical property than that of amorphous silicon thin film transistors (a-Si TFTs) and low production cost compared with the low-temperature polycrystalline silicon (LTPS). The AOSs TFTs have shown high mobility due to their ns-orbital overlap structure. In addition, AOS thin-films have advantages of transparency in the visible range due to a large bandgap(~3.3 eV)[1-3]. Recently, there have been many reports on different active layer materials of AOSs TFTs. Among them, the material that is most extensively

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This is an open-access article distributed under the terms of the Creative Commons Attribution Non-Commercial License (http://creativecommons.org/licenses/by-nc/3.0) which permits unrestricted noncommercial use, distribution, and reproduction in any medium, provided the original work is properly cited. used currently is indium-gallium-zinc-oxide (IGZO) [4]. However, indium is an expensive and rare material, and indium-free materials, like zinc tin oxide (ZTO), have been studied intensively these days [5,6]. Because indium is not an abundant material, it is necessary to reduce indium usage for the future. Most of AOSs TFTs are annealed for the improvement of electrical performance and stability of device after deposition at room temperature (RT). Thermal annealing parameters are observed to be closely related with the annealing temperature and the ambient condition [7]. Therefore, it is very important to optimize the annealing process [8,9].

In this paper, we have fabricated zinc tin oxide (ZTO) TFTs without indium and compared them in the air and in wet and vacuum ambient with different annealing conditions. Followed by the completed fabrication of ZTO TFT, the performance of TFT has been compared depending on the annealing conditions.

#### 2. EXPERIMENTS

ZTO films were deposited by solution process on a  $SiO_2(200 \text{ nm})/\text{p-type Si(heavily doped)}$  substrate with the bottom gate



Fig. 1. Schematic diagram of ZTO TFTs structure.

structure. A 0.05 M precursor solution of ZTO was prepared by solution method, dissolving mixture of tin chloride [ZrCl<sub>4</sub>] and zinc acetate dihydrate [Zn(CH<sub>3</sub>COO)<sub>2</sub>·2H<sub>2</sub>O] in 2-methoxyethanol [C3H8O<sub>2</sub>] and H<sub>2</sub>O<sub>2</sub>. Then, it was stirred at 50 °C for 12 h to form the ZTO sol-gel precursor. The molar ratio of Zn:Sn was fixed at 3:7.

Figure 1 shows the schematic diagram of ZTO TFT structure. The ZTO film thickness was about 40 nm and patterned by using photolithography. The channel width and length are 250 µm and 50 µm, respectively. After deposition of the ZTO films, source and drain electrodes (Ti/Au=10 nm/50 nm) were fabricated by ebeam evaporation and lift off process. Then, annealing process was performed in different annealing conditions, such as air, wet and vacuum ambient. The ZTO TFT was annealed at 500 °C for 2 h. Air annealing treatment was performed in atmosphere condition, while wet annealing was performed in the ambient of heated de-ionized (DI) water. The amorphous structure of ZTO thin films was confirmed by X-ray diffraction (XRD). All electrical performance were measured by using a semiconductor parameter analyzer(EL 423).

#### 3. RESULTS AND DISCUSSION

fraction profiles for the films that are annealed at 500°C exhibited amorphous structure [10,11].

Figure 2 shows XRD patterns of ZTO thin films. The X-ray dif-



Fig. 2. X-ray diffraction (XRD) spectrum of ZTO thin-film.

Figure 3 shows the surface microstructure of the ZTO thin films analyzed by SEM. In ZTO thin films, the surface is composed of small and uniform grains.

Figure 4 shows transfer characteristic of air, wet and vacuum annealed ZTO TFTs measured at  $V_{DS}$ =5.1 V. Gate voltage ( $V_{CS}$ ) was swept from -30 V to 20 V. Transfer curve shows drain current of log plot (black line) and leakage current of linear plot (red line). We deduced threshold voltage ( $V_{th}$ ) by extrapolation method in linear plot of drain current.



Fig. 3. SEM image of ZTO thin film.



Fig. 4. Transfer characteristic of ZTO TFTs with (a) air ambient, (b) vacuum ambient, (c) wet ambient annealing condition, and (d) mobility, subthreshold swing, and threshold voltage graph.

 Table 1. The electrical performance of ZTO TFTs with different annealing condition.

Annealing condition	Threshold voltage (V <sub>th</sub> )	Subthreshold slope (V/decade)	$I_{\rm on/off}$	Mobility (cm²/Vs)
Air annealing	-6.4	1.41	$1.7 \times 10^7$	8.2
Vacuum annealing	-10.4	0.77	$1.1 \times 10^7$	9.87
Wet annealing	-10.6	0.56	$8.4  imes 10^6$	9.23

Table 1 shows the electrical performance of ZTO TFTs with different annealing condition. The field-effect mobility ( $\mu_{FE}$ ) was calculated by using the following relation: [12]

$$\mu_{FE} = \frac{Lg_m}{WC_{OX}V_{DS}} \tag{1}$$

where,  $g_m$  is the transconductance;  $C_{ox}$  is the oxide capacitance of the gate insulator; W and L are channel width and length, respectively. In the case of air annealing, the threshold voltage (V<sub>th</sub>), subthreshold slop (SS), drain current on/off ratio (I<sub>on/off</sub>), and  $\mu_{FE}$  were measured to be about -6.4 V, 1.41 V/decade,  $1.7 \times 10^7$ , and 8.2 cm<sup>2</sup>/Vs, respectively. For the wet annealing, the V<sub>th</sub>, SS, Ion/off, and the  $\mu_{FE}$  were measured to be about -10.6 V, 0.56 V/decade, 1.1  $\times 10^7$ , and 9.23 cm<sup>2</sup>/Vs, respectively. Also, in vacuum annealing, the Vth, SS, I<sub>on/off</sub>, and the  $\mu$ FE were measured to be about -10.4 V, 0.77 V/decade, 8.4  $\times 10^6$ , and 9.87 cm<sup>2</sup>/Vs, respectively. Based on these results, it is interesting to note that wet and vacuum annealing treatment can improve the electrical characteristics of the device. It is understood by the generation of free electrons by adopting wet and vacuum annealing process. In the case of wet annealing, two possible mechanisms could be proposed to explain the performance improvement. One of the mechanisms is oxygen atoms of ZTO film reacting with neighboring hydrogen to generate H<sub>2</sub>O. This mechanism can be described as follows: [13]

$$M-OH+M-OH \rightarrow VO\cdots + M-O-M+H_2O\uparrow + 2e-$$
(2)

where, Vo…an ionized oxygen vacancy, and e- denotes free electron. The second possibility is the carrier injection by the adsorption of  $H_2O$  on the a-ZTO surface.  $H_2O$  adsorption donates a partial negate charge to the channel surface with either molecules or hydroxyl forms [14]. In this case, the role of  $H_2O$  could be defined as a donor. When the devices were annealed in vacuum, the oxygen in the channel would go through desorption from the device and induce more carrier concentration as well as density of states. On the other hand, air annealing treatment reduces the defective states such as oxygen vacancy [5]. It is understood by passivation effect of oxygen atoms.

Vacuum annealing process provides large amount of free electron, due to relatively less amount of oxygen in the furnace. Consequently, carrier concentration is increased by wet and vacuum annealing process.

#### 4. CONCLUSIONS

In summary, we have fabricated the amorphous ZTO TFTs by solution process with different annealing conditions in the air, wet and vacuum ambient. XRD data shows the amorphous structure of ZTO thin films. The air annealing process reduces the sub-gap defective states, such as oxygen vacancy, and decreases the instability of the channel material. However, air annealing has suppressed the generation of free electron; hence, electrical performance was deteriorated. As a result, electrical characteristics such as field effect mobility and subthreshold swing were improved after wet and vacuum annealing treatment compared with air annealing treatment. Through this result, it is expected that wet and vacuum annealing process can improve device performance.

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