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synchronization with the reference grid is one of the most

important aspects to consider in grid-connected DFIG control.

The most widely accepted solutions to provide this

synchronization are the phase-locked loop (PLL) techniques

Adaptive Neural PLL for Grid-connected DFIG Synchronization

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Abstract

In this paper, an adaptive neural phase-locked loop (AN-PLL) based on adaptive linear neuron is proposed for grid-connected doubly fed induction generator (DFIG) synchronization. The proposed AN-PLL architecture comprises three stages, namely, the frequency of polluted and distorted grid voltages is tracked online; the grid voltages are filtered, and the voltage vector amplitude is detected; the phase angle is estimated. First, the AN-PLL architecture is implemented and applied to a real three-phase power supply. Thereafter, the performances and robustness of the new AN-PLL under voltage sag and two-phase faults are compared with those of conventional PLL. Finally, an application of the suggested AN-PLL in the grid-connected DFIG-decoupled control strategy is conducted. Experimental results prove the good performances of the new AN-PLL in grid-connected DFIG synchronization.

Key words: Adaptive linear neuron (ADALINE), Decoupled P-Q control, Doubly fed induction generator (DFIG), Phase-locked loop (PLL), Synchronization

NOMENCLATURE		s, rSubscripts indicating stator and rotor.d, aSynchronous reference frame.
V_{as} , V_{bs} , V_{cs} , V_{ar} , V_{br} , V_{cr}	Stator and rotor <i>a</i> -, <i>b</i> -, <i>c</i> -phase voltages.	T_e Sampling period.
i_{as} , i_{bs} , i_{cs} , i_{ar} , i_{br} , i_{cr}	Stator and rotor <i>a</i> -, <i>b</i> -, <i>c</i> -phase currents.	I. INTRODUCTION
U_{g} $V_{ds}, V_{qs}, V_{dr}, V_{qr}$ $I_{ds}, I_{qs}, I_{dr}, I_{qr}$ $\varphi_{ds}, \varphi_{qs}, \varphi_{dr}, \varphi_{qr}$ L_{s}, L_{r} M R_{s}, R_{r} T_{s}, T_{r} $\sigma=1-M^{2}/(L_{s}L_{r})$ T_{em} P $\omega_{s}, \omega, \omega_{r}$	Grid voltage vector amplitude. Stator and rotor $d-q$ voltages. Stator and rotor $d-q$ currents. Stator and rotor $d-q$ flux. Stator and rotor cyclic inductances. Mutual inductance. Stator and rotor resistances. Stator and rotor time constants. Leakage factor. Electromagnetic torque. Number of pole pairs. Synchronous, rotor, and slip speeds.	Doubly fed induction generator (DFIG) is highly attractive for wind energy generation, in which this generator is driven by wind turbine, and its stator is directly connected to grid. DFIG rotor winding is connected to a back-to-back power converter that provides variable-frequency rotor voltage [1]-[3]. DFIG-based variable speed wind turbines have numerous advantages [4], [5]. First, the maximal mechanical power attainable from wind can be extracted and converted to fixed-frequency electric power by adjusting DFIG speed and electromagnetic torque. Second, only a fraction of the nominal electric power passes through the power converters, thus reducing its loss and cost. Third, stator active and reactive powers can be independently controlled.
<i>P</i> , <i>Q</i>	Active and reactive powers.	stator-flux-oriented vector control [6]-[9]. The proper

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[10]. In conventional control strategies of DFIG, the amplitude, frequency, and phase angle of positive-sequence grid voltages are necessary. These quantities are mainly used for synchronization of the system output variables, power flux calculations, or transformation of state variables into rotating reference frame coordinates [11].

The most used technique for grid-connected converter synchronization is the conventional three-phase PLL based on synchronous reference frame (SRF). This technique uses Concordia's and Park's transformations to translate the three-phase voltage vector from the natural reference frame to the rotating one. Conventional PLL technique provides acceptable results under ideal utility conditions. However, this technique is inefficient in the presence of unbalanced grid voltages [10], [11]. To overcome this limitation, several PLL algorithms with different characteristics have been developed and presented in the literature recently. A PLL based on adaptive linear optimal filter technique was presented in [11]. The authors in [12] proposed a filtered-sequence PLL structure using Park transformation and moving average filters. A synchronization method derived from the standard PLL based on pq theory with a control model using standard *qPLL* structure was provided in [13]. A selective harmonic detection system based on three-phase cascaded delayed signal cancellation PLL was developed in [14]. A PLL based on variable sampling period concept, which allows the automatic adjustment of sampling frequency, was proposed in [15]. In [16], the authors proposed an adaptive SRF-PLL that rejects voltage disturbances several adaptive by using infinite-impulse-response notch filters. Another adaptive PLL structure based on adaptively adjusting the gain of frequency estimation loop was developed in [17] to minimize false frequency transients. In [18], a generalized delayed signal cancellation PLL method was proposed. In [19], a frequency-locked loop dual second-order generalized integrator based on two adaptive filters was implemented on a stationary reference frame.

PLL performances depend on the computational cost of the used algorithms and their immunity to faults. To improve the robustness of the PLL architecture without increasing the computational cost, a new adaptive neural PLL (AN-PLL) for grid-connected DFIG synchronization is developed in this study. The proposed AN-PLL architecture is based on adaptive linear neuron (ADALINE) networks, which are successfully applied to several fields, such as network frequency tracking [20], [21], current harmonic estimation [22], and induction motor parameter identification [23], [24]. The main advantages of the proposed AN-PLL are its accuracy, robustness, and adaptive structure. This AN-PLL can accurately estimate the frequency in polluted and distorted utility conditions to filter grid voltages and to reconstitute the voltage vector amplitude. The phase angle of power supply is detected using a unitary three-phase system obtained from the grid voltage positive sequences.

The remaining of this paper is divided into seven sections. Section II presents the grid-connected DFIG model and decoupled P-Q control strategy. Section III provides voltage frequency estimation using pseudo-square ADALINE in the presence of harmonic distortion. Section IV demonstrates grid voltage filtering by ADALINE and PLL for phase angle estimation. Section V elucidates the performance comparison between the proposed AN-PLL and the conventional PLL in severe utility conditions. Section VI discusses an application of the new AN-PLL in the decoupled P-Q control scheme of the grid-connected DFIG. Finally, Section VII concludes.

II. MODELING AND DECOUPLING *P*–*Q* CONTROL OF THE GRID-CONNECTED DFIG

A. Modeling of the DFIG

In d-q Park reference frame, the DFIG electric equations can be written as follows:

$$V_{ds} = R_s I_{ds} + \frac{d\varphi_{ds}}{dt} - \omega_s \varphi_{qs}, \qquad (1a)$$

$$V_{qs} = R_s I_{qs} + \frac{d\varphi_{qs}}{dt} + \omega_s \varphi_{ds}, \qquad (1b)$$

$$V_{dr} = R_r I_{dr} + \frac{d\varphi_{dr}}{dt} - \omega_r \varphi_{qr} , \qquad (1c)$$

$$V_{qr} = R_r I_{qr} + \frac{d\varphi_{qr}}{dt} + \omega_r \varphi_{dr} , \qquad (1d)$$

with

$$\varphi_{ds} = L_s I_{ds} + M I_{dr} , \qquad (2a)$$

$$\mathcal{P}_{qs} = L_s I_{qs} + M I_{qr} , \qquad (2b)$$

$$\varphi_{dr} = L_r I_{dr} + M I_{ds} , \qquad (2c)$$

$$\mathcal{P}_{qr} = L_r I_{qr} + M I_{qs} \,. \tag{2d}$$

The stator and rotor angular speeds are linked by the following relationship:

$$\omega_s = \omega_r + \omega. \tag{3}$$

The DFIG electromagnetic torque is expressed by the stator flux and the rotor currents as follows:

$$T_{em} = pM(\varphi_{qs}I_{dr} - \varphi_{ds}I_{qr})/L_s.$$
(4)

Finally, the stator and rotor active and reactive powers can be written as follows:

$$P_s = V_{ds} I_{ds} + V_{as} I_{as}, \qquad (5a)$$

$$Q_s = V_{qs} I_{ds} - V_{ds} I_{qs} , \qquad (5b)$$

$$P_r = V_{dr}I_{dr} + V_{ar}I_{ar}, \qquad (5c)$$

$$Q_r = V_{qr} I_{dr} - V_{dr} I_{qr} \,. \tag{5d}$$

B. Decoupled P–Q Control of the DFIG

To simplify Equation (4), as well as the DFIG control, a

stator flux orientation according to *d*-axis is chosen, that is, $\varphi_{ds}=\varphi_s$ and $\varphi_{qs}=0$ [2], [5]. Equation (4) then becomes

$$T_{em} = -pM\varphi_{ds}I_{qr}/L_s.$$
(6)

By choosing d-q Park reference frame related to the stator flux and by neglecting stator resistances, (1) becomes

$$V_{ds} = 0 , \qquad (7a)$$

$$V_{qs} = U_g = \omega_s \varphi_{ds}, \qquad (7b)$$

$$V_{dr} = R_r I_{dr} + \frac{d\varphi_{dr}}{dt} - \omega_r \varphi_{qr} , \qquad (7c)$$

$$V_{qr} = R_r I_{qr} + \frac{d\varphi_{qr}}{dt} + \omega_r \varphi_{dr} \,. \tag{7d}$$

By replacing φ_{dr} and φ_{qr} in (7.c) and (7.d),

$$V_{dr} = R_r I_{dr} + \sigma L_r \frac{dI_{dr}}{dt} - \omega_r \sigma L_r I_{qr}, \qquad (8a)$$

$$V_{qr} = R_r I_{qr} + \sigma L_r \frac{dI_{qr}}{dt} + \omega_r \sigma L_r I_{dr} + \omega_r \frac{M}{L_s} \varphi_{ds} .$$
(8b)

The stator active and reactive powers can be reconstructed from the rotor currents and stator flux as follows [3], [9]:

$$P_s = -V_{qs} \frac{M}{L_s} I_{qr} , \qquad (9a)$$

$$Q_s = V_{qs} \frac{\varphi_{ds}}{L_s} - V_{qs} \frac{M}{L_s} I_{dr} \,. \tag{9b}$$

Equation (6) indicates that the stator flux orientation according to d-axis permits to obtain an electromagnetic torque; therefore, the stator active power is proportional to q-axis rotor current. The stator reactive power is proportional to d-axis rotor current with a constant imposed by the grid.

The stator powers can be controlled independently, as confirmed by the DFIG model given by (8). Each rotor current can be regulated with its own controller. To perform independent control, two proportional–integral (PI) controllers are used. The block diagram of the rotor current regulation is presented in Fig. 1.

III. PSEUDO-SQUARE ADALINE FOR VOLTAGE FREQUENCY ESTIMATION

In this section, the required objective is to estimate online the fundamental frequency of a sinusoidal voltage corrupted by noise and harmonic distortions. This objective is achieved by a pseudo-square ADALINE [21].

ADALINE was introduced for frequency estimation [20]. This approach has been used to identify the voltage parameters written as

$$v(k) = \sum_{n=1}^{\infty} A_n \sin(\omega_n k T_e + \varphi_n), \qquad (10)$$

where A_n , ω_n , and φ_n are the amplitude, pulsation, and phase of n^{th} term, respectively. The recursive expression of v(k) can be deduced as follows:



Fig. 1. Block diagram of the rotor current control.

$$v(k+1) + v(k-1) = \sum_{n=1}^{\infty} A_n \sin(\omega_n k T_e + \varphi_n) \cdot 2\cos(\omega_n T_e)$$
$$= 2\cos(\omega_n T_e) \cdot v(k).$$
(11)

If the harmonics are neglected, the voltage signal v(k) can be rewritten as

$$v(k) = 2\cos(\omega_{1}T_{e}).v(k-1) - v(k-2).$$
 (12)

Fig. 2 shows that by tacking the vector $X(k)=[v(k-1) v(k-2)]^T$ as input of ADALINE, after convergence, its weight vector $W(k)=[w_1(k) w_2(k)]$ will adapt and converge toward the vector $R(k)=[2\cos(\omega_1(k)T_e) -1]$. The least mean square algorithm with the learning rate η is used for weight training [20]–[24].

ADALINE weight vector $W(k)=[w_1(k) w_2(k)]$ is recursively updated as follows:

$$W(k+1) = W(k) + \frac{\eta e(k) X^{T}(k)}{\varepsilon + X^{T}(k) X(k)}, \qquad (13)$$

where $X(k)=[v(k-1) \ v(k-2)]$ is the input vector, $e(k)=v(k)-v_{est}(k)$ is the estimation error, η is the learning rate, and ε is a small value used to avoid division by zero if $X^{T}(k)X(k)=0$.

To prove the stability of the frequency estimator (ADALINE), Lyapunov function candidate for the frequency estimator (13) is used. This function is selected as

$$V(k) = \tilde{W}^{T}(k)\tilde{W}(k), \qquad (14)$$

where W(k) is ADALINE estimation error, which is defined as

$$\tilde{W}(k) \equiv R(k) - W(k). \tag{15}$$

Lyapunov's convergence criterion must be satisfied, such that

$$V(k)\Delta V(k) < 0, \qquad (16)$$

where $\Delta V(k)$ is the change in the Lyapunov function. This change is given by

$$\Delta V(k) = V(\tilde{W}(k+1)) - V(\tilde{W}(k)) < 0.$$
(17)

By using the frequency estimator error dynamics, which can



Fig. 2. ADALINE for frequency tracking.

be derived using (15), with the update law (13), $\Delta V(k)$ can be evaluated as

$$\Delta V(k) = \left\| \tilde{W}(k) - \frac{\eta X^{T}(k) X(k) \tilde{W}(k)}{\varepsilon + X^{T}(k) X(k)} \right\|^{2} - \tilde{W}^{T}(k) \tilde{W}(k)$$
$$= \frac{\eta \left[\tilde{W}^{T}(k) X(k) \right]^{2}}{\varepsilon + X^{T}(k) X(k)} \cdot \left[-2 + \frac{\eta X^{T}(k) X(k)}{\varepsilon + X^{T}(k) X(k)} \right].$$
(18)

Assuming that $\varepsilon >0$ and $0 < \eta <2$, the bracketed term in (18) is negative. Consequently, the stability condition in (17) is satisfied, as well as the following convergence properties:

$$\|R(k) - R_0\| \le \|R(k-1) - R_0\| \le \|R(0) - R_0\|, k \ge 1,$$
 (19a)

$$\lim_{k \to \infty} \frac{X(k)W'(k)}{\sqrt{\varepsilon + X^T(k)X(k)}} = 0, \qquad (19b)$$

where R_0 is the vector obtained at the perfect convergence of the estimator. According to (19), the augmented error is monotonically non-increasing; therefore, the convergence is guaranteed, and the weight w_1 will be converged to $2\cos(\omega_1(k)T_e)$. At each iteration, the voltage signal frequency can hence be reconstructed online in the following way:

$$f_1 = \frac{1}{2\pi T_e} \cdot \arccos\left(\frac{w_1}{2}\right). \tag{20}$$

A main advantage of this method is its immunity to the voltage signal amplitude and phase variation or disturbance. A proof of weight convergence of ADALINE can be found in [20].

However, the sampling period T_e greatly influences the performances of this approach. The estimated frequency f_1 depends on the sampling period T_e . In the presence of harmonics, the function arccos in (20) is sensitive to weight variation. Fig. 3 shows the weight value w_1 according to T_e for a 50 Hz grid frequency. This figure indicates that $T_e=5$ ms represents a good choice because it corresponds to $w_1=0$, which is the center of the range [+2, -2]. Fig. 4 shows the relationship between the frequency and the weight w_1 for various T_{e} values. The maximum variation of the weight is obtained with an adequate sampling period $T_e = 1/4f_1$ for $w_1 = 0$. An example is delivered for $f_1=50$ Hz in this figure. Consequently, the sampling period of $T_e = 1/4f_1$ is chosen to maximize the dynamics and ensure the system stability. The choice of $T_e = 1/4f_1$ removes a part of the existing harmonics in the voltage. In our case, all the frequencies superior to



Fig. 3. T_e influence on the weight w_1 convergence value.



Fig. 4. Observable frequency as a function of w_1 for different values of T_e .



Fig. 5. Frequency estimator based on the pseudo-square ADALINE.

100 Hz will be eliminated for $T_e=5$ ms.

In [21], ADALINE was concluded to be inadequate in the presence of harmonics. To improve the frequency estimation performance in polluted and distorted voltages, an alternative of this approach is proposed. Pre-multiplication of v(k) with v(k-D) is conducted before performing calculations to accentuate the difference between two signals in close frequencies over one period, where D is the number of the delayed sampling periods. Thus, the new voltage signal V(k) at ADALINE input is

$$V(k) = v(k).v(k-D)$$

= $\frac{A_i^2}{2} \Big[\alpha \Big\{ 1 - \cos(2\omega_1 kT_e + 2\varphi_1) \Big\} - \beta \sin(2\omega_1 kT_e + 2\varphi_1) \Big]$
(21)

with $\alpha = \cos(\omega_1 DT_e)$ and $\beta = \sin(\omega_1 DT_e)$. If $T_e = 1/(4f_1) = 2.5$ ms and $D = d/T_e$, with *d* selected to be equal to T/4 = 5 ms (*f*=50 Hz), α becomes negligible compared with β for frequencies close to 50 Hz. Thus, the voltage signal *V*(*k*) can be written in the following form:

$$V(k) = -\frac{A_{\rm l}^2}{2} \sin(2\omega_{\rm l}kT_{\rm e} + 2\varphi_{\rm l}).$$
 (22)

By using ADALINE and following the same procedure as illustrated in Fig. 2, the polluted and distorted voltage frequencies can be estimated from the weight w_1 . The principle scheme is illustrated in Fig. 5. This frequency will be used in the grid voltage filtering process developed in the following section.

IV. ADALINE FOR GRID VOLTAGE FILTERING AND PLL FOR PHASE ANGLE DETECTION

A. ADALINE for Grid Voltage Filtering

A grid voltage filtering using ADALINE networks is presented. The idea is to conduct a suitable decomposition of the measured voltage and estimate its positive-sequence component. Thereafter, this component will be transformed in an adequate form to be learned by ADALINE.

The proposed filtering method applied to the phase *a* is developed. This methodology can be easily generalized to the phases *b* and *c*. The measured grid voltage contains a fundamental component and harmonics. In discrete form, the voltage $v_a(k)$ can be written as follows:

$$v_{a}(k) = \sum_{n=1}^{\infty} \sqrt{2} V_{an} \cos(\omega_{n} k T_{e} + \varphi_{n})$$
$$= \sqrt{2} V_{a1} \cos(\omega_{1} k T_{e} + \varphi_{1}) + \sum_{n=2}^{\infty} \sqrt{2} V_{an} \cos(\omega_{n} k T_{e} + \varphi_{n})$$
(23)

where V_{an} is the root mean square value of n^{th} term, with ω_n as its pulsation and φ_n as its phase. The filtering process of this voltage consists of extracting the positive-sequence component. After filtering, (23) becomes

$$v_a(k) = \sqrt{2}V_{a1}\cos(\omega_1 kT_e + \varphi_1). \qquad (24)$$

This equation can also be written in the following explicit form:

$$v_a(k) = \sqrt{2V_{a1}}\cos(\varphi_1).\cos(\omega_1 k T_e) -\sqrt{2}V_{a1}\sin(\varphi_1).\sin(\omega_1 k T_e).$$
⁽²⁵⁾

Thus, (25) can be expressed in vectorial notation as follows:

$$v_a(k) = W_a^T X(k), \qquad (26)$$

with

$$W_{a}^{T} = \begin{bmatrix} w_{a1} & w_{a2} \end{bmatrix} = \begin{bmatrix} \sqrt{2}V_{a1}\cos(\varphi_{1}) & -\sqrt{2}V_{a1}\sin(\varphi_{1}) \end{bmatrix},$$

$$X(k) = \begin{bmatrix} \cos(\omega_{1}kT_{e}) \\ \sin(\omega_{1}kT_{e}) \end{bmatrix}.$$

(27)

One ADALINE with two adaptive weights is able to estimate (26). X(k) represents the input vector composed of two generated sine waves with unity amplitude and frequency $f_1=\omega_1/2\pi$. W_a^T represents the weight vector. The voltage



Fig. 6. ADALINE for grid voltage filtering.

fundamental frequency f_1 is obtained using the previously developed strategy (Section III).

For the phases b and c, the obtained vectorial notations are respectively given as follows:

$$v_b(k) = W_b^T X(k), \qquad (28)$$

$$v_c(k) = W_c^T X(k), \qquad (29)$$

with

$$W_b^T = \left[w_{b1} \, w_{b2} \right] = \left[\sqrt{2} V_{b1} \cos\left(\varphi_1 - \frac{2\pi}{3}\right) - \sqrt{2} V_{b1} \sin\left(\varphi_1 - \frac{2\pi}{3}\right) \right],$$
$$W_c^T = \left[w_{c1} \, w_{c2} \right] = \left[\sqrt{2} V_{c1} \cos\left(\varphi_1 - \frac{4\pi}{3}\right) - \sqrt{2} V_{c1} \sin\left(\varphi_1 - \frac{4\pi}{3}\right) \right].$$

After filtering, sinusoidal three-phase voltages are obtained. The resulting amplitudes are equal to the fundamental amplitudes of each phase. These filtered voltages can be written in the following form:

$$v_{af}(k) = \sqrt{2}V_{a1}\cos(\omega_1 kT_e + \varphi_1), \qquad (30a)$$

$$v_{bf}(k) = \sqrt{2}V_{b1}\cos\left(\omega_{1}kT_{e} + \varphi_{1} - \frac{2\pi}{3}\right), \quad (30b)$$

$$v_{cf}(k) = \sqrt{2}V_{c1}\cos\left(\omega_{1}kT_{e} + \varphi_{1} - \frac{4\pi}{3}\right).$$
 (30c)

The filtering scheme of the grid voltages v_a , v_b , and v_c using the proposed strategy is shown in Fig. 6. These voltages will be used in the following subsection for phase angle detection.

B. PLL for Phase Angle Detection

Before detecting the phase angle of the filtered voltages using a PLL, a new unitary three-phase system is applied to (30). Each voltage is divided by its amplitude. Consequently,



Fig. 7. (a) Unitary three-phase system determination and (b) amplitude detection algorithm of the grid voltages.

a sinusoidal and balanced three-phase system with unit amplitude is obtained. This new system is independent on the grid voltage levels and is in phase. By applying this principle to (30), a sinusoidal and balanced unitary three-phase system is computed as follows:

$$v_{a-r}(k) = \sqrt{2}V_{a-r}\cos(\omega_{\rm l}kT_e + \varphi_{\rm l}), \qquad (31a)$$

$$v_{b-r}(k) = \sqrt{2}V_{b-r}\cos\left(\omega_{1}kT_{e} + \varphi_{1} - \frac{2\pi}{3}\right),$$
 (31b)

$$v_{c-r}(k) = \sqrt{2}V_{c-r}\cos\left(\omega_{l}kT_{e} + \varphi_{l} - \frac{4\pi}{3}\right).$$
 (31c)

with $\sqrt{2}V_{a-r} = \sqrt{2}V_{b-r} = \sqrt{2}V_{c-r} = \sqrt{2}V_r = 1.$

The grid voltage amplitudes are detected by using an envelope detector available in MATLAB–Simulink library. The amplitude detector algorithm aims to detect the periodic signal amplitude. This detection is based on a switch block that sends the greater value of that obtained at times kT_e and $(k-1)T_e$ to the output. In this work, the proposed envelope detector scheme is slightly extended for negative signals. The block diagram of the unitary three-phase system determination and the amplitude detection algorithm schemes are shown in Fig. 7.

Thereafter, the obtained three-phase system (31) can be applied to a PLL to determine accurately the phase angle. The principle diagram of the PLL is presented in Fig. 8. The PLL uses a fundamental property of Park's transformation [11]. If the estimated pulsation ω_{est1} used in the transformation is equal to the balanced three-phase system pulsation ω_1 , then *d*- and *q*-axis components are constant. The obtained sinusoidal three-phase system (31) is translated via



Fig. 8. PLL principle scheme for phase angle detection from the unitary three-phase system.



Fig. 9. Complete scheme principle of the proposed AN-PLL.

Concordia's transformation. The estimated angle θ_{est1} of Park's rotation is performed by integrating ω_{est1} obtained by PI controller. The pulsation ω_{est1} must be identical to $v_{abc-r}(\theta_1)$ pulsation. The amplitude of the controlled V_{d-r} determines the shift between V_r value and $\sin(\theta_1)$. The difference in the phase will be controlled by V_{d-r}^* . The PLL will be locked when the estimated angle θ_{est1} is equal to θ_1 . This condition is realized if V_{d-r}^* is maintained equal to zero. To achieve good performances by this PLL, the three-phase system must be sinusoidal and balanced. To this end, the voltages are filtered and transformed into a unitary three-phase system. PI parameters are calculated by considering the linearized model for small variations in θ_1 . As the grid voltage vector is maintained according to q-axis, its amplitude can be estimated from the detected amplitudes of a, b, and c phases in the following way (given as a negative quantity):

$$U_{g-est} = -(V_{a1} + V_{b1} + V_{c1})/\sqrt{3}.$$
 (32)

The block diagram of the suggested AN-PLL strategy is shown in Fig. 9. The blocks "Amplitude" (described in Fig. 7) divide the input voltages by their amplitudes.

V. EXPERIMENTAL VERIFICATION OF THE PROPOSED AN-PLL

To verify and compare the suggested AN-PLL

performance with the conventional PLL, an experiment is conducted. A DS1104 dSPACE board based on TMS320F240 floating point DSP is used. The proposed AN-PLL and the conventional PLL algorithms are implemented under MATLAB–Simulink with a sampling period of 0.1 ms. In this section, the DFIG wind turbine is not operated. The used conventional PLL is based on SRF [11], as described in Fig. 8. The measured grid voltages are directly applied to this conventional PLL; therefore, no adaptive filter interfacing scheme is inserted at its input.

An experimental comparative study between the AN-PLL and the conventional PLL is presented. The estimated quantities, such as voltage vector amplitude, frequency, and phase angle, are considered as comparison quantities. The measured grid voltages, filtered voltages, and unitary three-phase system are shown to analyze the AN-PLL operation. The learning rates of the used ADALINE networks in the AN-PLL, ensuring an optimal speed of weight convergence, are experimentally adjusted. For the pseudo-square ADALINE, the learning rate is set to 0.04; for the adaptive filtering, the learning rate is set to 0.01. PI parameters in the PLL are calculated to obtain a response time $t_r=10$ ms, a damping coefficient z=1, and a natural pulsation ω_n =4.5 rad/s. Two disturbances, namely, voltage sag and two-phase fault, are considered. In the experiment, a performance analysis of the AN-PLL in normal operating conditions (without faults) is first conducted. A comparative study between the AN-PLL and the conventional PLL under severe conditions is then performed. In this study, the grid voltage frequency is unaffected. Only variations in the grid voltage amplitudes are created.

Fig. 10 shows AN-PLL performances under normal operating conditions. The filtered grid voltages [Fig. 10(b)] are in phase with the real measured grid voltages [Fig. 10(a)]. By dividing each filtered voltage by its detected amplitude, a sinusoidal and balanced three-phase system is obtained with unit amplitude and in phase with the grid voltages [Fig. 10(c)]. Thereafter, the detected maximal amplitudes are exploited to estimate the voltage vector amplitude U_{g-est} . As shown in Fig. 10d, the amplitude U_{g-est} is estimated with high precision and low oscillations caused by frequency fluctuation. This frequency fluctuation, in the grid voltage, affects the calculation in Park's transformation. Hence, small fluctuations are obtained in the calculated d- and q-axis components of the grid voltage. The pseudo-square ADALINE conceived to estimate the utility frequency in distortion voltages shows good performances [Fig. 10(e)]. The minor oscillations observed in the estimated frequency are due to the utility frequency fluctuation (± 0.2 Hz), which is acceptable because the tolerance interval in this condition is ±0.5 Hz. The obtained unitary three-phase system, which is given in Fig. 10c, is used to detect the phase angle by a PLL. Fig. 10(f) reveals that the phase angle is well estimated. This



Fig. 10. AN-PLL performances in normal operating conditions. (a) Grid voltages. (b) Filtered grid voltages. (c) Unitary three-phase system. (d) Voltage vector amplitude. (e) Frequency.(f) Phase angle.

result proves the good performance of the proposed AN-PLL strategy under normal operating conditions.

Fig. 11 shows the performance comparison between the conventional PLL and the AN-PLL under 50% voltage sag. At t=0.63 s, a voltage sag appears [Fig. 11(a)], which is generated by multiplying the voltage sensor gains of the phases a, b, and c by 0.5 at time 0.63 s. ADALINE-based filter follows perfectly the grid voltages change in steady state. Only 0.1 s is needed to estimate accurately the positive-sequence components after the disturbance appearance [Fig. 11(b)]. The unitary three-phase system has also been reconstructed in 0.1 s after disturbance starting [Fig. 11(c)]. The voltage vector amplitude is well estimated with the AN-PLL compared with the conventional PLL [Fig. 11(d)]. However, a transient state is observed in the amplitude estimation in AN-PLL. This transient state is due to ADALINE weight adaptation and learning process following the grid voltage change. Fig. 11(d) shows that voltage sag appears rapidly in the conventional PLL compared with the AN-PLL. However, the main advantage of the proposed AN-PLL is clearly shown in the steady state operation, in which the AN-PLL estimates better the grid voltage parameters than the conventional PLL does. To improve the transient state of the proposed AN-PLL, the sampling period T_e used in the implementation must be reduced because according to (13), ADALINE weight learning is performed by iterations. Therefore, using lower sampling period leads to fast learning, and the transient state



Fig. 11. Comparison between the conventional PLL and the AN-PLL under 50% voltage sag in grid voltages. (a) Grid voltages. (b) Filtered grid voltages. (c) Unitary three-phase system. (d) Voltage vector amplitude. (e) Frequency. (f) Phase angle.



Fig. 12. Comparison between the conventional PLL and the AN-PLL under a two-phase fault: (a) grid voltages, (b) filtered grid voltages, (c) unitary three-phase system, (d) voltage vector amplitude, (e) frequency, and (f) phase angle.

will be reduced. Nevertheless, the estimated utility frequency from the conventional PLL has considerable fluctuation compared with the signal obtained from the AN-PLL [Fig. 11(e)], which remains insensitive to the disturbance. As shown in Fig. 11(f), the estimated phase angle from the conventional PLL is distorted because the angle is directly calculated from the estimated frequency.

Fig. 12 shows the performances under two-phase faults. At t=0.925 s, a voltage sag of 50% appears in the phases a and b [Fig. 12(a)]. This voltage sag is also created by multiplying the voltage sensor gains of the phases a and b by 0.5 at time 0.925 s. The filtered grid voltages and the unitary three-phase system are accurately estimated as shown in Figs. 12b and 12c, respectively. This result confirms the ability of the proposed method in extracting a sinusoidal and balanced unitary three-phase system from unbalanced and distorted three-phase voltages. In Fig. 12(d), the estimated voltage vector amplitude from the conventional PLL presents a considerable distortion, especially during fault conditions, compared with the signal provided by the AN-PLL. Moreover, the estimated utility frequency and the phase angle performed by the AN-PLL are insensitive to the fault, as shown in Figs. 12(e) and 12(f), respectively.

VI. EXPERIMENTAL VALIDATION OF THE AN-PLL IN THE GRID-CONNECTED DFIG DECOUPLED P-Q CONTROL

To experimentally validate the decoupled P-Q control of the DFIG including the proposed AN-PLL, a test bench, which is realized in our laboratory, is used. DFIG control is conducted through a PWM converter connected to its rotor and feed from a battery system. The decoupled P-Q control of the DFIG is implemented in the same dSPACE board used in the previous experiment. The control algorithm is implemented under MATLAB–Simulink with a sampling period of 0.25 ms and a *Runge–Kutta* (ode4) method resolution. The principle scheme of the experimental set up is shown in Fig 13. The parameters and characteristics of the controlled DFIG are presented in Appendix. In this section, the proposed AN-PLL associated with the DFIG control strategy is validated only in normal operating conditions (no frequency variations and no grid voltage disturbances).

Two tests are conducted. The first test is performed at negative stator active power ($P_s < 0$), and the second test is performed at negative stator reactive power ($Q_s < 0$). These two tests are preceded by a test at zero stator powers ($P_s=0$, $Q_s=0$). During these tests, the DFIG is driven by a DC motor operating in open loop control. Hence, the rotor speed is likely changed when the DFIG provides stator power to the grid.

A. Test at $P_s=0$ and $Q_s=0$

Figs. 14 and 15 illustrate the obtained experimental results at zero stator active and reactive powers of the controlled DFIG, respectively.



Fig. 13. Implementation scheme of the grid-connected DFIG decoupled P-Q control strategy using the proposed AN-PLL.

The AN-PLL performances are shown in Fig. 14. The AN-PLL estimates accurately the pulsation [Fig. 14(a)], amplitude [Fig. 14(b)], and phase angle [Fig. 14(c)] of the grid voltage vector. These variables are used directly in the decoupled P-Q control of the DFIG. The stator phase voltage of the DFIG is set to 110 V, which is the half of the rated voltage.

The obtained results from the decoupled P–Q control of the DFIG at zero stator active and reactive powers are illustrated in Fig. 15. Ps and Qs are regulated to zero, as shown in Fig. 15(a). In this test, the DFIG is driven at 1530 rpm [Fig. 15(b)]. To obtain a zero stator power, the controllers apply a rotor voltage [Fig. 15(c)]. Hence, the quadratic rotor current (Iqr) is set to zero (therefore, Ps=0), and the direct rotor current (Idr) is maintained at a sufficient value for canceling the stator flux [Fig. 15(d)]. As a result, the stator currents become null [Fig. 15(e)].

B. Test at Ps < 0 and Qs = 0

This test consists of applying a negative step of the active

power reference (Ps-ref<0) to the DFIG stator to provide an active power to the grid. Fig. 16a shows that Ps follows perfectly the reference, whereas Qs is set to zero. The rotor speed decreased from 1530 rpm to 1430 rpm because of the resisting torque applied by the DFIG to the DC motor (Fig. 16b). Fig. 16c shows the rotor voltages calculated by the controllers to maintain Qs=0 and Ps=-1 kW. Fig. 16d illustrates that the sudden change in the quadratic rotor current (Iqr) does not affect the direct rotor current (Idr), which demonstrates that d–q-axis control is perfectly decoupled. The stator voltage and current of the phase a are shown in Fig. 16e. The current and voltage are in the phase opposition, which corresponds to Ps<0 and Qs=0.

C. Test at $Q_s < 0$ and $P_s = 0$

This test is performed for a negative step in the reactive power reference $Q_s < 0$ and $P_s = 0$. Fig. 17a shows that the stator reactive power follows perfectly the reference $(Q_{s-ref} = -500 \text{ VAR})$, whereas the stator active power is set to zero. The rotor speed of the DFIG decreases slightly when



Fig. 14. AN-PLL performances: (a) stator voltage pulsation, (b) grid voltage vector amplitude, and (c) phase angle.

 $Q_s<0$ (Fig. 17b). Fig. 17c gives the rotor voltages calculated by the controllers to obtain $P_s=0$ and $Q_s=-500$ VAR. The rotor currents I_{dr} and I_{qr} are illustrated in Fig. 17d, where a perfect decoupling can be observed in the current control. The current I_{qr} is set to zero, whereas the current I_{dr} increases to -11 A at $Q_s=-500$ VAR. The stator voltage and current of the phase *a* are given in Fig. 17e. This figure reveals that the current and voltage are in quadratic, which corresponds to $P_s=0$ and $Q_s<0$.

In the illustrated results, lower ripples of the currents and powers are observed. The signal quality can be achieved by increasing the PWM frequency, decreasing the sampling period T_e , and improving the accuracy of different sensors (current, voltage, speed sensors, etc.).

VII. CONCLUSIONS

In this paper, an AN-PLL based on ADALINE for grid-connected DFIG control is proposed for the voltage vector amplitude, frequency, and phase angle estimation as an alternative to conventional PLL. The AN-PLL architecture is implemented using the dSPACE board and applied to a real power supply to prove its efficiency. A comparative experimental study between the proposed AN-PLL and the conventional PLL is also performed. According to the obtained experimental results, the proposed AN-PLL improves the robustness and steady state estimation performances. Hence, AN-PLL is a suitable synchronization system under severe utility conditions. Finally, experimental validation of the DFIG



Fig. 15. Decoupling P-Q control of the DFIG at $P_s=0$ and $Q_s=0$: (a) stator active and reactive powers, (b) rotor speed, (c) rotor voltages, (d) rotor currents, and (e) stator voltage and current of the phase *a*.

decoupled P-Q control using the new AN-PLL is conducted. The experimental results demonstrate the good performances of the AN-PLL included in the decoupled P-Q control scheme of the DFIG. As future work, other experiments can be accomplished to test the proposed AN-PLL in DFIG control connected to disturbed grid voltages.



Fig. 16. Decoupling P-Q control of the DFIG at $P_s<0$ and $Q_s=0$: (a) stator active and reactive powers, (b) rotor speed, (c) rotor voltages, (d) rotor currents, and (e) v_{as} and i_{as} at $P_s<0$ and $Q_s=0$.

APPENDIX

Parameters and characteristics of the controlled DFIG.

Rated power P_n (kW)	3
Rated stator voltage Δ /Y U_{sn} (V)	220/380
Rated stator current $\Delta/Y I_{sn}$ (A)	11/6.3
Rated rotor voltage Y U_{rn} (V)	180
Rated rotor current I_{rn} (A)	11
Rated speed N_n (rpm)	1415



Fig. 17. Decoupling P-Q control of the DFIG at $P_s=0$ and $Q_s<0$: (a) stator active and reactive powers, (b) rotor speed, (c) rotor voltages, (d) rotor currents, and (e) v_{as} and i_{as} at $P_s=0$ and $Q_s<0$.

Number of pole pairs <i>p</i>	2
Stator resistance $R_s(\Omega)$	1.5
Stator cyclic inductance L_s (mH)	220
Mutual inductance M (mH)	109.8
Rotor resistance $R_r(\Omega)$	0.6
Rotor cyclic inductance L_r (mH)	59.4

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