

Blistering Induced Degradation of Thermal Stability Al_2O_3 Passivation Layer in Crystal Si Solar Cells

Meng Li, Hong-Sik Shin, Kwang-Seok Jeong, Sung-Kwen Oh, Horyeong Lee, Kyumin Han, Ga-Won Lee, and Hi-Deok Lee*

Abstract—Different kinds of post-deposition annealing (PDA) by a rapid thermal process (RTP) are used to enhance the field-effect passivation of Al_2O_3 film in crystal Si solar cells. To characterize the effects of PDA on Al_2O_3 and the interface, metal-insulator semiconductor (MIS) devices were fabricated. The effects of PDA were characterized as functions of RTP temperature from 400~700 °C and RTP time from 30~120 s. A high temperature PDA can retard the passivation of thin Al_2O_3 film in c-Si solar cells. PDA by RTP at 400 °C results in better passivation than a PDA at 400 °C in forming gas (H_2 4% in N_2) for 30 minutes. A high thermal budget causes blistering on Al_2O_3 film, which degrades its thermal stability and effective lifetime. It is related to the film structure, deposition temperature, thickness of the film, and annealing temperature. RTP shows the possibility of being applied to the PDA of Al_2O_3 film. Optimal PDA conditions should be studied for specific Al_2O_3 films, considering blistering.

Index Terms—Solar cell, field-effect passivation, Al_2O_3 , post-deposition annealing, negative fixed oxide charge, thermal stability, blistering

I. INTRODUCTION

Currently, the solar cell market is dominated by crystalline Si solar cells. Carrier losses induced by Shockley-Read-Hall (SRH) recombination retard the efficiency of energy conversion. Surface passivation of the carriers is an important issue for high efficiency crystalline Si solar cell applications. Al_2O_3 film grown by atomic layer deposition (ALD) has demonstrated excellent uniformity and growth control, suggesting better passivation quality [1]. Al_2O_3 synthesized by ALD provides a high quality surface because its low interface state density provides good chemical passivation and the negative fixed charges near the interface for p-type c-Si provide excellent field-effect passivation [2-6]. Al_2O_3 film also can offer excellent dielectric properties [7]. Several studies show that both chemical and field-effect passivation is enhanced by post-deposition annealing in forming gas (H_2 in N_2) at a moderate temperature for about 15~30 minutes after the deposition of Al_2O_3 film [8-10].

Obviously, this high thermal budget forming gas annealing (FGA) is neither cost- nor energy-efficient. On the other hand, rapid thermal processing (RTP) is another widely used annealing method with such advantages as a short cycle time and low thermal budget. Dong Lei *et al.* studied the relationship between only the RTP temperature and the passivation of Al_2O_3 and found the SiO_x interlayer became thicker after the RTP [5]. However, few works address RTP annealing to passivate Al_2O_3 in c-Si solar cell applications [5, 8].

In this work, we studied the relationship of the field-effect passivation and the effective carrier lifetime as a

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function of the RTP time and temperature. A common FGA at 400 °C for 30 minutes was also used for comparison to ascertain whether the RTP can provide good enough field-effect passivation for Al₂O₃ on c-Si solar cells.

II. EXPERIMENTAL

To examine the electrical properties of the Al₂O₃ film, metal-insulator semiconductor (MIS) devices were fabricated. The process flow is summarized in Fig. 1. p-type silicon wafers were used. After a standard Radio Corporation of America (RCA) cleaning, a 20 nm Al₂O₃ thin film was deposited by thermal ALD at 250 °C, with trimethylaluminium (Al(CH₃)₃) and water (H₂O) precursors. Then samples were annealed in an RTP system with a vacuum of 1.5x10⁻⁵ Torr as a function of temperature at 400, 500, 600 and 700 °C for 30 seconds and as a function of time at 30, 60, 90, and 120 seconds at 400 °C. A reference sample and another sample annealed in a tube furnace at 400 °C for 30 minutes in forming gas ambient (H₂ 4% in N₂) were also prepared for comparison. After a 100 nm thick layer of Ti was sputtered as the top metal by an RF sputter system, the samples were patterned by photolithography. Finally, 100 nm thick Al was sputtered on the backside of the samples to reduce resistance. The capacitance-voltage (C-V) measurements were performed by an HP 4284A LCR meter. Effective carrier lifetime was measured by the Quasi Steady State PhotoConductance (QSSPC) method on samples cut in 5 × 5 cm squares. Morphological stability was observed by field-emission scanning electron microscopy (FESEM). X-ray photoelectron spectroscopy (XPS) and secondary ion mass

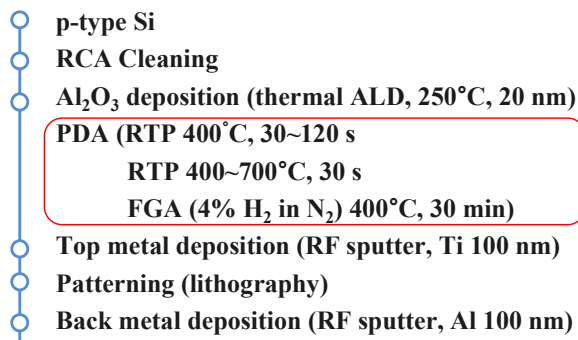
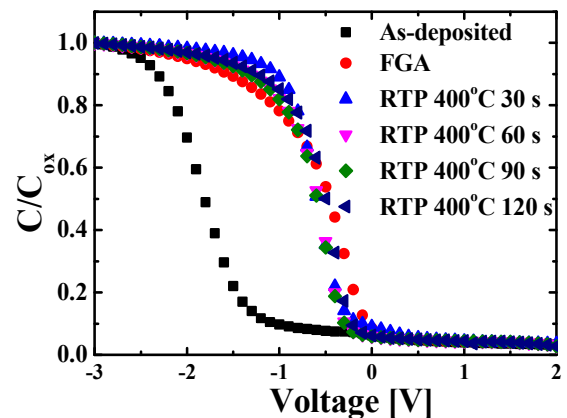


Fig. 1. Process flow for the MIS devices.

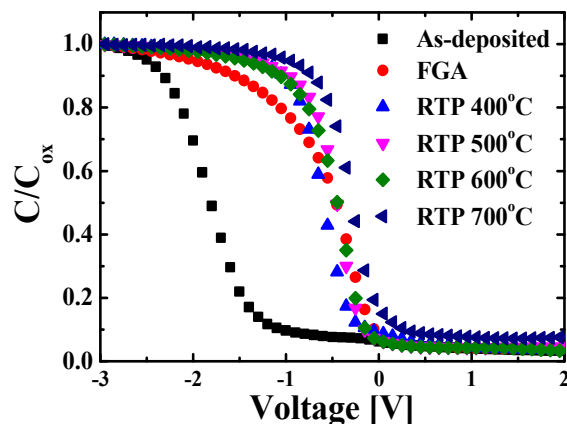
spectroscopy (SIMS) were used to examine the change in the binding energy, the atomic ratio of Al to O, and the depth profile of elements before and after annealing, respectively.

III. RESULTS AND DISCUSSIONS

The results of C-V measurements at a frequency of 1 MHz are shown in Fig. 2. After the PDA, the curves shift to the positive direction, which means that the negative fixed charges ($-Q_f$) increased. The Q_f are related to the field-effect passivation of the solar cell. The minority charge carriers are repelled from the interface by the Q_f formed electric field. Thus, fewer minority charge carriers will be lost. The fixed charge density (N_f) was calculated by $N_f = -\Delta V_{FB} \times C_{ox}$, ΔV_{FB} is the difference between the experimental flatband voltage (V_{FB}) and the



(a)



(b)

Fig. 2. The normalized C-V curves obtained at 1MHz (a) PDA at RTP 400 °C from 30 seconds to 120 seconds, (b) PDA by RTP from 400 °C to 700 °C for 30 seconds.

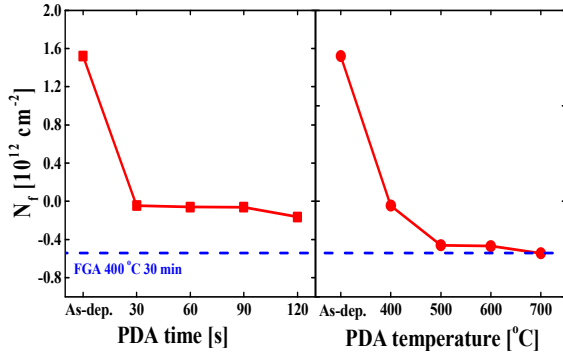


Fig. 3. Fixed oxide charge density (a) annealed at a 400 °C RTP, (b) annealed at 400~700 °C RTP for 30 s.

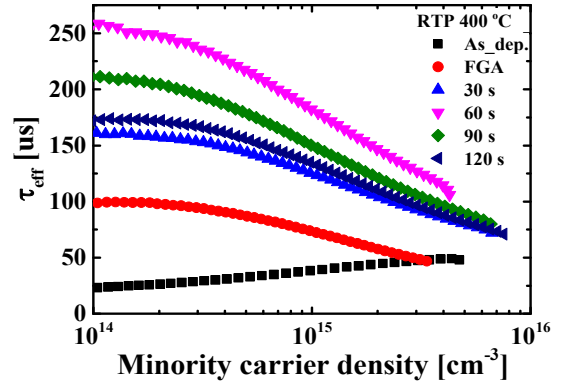
ideal V_{FB} of -0.53 V, which was calculated in our previous work by fitting the V_{FB} vs. the thickness of the Al_2O_3 . C_{ox} is the maximum capacitance from the C-V curve.

In Fig. 3, the as-deposited sample shows that the sign of the positive fixed charges and the fixed charges turn to negative fixed charges after the PDA. The negative fixed charges do not significantly increase with longer process times from 30 seconds to 120 seconds at 400 °C. The better interface states help improve the slopes of the C-V curves annealed by RTP as opposed to FGA. When the temperature increases from 400 to 700 °C for 30 seconds, the sample has nearly the same negative fixed charges as the sample treated by FGA in Fig. 3(b). With a relatively high temperature anneal, an interlayer of SiO_x can be formed [5]. Tetrahedrally coordinated Si in SiO_x contributes to tetrahedrally coordinated Al at the interface, causing the negatively charged Al vacancies close to the interface [4-6, 8, 11, 12].

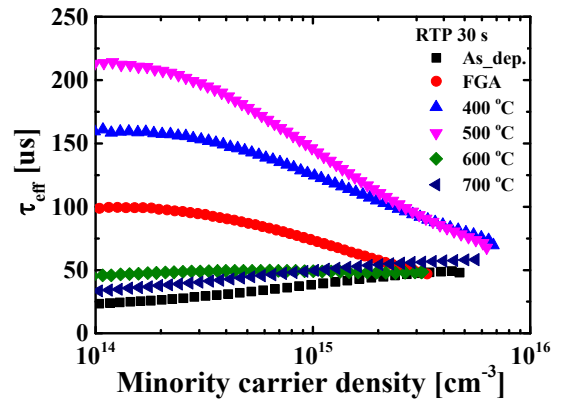
Effective lifetime (τ_{eff}) measured by QSSPC is shown in Fig. 4. In the QSSPC measurement, a conventional xenon photoflash generates excess carriers in the sample. A fast or slowly diminishing flash is used to measure the photoconductivity. The derived excess carrier density (Δn) and generation rate (G) vs. time give the τ_{eff} in Eq. (1).

$$\tau_{eff}(\Delta n) = \frac{\Delta n(t)}{G(t) - \frac{d\Delta n(t)}{dt}} \quad (1)$$

The sample annealed with an RTP at 400 °C for 60 s has the longest τ_{eff} , and 90 s and 120 s annealed samples show degradation in Fig. 4(a). With the increase of



(a)



(b)

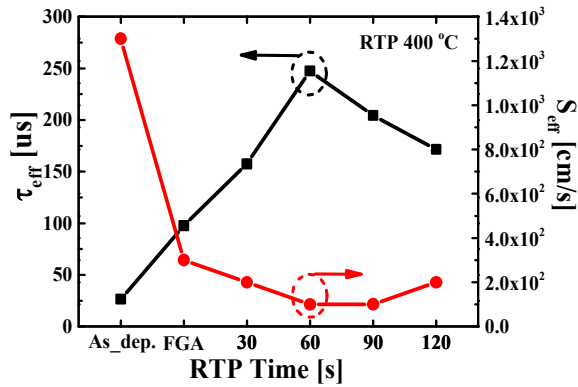
Fig. 4. Effective lifetime measured by QSSPC method (a) annealed at the RTP 400 °C, (b) annealed by the RTP 400~700 °C for 30 s.

temperature, a 500 °C RTP shows the best lifetime and above 500 °C, rapid degradation occurs in Fig. 4(b). However, even a 500 °C annealed sample shows no better τ_{eff} than a 60 s annealed sample at RTP 400 °C.

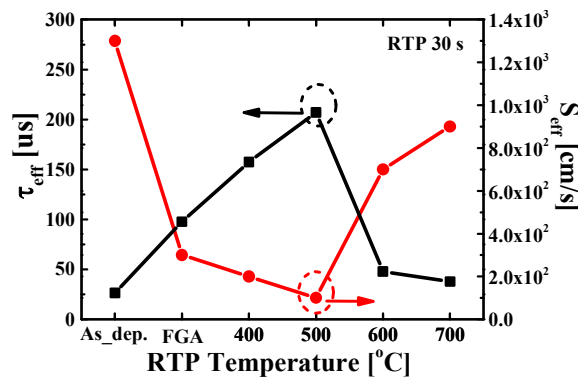
At an injection level of $2 \times 10^{14} \text{ cm}^{-3}$, τ_{eff} and surface recombination velocity (S_{eff}) were extracted (shown in Fig. 5). S_{eff} was extracted from Eq. (2),

$$\frac{1}{\tau_{eff}} = \frac{1}{\tau_{bulk}} + \frac{2S_{eff}}{W}, \quad (2)$$

where W is the effective wafer thickness (carrier profile thickness) and τ_{bulk} is the bulk minority carrier lifetime that is assumed to be infinite [15, 16]. In Fig. 5(a), the increased RTP time at 400 °C, 60 s, shows the highest τ_{eff} and lowest S_{eff} . In Fig. 5(b), the RTP temperature of 500 °C shows the highest τ_{eff} and lowest S_{eff} . However, it is not as good as the 60 s RTP at 400 °C. The degradation at high temperature may be explained by the formation of



(a)

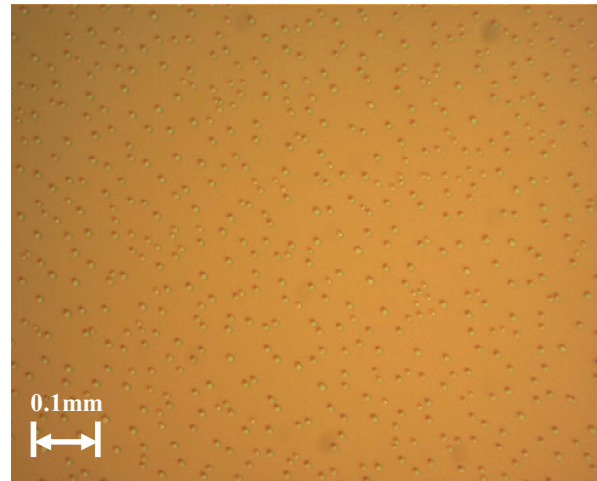


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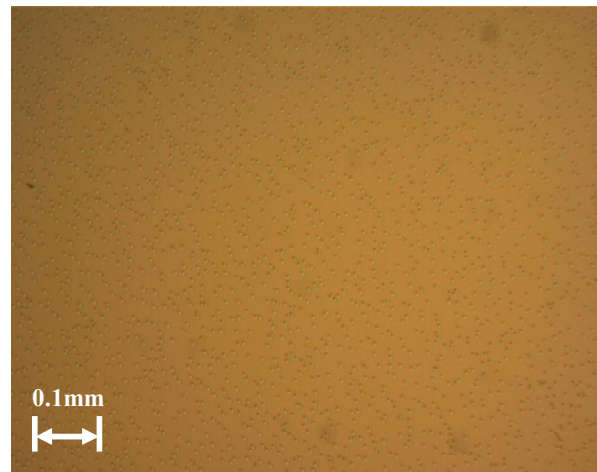
Fig. 5. Effective lifetime and surface recombination velocity at $2 \times 10^{14} \text{ cm}^{-3}$ (a) annealed at RTP 400°C , (b) annealed at RTP $400\sim 700^\circ\text{C}$ for 30 s.

silicate under high temperature annealing [5]. Nevertheless, annealing Al_2O_3 at a high temperature may be risky because it may induce blistering. D. Schuldis *et al.* reported in detail the blistering mechanism of Al_2O_3 , which is related to the film structure, deposition temperature, thickness of the film, and the annealing temperature and time. It is thought that the diffusion barrier properties of Al_2O_3 and the outdiffusion of H-containing species during annealing are the reasons for the happening of blister [13], which degrade τ_{eff} and thermal stability. Although the sample treated by FGA has more negative fixed charges, poor interface states are shown and blistering may initiate this. Fig. 6 shows the blisters observed by optical microscopy. A lower process temperature (Fig. 6(a)) causes larger but fewer blisters and a higher process temperature (Fig. 6(b)) results in smaller but more blisters.

Fig. 7 shows an overview and side view of blisters observed after RTP at 600°C (Fig. 7(a)) and 700°C (Fig.



(a)

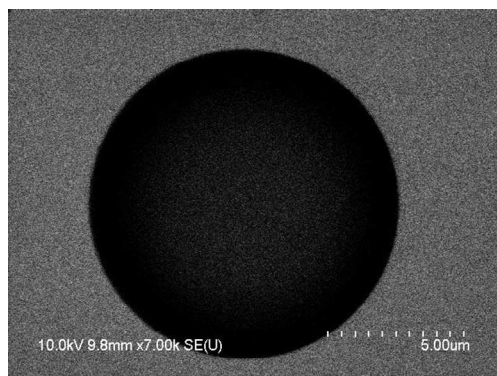


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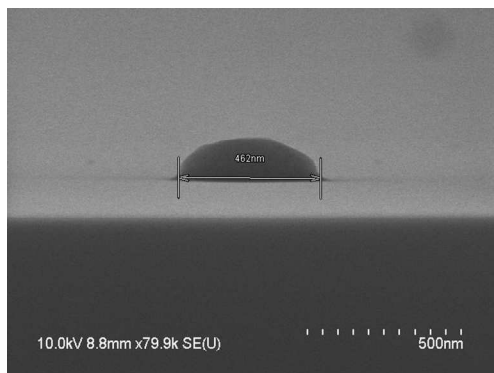
Fig. 6. Blisters observed by optical microscopy (a) PDA by RTP 600°C for 30 s, (b) PDA by RTP 700°C for 30 s.

7(b)). The blister looks like a hillock, which degrades the thermal stability in the firing process. Thus the annealing time should be chosen carefully. Increasing it to over 400°C is probably not wise for a thin Al_2O_3 film.

Without considering high temperature annealing, the FGA at 400°C for 30 minutes apparently has the greatest thermal budget; therefore, it was selected to examine by XPS and SIMS the trend of the changes in the Al_2O_3 film after PDA. Fig. 8 shows the curve-fitting results of the $\text{O } 1s$ from XPS. After FGA at 400°C for 30 min, the Al_2O_3 peak becomes higher than the peak of the adsorbed water. The atomic ratio of Al to O is 1:2.4 as-deposited and becomes 1:2.6 after the FGA. This means the surface of the sample becomes relatively O-rich. Moreover, the metallic Al atoms decrease and turn to Al_2O_3 or atoms of



(a)



(b)

Fig. 7. Morphological analysis of annealed samples by FESEM (a) The overlook of blister after RTP 600 °C, (b) The side view of blister after RTP 700 °C

other compositions and bonding structures. Byungha Shin *et al.* reported that ALD Al₂O₃ films are O-rich near the interface and become progressively more Al-rich further away from the interface. Additionally, the sign of the fixed charge is correlated with the local stoichiometry of the films, such that a negative fixed charge exists within the O-rich regions near the interface and a positive fixed charge is within Al-rich regions away from the interface [14]. The ratio of O to Al increases after annealing, indicating more O-related interstitials or fewer Al-related vacancies, which may be responsible for the change in the fixed charge density.

In Fig. 9, SIMS shows the depth profile before and after FGA. It can be seen that H and O diffuse at the interface to the substrate of Si, which can explain the chemical passivation of the dangling bonds at the interface by forming –OH. The O element diffuses to the interface and makes a more O-rich interface, which is the source of field passivation by the negative fixed charges.

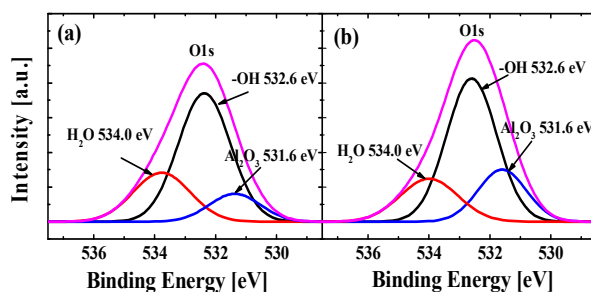


Fig. 8. O 1s photoelectron spectra of Al₂O₃ films recorded by XPS (a) the sample of as-deposited, (b) the sample after FGA at 400 °C, 30 minutes.

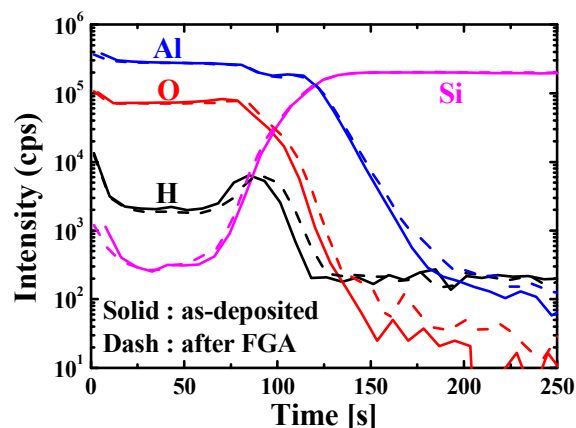


Fig. 9. Depth profile of Al, O, H and Si elements by SIMS. The sample of as-deposited in solid line, and the sample after FGA treatment in dash line.

IV. CONCLUSIONS

We have found that a PDA using RTP at 400 °C can better passivate 20 nm thick Al₂O₃ than FGA at 400 °C for 30 minutes, indicating the potential of PDA for passivating Al₂O₃ on c-Si by RTP. A high thermal budget treatment, especially a high temperature treatment, must be used carefully with Al₂O₃ films because of the blistering. The outdiffusion of H-containing species is believed to be the reason for the formation of blisters. The blistering degrades thermal stability and effective lifetime. An optimal annealing time and temperature that avoids the formation of blisters should be studied for different kinds of Al₂O₃ in the passivation of solar cell applications. The diffusion of O and H elements to the interface are believed to improve the passivation of the interface.

ACKNOWLEDGMENTS

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REFERENCES

- [1] D. S. Kil, S. J. Yeon, K. Hong, J. S. Roh, H. C. Sohn, J. W. Kim, and S. W. Park, "Theoretical Calculation and Experimental Verification of the Hf/Al Concentration Ratio in Nano-mixed $\text{Hf}_x\text{Al}_y\text{O}_z$ Films Prepared by Atomic Layer Deposition," *Journal of Semiconductor Technology and Science*, Vol. 5, No. 2, pp. 120 – 126, June, 2005.
- [2] G. Dingemans, P. Engelhart, R. Seguin, F. Einsele, B. Hoex, M. C. M. van de Sanden, and W. M. M. Kessels, "Stability of Al_2O_3 and $\text{Al}_2\text{O}_3/\text{a-SiNx:H}$ stacks for surface passivation of crystalline silicon," *Journal of Applied Physics*, Vol. 106, No. 11, pp. 114907, Dec, 2009.
- [3] Florian Werner, Boris Veith, Veronica Tiba, Paul Poodt, Fred Roozeboom, Rolf Brendel, and Jan Schmidt, "Very low surface recombination velocities on p- and n-type c-Si by ultrafast spatial atomic layer deposition of aluminum oxide," *Applied Physics Letters*, Vol. 97, No. 16, pp. 162103, Oct, 2010.
- [4] B. Hoex, J. J. H. Gielis, M. C. M. van de Sanden, and W. M. M. Kessels, "On the c-Si surface passivation mechanism by the negative-charge-dielectric Al_2O_3 ," *Journal of Applied Physics*, Vol. 104, No. 11, pp. 113703, Dec, 2008.
- [5] Dong Lei, Xuegong Yu, Lihui Song, Xin Gu, Genhu Li, and Deren Yang, "Modulation of atomic-layer-deposited Al_2O_3 film passivation of silicon surface by rapid thermal processing," *Applied Physics Letters*, Vol. 99, No. 05, pp. 052103, Aug, 2011.
- [6] G. Dingemans, N. M. Terlinden, M. A. Verheijen, M. C. M. van de Sanden, and W. M. M. Kessels, "Controlling the fixed charge and passivation properties of Si(100)/ Al_2O_3 interfaces using ultrathin SiO_2 interlayers synthesized by atomic layer deposition," *Journal of Applied Physics*, Vol. 110, No. 09, pp. 093715, Nov, 2011.
- [7] K. Y. Park, H. I. Cho, E. J. Lee, S. H. Hahm and J. H. Lee, "Device Characteristics of AlGaIn/GaN MIS-HFET using Al_2O_3 Based High-k Dielectric," *Journal of Semiconductor Technology and Science*, Vol. 5, No. 2, pp. 107 – 112, June, 2005.
- [8] G. Dingemans, F. Einsele, W. Beyer, M. C. M. van de Sanden, and W. M. M. Kessels, "Influence of annealing and Al_2O_3 properties on the hydrogen-induced passivation of the Si/ SiO_2 interface," *Journal of Applied Physics*, Vol. 111, No. 09, pp. 093713, May, 2012.
- [9] E. Simoen, A. Rothschild, B. Vermang, J. Poortmans, and R. Mertens, "Impact of Forming Gas Annealing and Firing on the $\text{Al}_2\text{O}_3/\text{p-Si}$ Interface State Spectrum," *Electrochemical and Solid-State Letters*, Vol. 14, No. 9, pp. H362-H364, June, 2011.
- [10] G. Dingemans, W. Beyer, M. C. M. van de Sanden, and W. M. M. Kessels, "Hydrogen induced passivation of Si interfaces by Al_2O_3 films and $\text{SiO}_2/\text{Al}_2\text{O}_3$ stacks," *Applied Physics Letters*, Vol. 97, No. 15, pp. 152106, Oct, 2010.
- [11] G. Dingemans, M. C. M. van de Sanden, and W. M. M. Kessels, "Influence of the Deposition Temperature on the c-Si surface Passivation by Al_2O_3 Films Synthesized by ALD and PECVD," *Electrochemical and Solid-State Letters*, Vol. 13, No. 3, pp. H76-H79, Dec, 2010.
- [12] Kunihiko Iwamoto, Kuuichi Kamimuta, Arito Ogawa, Yukimune Watanabe, Shinji Migita, Wataru Mizubayashi, Yukinori Morita, Masashi Takahashi, Hiroyuki Ota, Toshihide Nabatame, and Akira Toriumi, "Experimental evidence for the flatband voltage shift of high-k metal-oxide-semiconductor devices due to the dipole formation at the high-k/ SiO_2 interface," *Applied Physics Letters*, Vol. 92, No. 13, pp. 132907, April, 2008.
- [13] D. Schuldis, A. Richter, J. Benick, and M. Hermle, "Influence of different post deposition treatments on the passivation quality and interface properties of thermal ALD Al_2O_3 capped PECVD SiNx ," The 27th European PV Solar Energy Conference and

Exhibition, pp. 24-28, Sep, 2012, Frankfurt, Germany.

- [14] Byungha Shin, Justin R. Weber, Rathnait D. Long, Paul K. Hurley, Chris G. Van de Walle, and Paul C. McIntyre, "Origin and passivation of fixed charge in atomic layer deposited aluminum oxide gate insulators on chemically treated InGaAs substrates," *Applied Physics Letters*, Vol. 96, No. 15, pp. 152908, April, 2010.
- [15] A. W. Stephens, A. G. Aberle, and M. A. Green, "Surface recombination velocity measurements at the silicon-silicon dioxide interface by microwavedetected photoconductance decay," *Journal of Applied Physics*, Vol. 76, No. 1, pp. 363 – 370, Jul, 1994.
- [16] P. Saint-Cast, D. Kania, M. Hofmann, J. Benick, J. Rentsch, and R. Preu, "Very low surface recombination velocity on p-type c-Si by high-rate plasma-deposited aluminum oxide," *Applied Physics Letters*, Vol. 95, No. 15, pp. 151502 – 151502-3, Oct, 2009.



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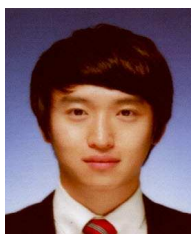
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