

# High-Performance Optical Gating in Junction Device based on Vanadium Dioxide Thin Film Grown by Sol-Gel Method

Yong Wook Lee\*, Eung-Soo Kim\*\*, Bo-Sung Shin\*\*\* and Sang-Mae Lee†

**Abstract** – In this paper, a high-performance optical gating in a junction device based on a vanadium dioxide (VO<sub>2</sub>) thin film grown by a sol-gel method was experimentally demonstrated by directly illuminating the VO<sub>2</sub> film of the device with an infrared light at ~1554.6 nm. The threshold voltage of the fabricated device could be tuned by ~76.8 % at an illumination power of ~39.8 mW resulting in a tuning efficiency of ~1.930 %/mW, which was ~4.9 times as large as that obtained in the previous device fabricated using the VO<sub>2</sub> thin film deposited by a pulsed laser deposition method. The rising and falling times of the optical gating operation were measured as ~50 ms and ~200 ms, respectively, which were ~20 times as rapid as those obtained in the previous device.

**Keywords:** Vanadium dioxide, Thin film, Junction device, Phase transition, Laser-induced breakdown

## 1. Introduction

To date, a phase transition (PT) between insulating and metallic states in a vanadium dioxide (VO<sub>2</sub>) thin film, known to be induced by temperature [1], pressure [2], light [3], electric field [4], etc., has attracted intensive interest due to some potential applications like ultrafast optical switches [5], bistable optoelectronic devices [6], microbolometers [7, 8], and programmable critical temperature sensors [9]. This PT of the VO<sub>2</sub> thin film can be employed to embody a two-terminal junction device with a negative-differential-resistance (NDR) property resulting in an abrupt current jump at a specific threshold voltage [4, 10]. In order to control the threshold voltage of the VO<sub>2</sub> junction device, the photo-assisted electrical gating (optical gating) was recently proposed by utilizing an infrared light illumination that played a role of a gate terminal of a three-terminal device [11]. At an illumination power of 100 mW, however, the tuning displacement of the threshold voltage of the previous device, which was fabricated by using VO<sub>2</sub> thin film deposited by a pulsed laser deposition method, was only ~38.9 % with respect to the initial threshold voltage without the illumination, resulting in a tuning efficiency of ~0.389 %/mW [11]. Moreover, the rising and falling times of the optical gating operation were limited as ~1.0 ms and ~1.2 ms, respectively.

In this paper, a high-performance optical gating in a two-terminal junction device based on a VO<sub>2</sub> thin film grown

by a sol-gel method was experimentally demonstrated by using an infrared light with a wavelength of ~1554.6 nm as the gate terminal of the three-terminal device. At an illumination power of ~39.8 mW, the threshold voltage of the fabricated VO<sub>2</sub> junction device could be tuned by ~76.8 % resulting in the tuning efficiency of ~1.930 %/mW, which was ~4.9 times as large as that obtained in the previous device. In particular, the rising and falling times of the optical gating operation were improved up to ~50 μs and ~200 μs, respectively, which were ~20 times as rapid as those obtained in the previous device.

## 2. Experimental Setup

Fig. 1 shows a plane-view and cross-section of the two-terminal VO<sub>2</sub> junction device with Ni/Au electrodes. A plane-view optical microscope image and a zoomed snapshot of the VO<sub>2</sub> film area of the fabricated device are also shown in Fig. 1. VO<sub>2</sub> thin films with a thickness of ~100 nm were grown onto Al<sub>2</sub>O<sub>3</sub> substrates by the sol-gel method [12]. The fabricated VO<sub>2</sub> film was proven to be highly oriented by using an X-ray diffraction analysis, and the electrical resistance of the film was observed to be changed by more than four orders of magnitude [12]. For the device fabrication, the VO<sub>2</sub> film was selectively etched by an RF ion milling technique in order to isolate the VO<sub>2</sub> film to be used as a current channel, and Ni and Au electrodes were formed on the etched VO<sub>2</sub> film using a lift-off method and an RF magnetron sputter deposition technique. The dimension of the fabricated VO<sub>2</sub> junction device (L × W) was 10 × 5 μm<sup>2</sup> where L and W are the length and width of the exposed film, respectively.

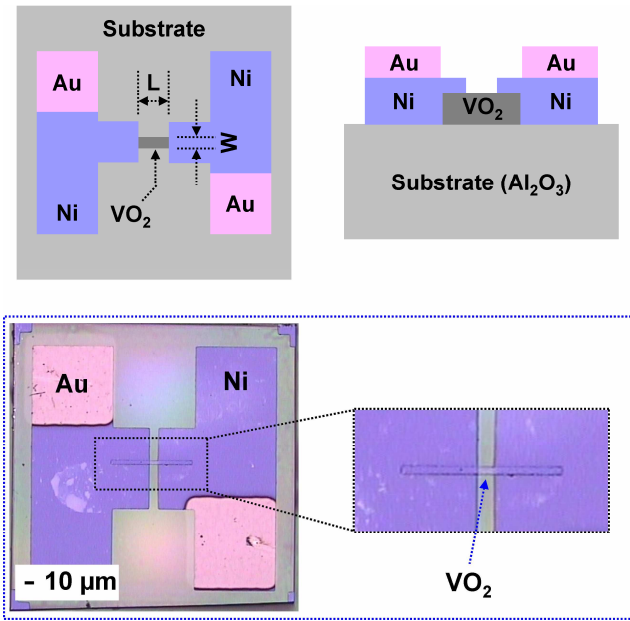
A schematic diagram of the experimental setup, in which the infrared light with a wavelength of ~1554.6 nm illuminates the film for the optical gating in the fabricated

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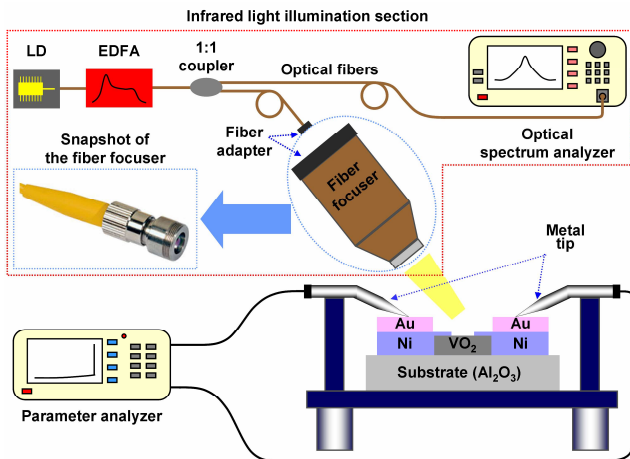
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**Fig. 1.** Plane-view and cross-sectional view of VO<sub>2</sub> device and plane-view optical microscope image of fabricated VO<sub>2</sub> device



**Fig. 2.** Schematic diagram of experimental setup for implementing high-performance optical gating in fabricated VO<sub>2</sub> devices

devices, is composed of the infrared light illumination and the electrical measurement sections, as shown in Fig. 2. In the optical part, an optical output of a distributed feedback laser diode (LD) goes into an erbium-doped fiber amplifier (EDFA) to increase its optical power. Through a 1:1 optical fiber coupler, the amplified output of the EDFA is routed into two output arms of the fiber coupler, and each light component at each output arm has an equal power. One component enters the fiber focuser for illuminating the VO<sub>2</sub> film, and the other component an optical spectrum analyzer (Yokogawa AQ6370) for monitoring the optical power and spectrum of the fiber focuser input. For the investigation of the gating speed, an optical chopper will

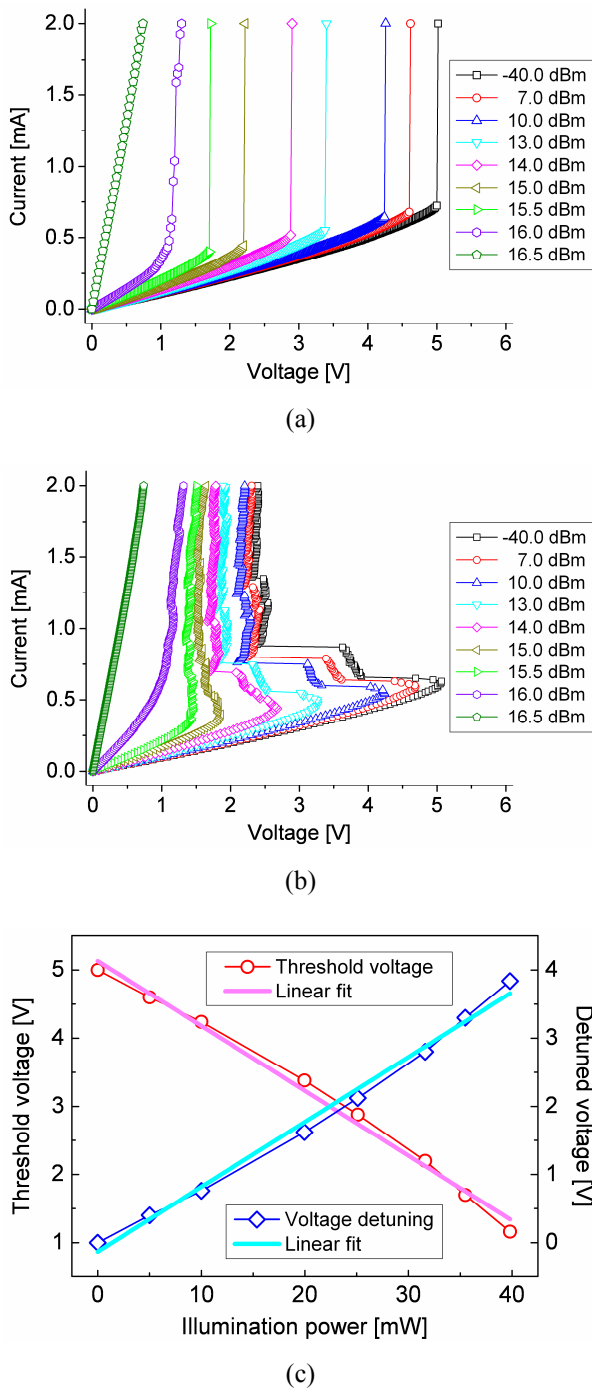
be inserted between the EDFA and the fiber coupler. The fiber focuser output is launched into the film at 30° incidence. The location of the focuser, whose spot diameter at beam waist and working distance to beam waist were designed as ~18 μm and ~10 mm, respectively, was adjusted for the beam spot diameter to be ~450 μm by using an *xyz* translation stage. For electrical measurements of the VO<sub>2</sub> devices, a parameter analyzer (HP 4156C) and a microprobe-station were employed. For fine position control of the metal tip brought into contact with the electrodes of the junction devices, the microprobe-station contained micromanipulators and an optical microscope.

### 3. Experimental Results and Discussions

For the fabricated VO<sub>2</sub> junction devices, optical gating operations were experimentally investigated in a voltage-controlled mode (*V* mode) and a current-controlled mode (*I* mode) as shown in Figs. 3(a) and 3(b), respectively. The central wavelength of the amplified light from the EDFA was measured as ~1554.6 nm, and the optical intensity of the light beam projected onto the film was ~25.2 W/cm<sup>2</sup> at an illumination power of 40 mW. Figs. 3(a) and 3(b) show the change of current-voltage (*I-V*) properties of the junction device with respect to various illumination powers, which are measured with a parameter analyzer in *V* mode and *I* mode, respectively. In order to prevent the high current from flowing through the device, the device current was restricted to 2 mA, that is to say, the compliance current was set as 2 mA.

As can be seen from Fig. 3(a), the threshold voltage of the fabricated VO<sub>2</sub> device is fixed as ~5.0 V without the light illumination, but it decreases, i.e., is shifted toward 0 V, with the increase of the illumination power. The tuning displacement of the threshold voltage is evaluated as ~3.9 V with respect to the illumination power variation of ~39.8 mW (16.0 dBm). Fig. 3(b) shows the NDR characteristics of the fabricated VO<sub>2</sub> device resulting in abrupt current jumps measured in *V* mode. With the increase of the illumination power, these NDR curves also shrink toward 0 V, and the NDR features gradually disappear. At an illumination power of ~44.7 mW (16.5 dBm), the current jump cannot be found in both measurement mode, just showing an *I-V* behavior of a standard resistor, which implies that metallic VO<sub>2</sub> grains dominate within the VO<sub>2</sub> film compared with insulating VO<sub>2</sub> grains [13].

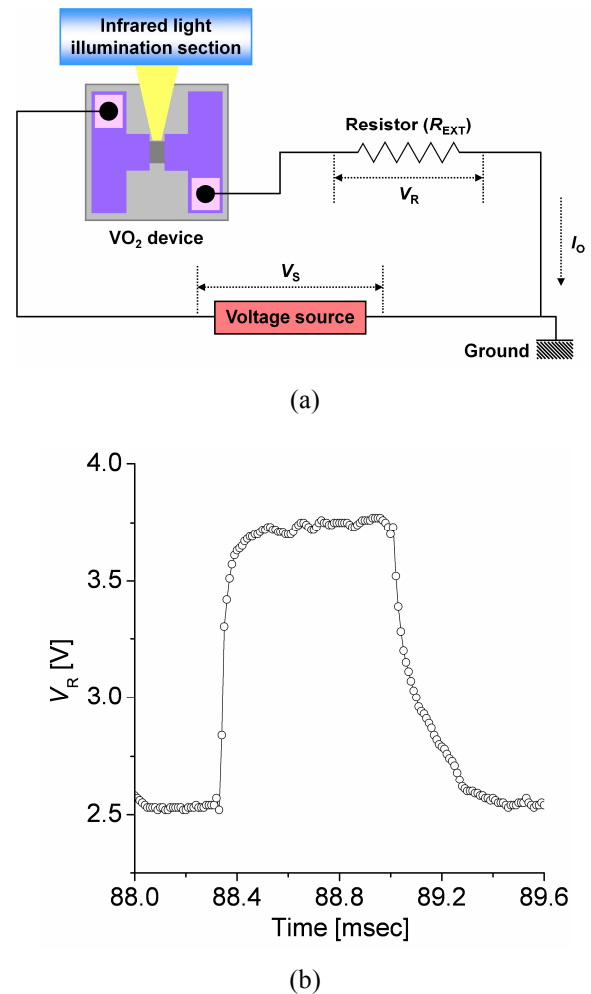
Fig. 3(c) shows the tuning linearity of the measured threshold voltage and detuned voltage with respect to illumination power, which are indicated in red circular and blue diamond symbols, respectively. On the basis of the threshold voltages obtained at various illumination powers in Fig. 3(a), detuned voltages were calculated by setting the threshold voltage at zero illumination power as the reference. Both measured threshold voltages and detuned voltages calculated from them were linearly fitted with the



**Fig. 3.** Optical gating operations in (a)  $V$  mode; (b)  $I$  mode; (c) Tuning linearity of threshold voltage and detuned voltage of  $\text{VO}_2$  device

adjusted R-square value of the linear fit evaluated as 0.99092, and the tuning sensitivity of the threshold voltage with respect to the illumination power was obtained as  $\sim 96.5$  V/W. Especially, the threshold voltage could be tuned by  $\sim 76.8$  % (from  $\sim 5.0$  V to  $\sim 1.1$  V) at an illumination power of  $\sim 39.8$  mW resulting in a tuning efficiency of  $\sim 1.930$  %/mW, which was  $\sim 4.9$  times as large as that obtained in the previous device [11].

In order to examine the enhancement of the gating speed, additional experiments on the gating speed were performed by employing an optical chopper that was inserted between the EDFA and the fiber coupler in the infrared light illumination section of Fig. 2. A test electrical circuit used in the previous study, composed of a standard resistor ( $R_{\text{EXT}}$ ) connected in series with the  $\text{VO}_2$  device and a DC voltage source ( $V_S$ ), as shown in Fig. 4(a), was utilized again. When  $V_S$  is set at a fixed value that is slightly less than the threshold voltage of the  $\text{VO}_2$  device, the device remains in its high resistance state. If the device is illuminated by the infrared light, however, its threshold voltage becomes less than  $V_S$ ; then, it changes into its low resistance state. In the test circuit, the voltage across the standard resistance ( $V_R$ ) is defined as  $V_S$  minus the device voltage, and  $V_R$  has a complementary relationship with the device voltage. For the investigation of the gating speed, the transient electrical responses of  $V_R$  were observed with  $V_S$  and  $R_S$  fixed as 4 V and  $2$  k $\Omega$ , respectively, when the optical chopper was rotated at a fixed operating frequency



**Fig. 4.** (a) Test electrical circuit for investigation of gating speed in  $\text{VO}_2$  devices; (b) Transient electrical response of  $V_R$  with optical chopper rotated at 700 Hz

with the LD continuously operated. The wavelength and optical power of the light coming out of the optical chopper were  $\sim 1554.6$  nm and 25 mW, respectively.

Fig. 4(b) shows the transient electrical response of  $V_R$  when the optical chopper is rotated with its operating frequency of 700 Hz. The rising and falling times were measured as  $\sim 50$   $\mu$ s and  $\sim 200$   $\mu$ s, respectively, which were  $\sim 20$  times as rapid as those obtained in the previous study. Longer falling time is considered to be caused from the thermal diffusion due to the subsequent system thermalization. Although this thermalization cannot be completely avoided, the area of the heat sink is expected to be somewhat diminished by reducing the spot diameter of the illuminated beam so that the beam should not be illuminated onto the surrounding materials (electrodes and substrates) adjacent to the VO<sub>2</sub> thin film.

#### 4. Conclusion

In this paper, a high-performance optical gating operation was experimentally investigated with respect to the junction devices based on VO<sub>2</sub> thin films grown by the sol-gel method by direct infrared illumination of the film of the device. The tuning efficiency and gating speed were highly enhanced compared with the previous study employing the VO<sub>2</sub> film grown by the pulsed laser deposition technique. The demonstrated high-performance optical gating is expected to be usefully applied to light triggered thyristors recently being adopted for many power stations, substations, and power utilities.

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#### References

- [1] F. J. Morin, "Oxides which show a metal-to-insulator transition at the neel temperature," *Phys. Rev. Lett.*, vol. 3, pp. 34-36, 1959.
- [2] E. Arcangeletti, L. Baldassarre, D. D. Castro, S. Lupi, L. Malavasi, C. Marini, A. Perucchi, and P. Postorino, "Evidence of a pressure-induced metallization process in monoclinic VO<sub>2</sub>," *Phys. Rev. Lett.*, vol. 98, pp. 196406(1-4), 2007.
- [3] A. Cavalleri, Cs. Tóth, C. W. Siders, J. A. Squier, F.

- Ráksi, P. Forget, and J. C. Kieffer, "Femtosecond structural dynamics in VO<sub>2</sub> during an ultrafast solid-solid phase transition," *Phys. Rev. Lett.*, vol. 87, pp. 237401(1-4), 2001.
- [4] H.-T. Kim, B.-G. Chae, D.-H. Youn, S.-L. Maeng, G. Kim, K.-Y. Kang, and Y.-S. Lim, "Mechanism and observation of Mott transition in VO<sub>2</sub>-based two- and three-terminal devices," *N. J. Phys.*, vol. 6, pp. 52-70, 2004.
- [5] S. Lysenko, A. J. Rua, V. Vikhnin, J. Jimenez, F. Fernandez, and H. Liu, "Light-induced ultrafast phase transitions in VO<sub>2</sub> thin film," *Appl. Surf. Sci.*, vol. 252, pp. 5512-5515, 2006.
- [6] S. Chen, H. Ma, X. Yi, H. Wang, X. Tao, M. Chen, X. Li, and C. Ke, "Optical switch based on vanadium dioxide thin films," *Infrared Phys. Tech.*, vol. 45, pp. 239-242, 2004.
- [7] C. Chen, X. Yi, X. Zhao, and B. Xiong, "Characterizations of VO<sub>2</sub>-based uncooled microbolometer linear array," *Sens. Actuators A*, vol. 90, pp. 212-214, 2001.
- [8] C. Chen and Z. Zhou, "Optical phonons assisted infrared absorption in VO<sub>2</sub> based bolometer," *Appl. Phys. Lett.*, vol. 91, pp. 011107(1-3), 2007.
- [9] B.-J. Kim, Y. W. Lee, B.-G. Chae, S. J. Yun, S.-Y. Oh, H.-T. Kim, and Y.-S. Lim, "Temperature dependence of the first-order metal-insulator transition in VO<sub>2</sub> and programmable critical temperature sensor," *Appl. Phys. Lett.*, vol. 90, pp. 023515(1-3), 2007.
- [10] C.-R. Cho, S. Cho, S. Vadim, R. Jung, and I. Yoo, "Current-induced metal-insulator transition in VO<sub>x</sub> thin film prepared by rapid-thermal-annealing," *Thin Solid Films*, vol. 495, pp. 375-379, 2006.
- [11] Y. W. Lee, B.-J. Kim, S. Choi, H.-T. Kim, and G. Kim, "Photo-assisted electrical gating in a two-terminal device based on vanadium dioxide thin film," *Opt. Express*, vol. 15, pp. 12108-12113, 2007.
- [12] B.-G. Chae, H.-T. Kim, S. J. Yun, Y. W. Lee, B.-J. Kim, D.-H. Youn, and K.-Y. Kang, "Highly oriented VO<sub>2</sub> thin films prepared by sol-gel deposition," *Electrochem. Solid-State Lett.*, vol. 9, pp. C12-C14, 2006.
- [13] Y. W. Lee, B.-J. Kim, J.-W. Lim, S. J. Yun, S. Choi, B.-G. Chae, G. Kim, and H.-T. Kim, "Metal-insulator transition-induced electrical oscillation in vanadium dioxide thin film," *Appl. Phys. Lett.*, vol. 92, pp. 162903(1-3), 2008.



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