

# Physical Modeling of SiC Power Diodes with Empirical Approximation

Leobardo Hernández<sup>†</sup>, Abraham Claudio\*, Marco A. Rodríguez\*\*, Mario Ponce\*, and Alejandro Tapia\*\*\*

<sup>†\*\*\*</sup> Departamento de Ingeniería en Comunicaciones y Electrónica del I.P.N., Mexico City, México

\* Centro Nacional de Investigación y Desarrollo Tecnológico (CENIDET) Cuernavaca, Morelos, México

\*\* Universidad Autónoma del Carmen (UNACAR), Ciudad del Carmen, Campeche, México

## Abstract

This article presents the development of a model for SiC power diodes based on the physics of the semiconductor. The model is able to simulate the behavior of the dynamics of the charges in the  $N-$  region based on the stored charge inside the SiC power diode, depending on the working regime of the device (turn-on, on-state, and turn-off). The optimal individual calculation of the ambipolar diffusion length for every phase of commutation allows for solving the ambipolar diffusion equation (ADE) using a very simple approach. By means of this methodology development a set of differential equations that models the main physical phenomena associated with the semiconductor power device are obtained. The model is developed in Pspice with acceptable simulation times and without convergence problems during its implementation.

**Key Words:** Physical Modeling, Silicon Carbide, Simulation Semiconductor

## I. INTRODUCTION

Silicon carbide (SiC) is an attractive material for replacing the silicon used in the manufacturing of power devices. SiC presents better properties than silicon, such as: a wide bandgap (2x), a higher saturation electron drift velocity (2x), a high thermal conductivity (5x) and a very high breakdown field (10x) [1]–[3]. Among the different silicon carbide power devices that have been developed [4]–[11], the SiC power diode structure is one of most interesting. It can appear as a discrete component or as an internal diode in more complex power conversion circuits. Diverse modeling methodologies have appeared in the literature for the development of models for semiconductor power devices. According to [12] these can be classified in two groups: functional models and analytical solutions. The proposed approaches in the last group can be subdivided into: separation of variables, transformation (Laplace, Fourier), concentrated charges and numerical solutions [13]–[16]. This type of methodology is known as a physical model. It is one of most efficient and it is mainly concerned with modeling the charges in the  $N-$  region, where every physical phenomenon of the device is related to a

specific term in a set of derived equations.

The proposed model describes the static and dynamic behaviors with a temperature dependency in the experimental operation range (25°C–225°C) for silicon carbide, which has been reported in the literature [13]–[15]. Furthermore, obtaining a set of linear differential equations allows simulating in Pspice the main associate physical phenomena for the semiconductor device, as well as modeling of the injection and the evacuation of charges in the  $N-$  region. The proposed methodology solves the ADE with dependency on the adjustment of the ambipolar diffusion length ( $L$ ), which is achieved by using a simplified semi-theoretical approach. For every commutation phase, the proposed suitable analytical expression allows for the calculation of a new  $L$  value.

## II. MODELING PRINCIPLE

The power diode structure has been simplified in order to help the mathematical analysis. As a result, only two conventional  $p-n$  junctions are supposed. Fig. 1(a) shows the typical structure of the power diode which is used for the physical design of this kind of device. Fig. 1(b) shows the simplified structure used to model the SiC power diode.

Fig. 2 shows the basic structure of a power diode. In this Figure,  $I_{n0}$ ,  $I_{p0}$ ,  $I_{nW}$  and  $I_{pW}$ , represent the injected currents into the  $N-$  region,  $V_{J1}$  and  $V_{J2}$  represent the drop voltages of the  $P^+N^-$  and  $N^-N^+$  junctions, respectively,  $x_L$  and  $x_R$  represent the limits of the depletion regions within  $N-$ ,  $W'$  represents the width of the depletion region formed in the  $P^+N^-$  junction, and  $W = W_B - W'$  is the effective width of the  $N-$  region.

Manuscript received Dec. 23, 2010; revised Feb. 25, 2011

Recommended for publication by Associate Editor Chong-Man Yun.

<sup>†</sup> Corresponding Author: lhernandezg@ipn.mx, bilbito\_98@yahoo.com  
Tel: +55-562-420-00, ESIME-Culhuacan

\* Centro Nacional de Investigación y Desarrollo Tecnológico (CENIDET) Cuernavaca, México

\*\* Universidad Autónoma del Carmen (UNACAR), Ciudad del Carmen, México

\*\*\* Departamento de Ingeniería en Comunicaciones y Electrónica del I.P.N., México

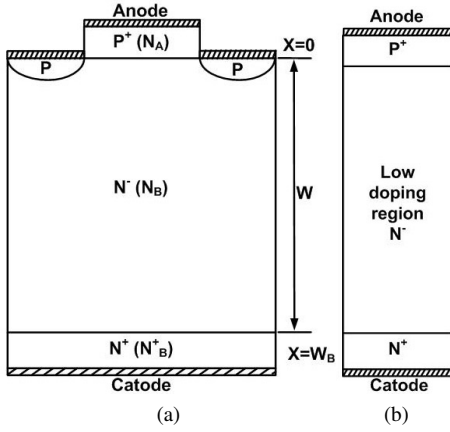


Fig. 1. Structure for: (a) Design, (b) Modeling.

Assuming a high-level injection, the charge dynamics are described by the ambipolar diffusion equation (1):

$$\frac{\partial^2 p(x,t)}{\partial x^2} = \frac{p(x,t)}{L^2} + \frac{1}{D} \frac{\partial p(x,t)}{\partial t}. \quad (1)$$

The currents depicted in the Figure 2, can be calculated from the transport equations given by (2) and (3), and the total  $N-$  region charge,  $Q_B$ , is expressed by (4) [17], [18].

$$I_{n(x,t)} = \frac{b \cdot I_T}{1+b} + qAD \cdot \frac{\partial p(x,t)}{\partial x} \quad (2)$$

$$I_{p(x,t)} = \frac{I_T}{1+b} - qAD \cdot \frac{\partial p(x,t)}{\partial x} \quad (3)$$

$$Q_B = qA \cdot \int_0^W p(x,t) \cdot dx. \quad (4)$$

In a high injection condition, the low resistance that appears in the  $N-$  region is the result of a high level concentration of charges that are injected by both emitters and it allows for the transport of a high current through a power diode with a low on-state voltage drop. The value of the resistance  $R_M$ , in the  $N-$  region for the semiconductor power devices is given by (5), [17], [18].

$$R_M = \int_0^W \frac{dx}{qA \cdot (\mu_n n + \mu_p p)} = \frac{W^2}{(\mu_n + \mu_p) \cdot Q_B + \mu_n Q_0} \quad (5)$$

where  $Q_0$  represents the charge stored in the thermodynamic-equilibrium. To calculate the  $V_{J1}$  and  $V_{J2}$  voltages, the Shockley equation will be used in (6) and (7). The mathematical expressions (1)-(7) are the basis of the static and dynamic models of the SiC power diode.

$$I_{n0} = I_{P+S} \left[ \exp\left(\frac{V_{J1}}{V_T}\right) - 1 \right] \quad (6)$$

$$I_{nW} = I_{N+S} \left[ \exp\left(\frac{V_{J2}}{V_T}\right) - 1 \right] \quad (7)$$

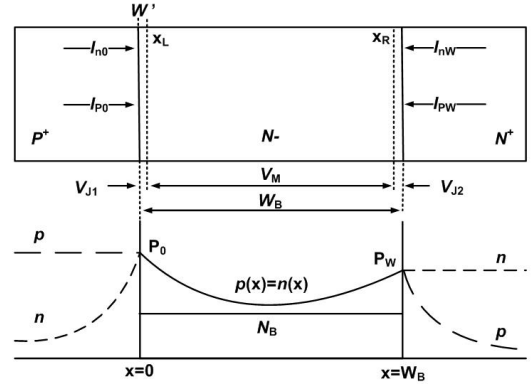


Fig. 2. Power diode basic structure and charges distribution.

### III. STATIC STATE MODEL

#### A. Carrier distribution $p(x)$

Equation (1) can be reduced to a differential equation. This is because during the on-state condition,  $\partial p(x,t)/\partial t = 0$ . Under this consideration the analytical solution of (1) is given by (8).

$$p(x) = \frac{P_W \cdot \sinh\left(\frac{x}{L_S}\right) + P_0 \cdot \sinh\left(\frac{W-x}{L_S}\right)}{\sinh\left(\frac{W}{L_S}\right)} \quad (8)$$

where  $L_S = \sqrt{D \cdot \tau}$  represents the on-state ambipolar diffusion length, where  $\tau$  is the steady state life time defined in [13].  $P_0$  and  $P_W$  represent the carrier concentrations at the two edges of the  $N-$  region. The boundary concentrations  $P_0$  and  $P_W$  are calculated under the quasi-equilibrium condition for a high-level injection condition [19].

$$P_0 = n_i \sqrt{\frac{I_{n0}}{I_{P+S}}} \quad (9)$$

$$P_W = n_i \sqrt{\frac{I_{nW}}{I_{N+S}}} \quad (10)$$

where  $I_{P+S}$  and  $I_{N+S}$  represent the saturation current in the  $P^+N^-$  and  $N^-N^+$  junctions, respectively.  $I_{n0}$  and  $I_{nW}$  can be calculated from (6) and (7). The stored charges  $Q_B$  in the  $N-$  region can be calculated by (11), which is obtained by substituting (8) into (4).

$$Q_B = qAL_S \cdot (P_0 + P_W) \tanh\left(\frac{W}{2L_S}\right). \quad (11)$$

That allows the calculation of the modulation resistance according to (5), which is obtained as follows:

$$R_M = \frac{W^2}{[\mu_n + \mu_p] \left[ qAL_S (P_0 + P_W) \tanh\left(\frac{W}{2L_S}\right) \right] + \mu_n Q_0}. \quad (12)$$

#### B. Modeling the injected currents

The total injected current is the sum of the hole and electron currents. The Pspice implementation is performed because the electron currents are modeled through standard diodes adjusted to the SiC electrical characteristics, Equ.(6) and (7), and the

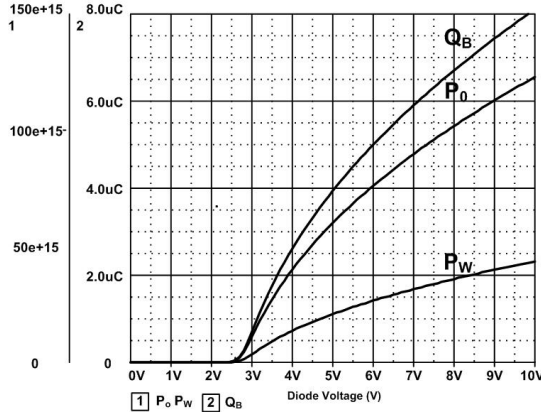


Fig. 3. Simulation results for the  $P_0$ ,  $P_W$ , and  $Q_B$  with  $V_{ON} \approx 2.8V$ .

holes currents are modeled by the transport equation, Equ. (13) and (14).

$$I_{p0} = \frac{I_T}{1+b} - qAD \cdot \left. \frac{\partial p(x)}{\partial x} \right|_{x=0} \quad (13)$$

$$I_{pW} = \frac{I_T}{1+b} - qAD \cdot \left. \frac{\partial p(x)}{\partial x} \right|_{x=W} \quad (14)$$

The gradients of the carrier concentrations are obtained from (8), which is evaluated at  $x=0$  and  $x=W_B$ .

$$\left. \frac{dp(x)}{dx} \right|_{x=0} = \frac{1}{L_S} \frac{P_W - P_0 \cdot \cosh\left(\frac{W}{L_S}\right)}{\sinh\left(\frac{W}{L_S}\right)} \quad (15)$$

$$\left. \frac{dp(x)}{dx} \right|_{x=W} = \frac{1}{L_S} \frac{P_W \cdot \cosh\left(\frac{W}{L_S}\right) - P_0}{\sinh\left(\frac{W}{L_S}\right)} \quad (16)$$

In Fig. 3, the simulation results for  $P_0$ ,  $P_W$  and  $Q_B$  are presented. For  $n_i \approx 6.7 \times 10^{-11} \text{cm}^{-3}$  and assuming that the  $N_A \cdot N_D$  product has the typical value of  $10^{35} \text{cm}^{-6}$ , the theoretically expected on-state voltage is 2.70V [17], [18]. The obtained simulation results concerning the on-state voltage are corroborated by the expected theoretical value, which is 2.8V, as can be seen in Fig. 3.

#### IV. DYNAMIC MODEL

##### A. Turn-on carrier distribution $p(x,t)$ solution

The solution of  $p(x,t)$  is similar to the on-state case. An interesting contribution has been introduced in the proposed dynamic model which is dependent on time based on a new  $L_{ON}$ , as can be observed in (17). The  $L_{ON}(x,t)$  variation is based on the stored charges  $Q_B$ , which are installed in the  $N$ -region depending on time, during this commutation.

$$p(x,t) = \frac{P_W \cdot \sinh\left(\frac{x}{L_{ON}(x,t)}\right) + P_0 \cdot \sinh\left(\frac{W-x}{L_{ON}(x,t)}\right)}{\sinh\left(\frac{W}{L_{ON}(x,t)}\right)} \quad (17)$$

The holes currents are modeled by the transport equation again, equations (13) and (14) with (17). The new gradients evaluated in  $x=0$  and  $x=W$ , as well as the calculation of

the  $Q_{BON}$  charges that are injected during the turn-on, are presented in (18)-(20).

$$\left. \frac{dp(x,t)}{dx} \right|_{x=0} = \frac{P_W - P_0 \cdot \cosh\left(\frac{W}{L_{ON}(x,t)}\right)}{L_{ON}(x,t) \cdot \sinh\left(\frac{W}{L_{ON}(x,t)}\right)} \quad (18)$$

$$\left. \frac{dp(x,t)}{dx} \right|_{x=W} = \frac{P_W \cdot \cosh\left(\frac{W}{L_{ON}(x,t)}\right) - P_0}{L_{ON}(x,t) \cdot \sinh\left(\frac{W}{L_{ON}(x,t)}\right)} \quad (19)$$

$$Q_{BON} = qAL_{ON}(x,t) \cdot (P_0 + P_W) \tanh\left(\frac{W}{2L_{ON}(x,t)}\right) \quad (20)$$

##### B. Diffusion length $L_{ON}(x,t)$ calculation

To calculate  $L_{ON}(x,t)$ , the charge control equation (21) is modified by the introduction of a new stored charge variable,  $Q_B = Q_{BON}$ .

$$I_T = \frac{Q_{BON}}{\tau} + \frac{dQ_{BON}}{dt} \quad (21)$$

Under the conditions where  $I_{p0} > I_{n0}$  then  $I_T \approx I_{p0} - I_{pW}$  and by replacing the previous conditions into (21) the following modified expression is obtained:

$$I_{p0} - I_{pW} = \frac{Q_{BON}}{\tau} + \frac{dQ_{BON}}{dt} \quad (22)$$

Combining (13), (14), (18), (19) and (20) with (22), the final expression used to calculate  $L_{ON}(x,t)$  is obtained.

$$L_{ON}(x,t) = \sqrt{\frac{D \cdot Q_{BON}}{\frac{Q_{BON}}{\tau} + \frac{dQ_{BON}}{dt}}} \quad (23)$$

Analyzing (23), it is possible to conclude that the expected theoretical behavior is as follows:

- Before the beginning the injection of currents to the  $N$ -region, the stored charges are ( $Q_{BON} = 0$ ) and  $L_{ON}(x,t) = L_S$ . It can be concluded that the power diode structure behavior is under a static condition.
- When the injection begins ( $Q_{BON} > 0$ ), a high injection of charges is imposed ( $dQ_{BON}/dt > 0$ ), which implies  $L_{ON}(x,t) < L_S$ , as can be seen in (23).
- During the turn-on, the injected charges reach the maximum value ( $Q_{BON} = Q_B$ ), and are stabilized for the on-state condition, which implies  $L_{ON}(x,t) = L_S$ .

Fig. 4 shows the simulation results for the  $L_{ON}(x,t)$  variation during the turn-on. During this phase,  $L_{ON}(x,t)$  takes the minimum value of  $0.597 \times 10^{-3} \text{cm}$  and converges to the static value of  $L_S = 0.737 \times 10^{-3} \text{cm}$ . A simulation of the injected charges,  $Q_B$ , is also observed, where  $Q_0 = 104 \text{nC}$ . At  $t \approx 20.20 \text{nS}$  the turn-on ends and the on-state initiates. Based on the simulation results, the validity of the  $L_{ON}(x,t)$  expression to solve  $p(x)$  is verified.

### C. Turn-off carrier distribution $p(x,t)$ solution

Fig. 5 shows the typical reverse recovery waveforms of current and voltage that are observed in typical applications of power converters. The typical carrier distributions evolution, during this phase, can be observed in the numerical results shown in Fig. 6 [20]. In order to obtain an adequate  $p(x,t)$  approximation, it is proposed to model the charges, by modelling the three sections defined by a typical distribution for this kind of devices. This means a hyperbolic distribution of the central part, and a sinusoidal distribution of the lateral sides. It can be observed in Fig. 7, where  $P_L$  and  $P_R$  represent the initial and final concentrations for the  $p_{2(x)}$  section. The proposed expressions to model every section are observed in (25)-(27).

$$p_{1(x,t)} = P_L \cdot \cos\left(\frac{x - x_L}{L_L(x,t)}\right) \quad (24)$$

$$p_{2(x,t)} = \frac{P_R \cdot \sinh\left(\frac{x - x_L}{L_M(x,t)}\right) + P_L \cdot \sinh\left(\frac{x_R - x}{L_M(x,t)}\right)}{\sinh\left(\frac{x_R - x_L}{L_M(x,t)}\right)} \quad (25)$$

$$p_{3(x,t)} = P_R \cdot \cos\left(\frac{x - x_R}{L_R(x,t)}\right). \quad (26)$$

The three new diffusion lengths ( $L_L(x,t)$ ,  $L_M(x,t)$  and  $L_R(x,t)$ ) are very important due to the fact that they model the curvature in every section for simulation. The new ambipolar diffusion lengths for every section are obtained as follows: for  $L_L(x,t)$ , the  $p_{1(x,t)}$  derivative evaluated in  $x = 0$ , is:

$$\left. \frac{\partial p_{1(x,t)}}{\partial x} \right|_{x=0} = \frac{P_L}{L_L(x,t)} \cdot \sin\left(\frac{x_L}{L_L(x,t)}\right). \quad (27)$$

Replacing in (27), the  $P_0/P_L$  evaluated at  $x = 0$  from (24) and using the identity  $\cos^{-1}(x) = \sin^{-1}\sqrt{1-x^2}$ , the final expression for  $L_L(x,t)$  is given in (28).

$$L_L(x,t) = \frac{\sqrt{P_L^2 - P_0^2}}{\left. \frac{dp(x,t)}{dx} \right|_{x=0}}. \quad (28)$$

For  $L_M$ , the total current is used. It can be approximated to the turn-on analogously.

$$I_T(x) \approx I_{p(x_L)} - I_{p(x_R)}. \quad (29)$$

Replacing (29) into (22) and considering the displacement currents that appear in the depletion regions, the following is obtained:

$$I_{p(x_L,t)} - I_{p(x_R,t)} = \frac{Q_M}{\tau} + \frac{dQ_M}{dt} - AqPL \frac{dx_L}{dt} + AqPR \frac{dx_R}{dt}. \quad (30)$$

To calculate  $I_{p(x_L,t)}$  and  $I_{p(x_R,t)}$ , (3) is evaluated in  $x_L$  and  $x_R$ . Replacing this into (30), the final expression for  $L_M$  is obtained.

$$L_M(x,t) = \sqrt{\frac{D \cdot Q_M}{\frac{Q_M}{\tau} + \frac{dQ_M}{dt} - qAPL \frac{dx_L}{dt} + qAPR \frac{dx_R}{dt}}}. \quad (31)$$

For  $L_R(x,t)$ , the  $p_{3(x,t)}$  derivative evaluated in  $x = W$  is obtained.

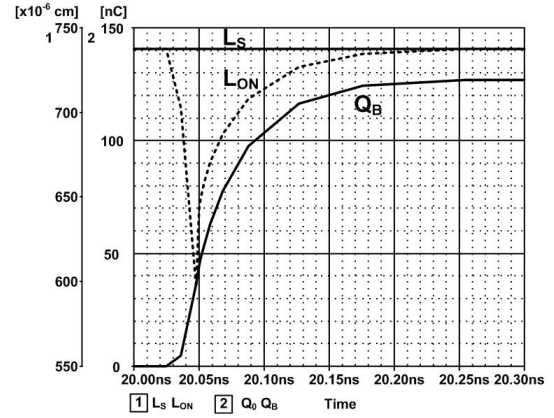


Fig. 4.  $L_{ON}(x,t)$ , and  $Q_B$  behavior in relation to  $L_S$  y  $Q_0$  respectively.

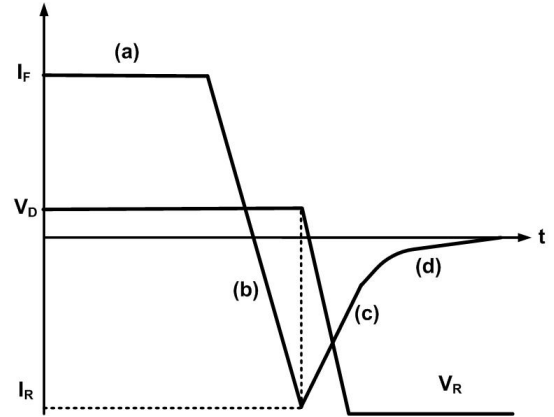


Fig. 5. Reverse recovery waveforms for power diode.

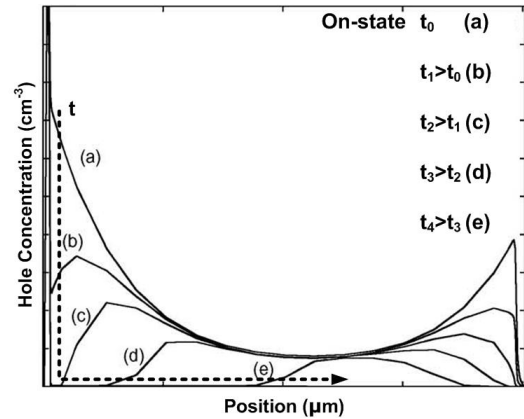


Fig. 6.  $p(x,t)$  numerical results during the turn-off phase.

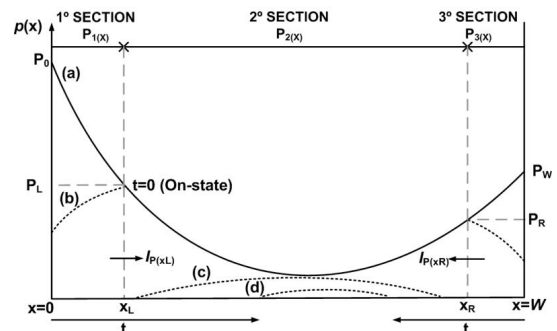


Fig. 7.  $p(x)$  modeling proposal for the turn-off phase.

$$\left. \frac{\partial p_{3(x,t)}}{\partial x} \right|_{x=W} = \frac{P_R}{L_R(x,t)} \cdot \sin\left(\frac{W-x_R}{L_R(x,t)}\right). \quad (32)$$

Replacing in (32), the  $P_W/P_R$  relation evaluated in  $x = W$  of (26) and using the identity  $\cos^{-1}(x) = \sin^{-1}\sqrt{1-x^2}$ , the  $L_R(x,t)$  final expression is obtained.

$$L_R(x,t) = \frac{\sqrt{P_R^2 - P_W^2}}{\left. \frac{dp(x,t)}{dx} \right|_{x=W}}. \quad (33)$$

As in Equ. (23) during the on-state, the development expressions given by (28), (31) and (33) can allow modeling the time dependency in a continuous way, which can guarantee an adequate approximation of the model. The new gradients for  $p(x,t)$  evaluated in  $x = 0$ ,  $x_L$ ,  $x_R$  and  $x = W$ , are calculated as follows: for the  $0 < x \leq x_L$  range, it is fulfilled:

$$I_{p(0,t)} - I_{p(x_L,t)} = \frac{Q_L}{\tau} + \frac{dQ_L}{dt} - AqP_L \cdot \frac{dx_L}{dt}. \quad (34)$$

$I_{p(0)}$ ,  $I_{p(x_L)}$ , are calculated by (3) evaluated in  $x = x_L$  and  $x = 0$ , as  $dp(x = x_L)/dx = 0$ . By replacing the previous conditions into (34), the following is obtained:

$$\left. \frac{dp(x,t)}{dx} \right|_{x=0} = \frac{1}{qAD} \left[ -\frac{Q_L}{\tau} - \frac{dQ_L}{dt} + qAP_L \cdot \frac{dx_L}{dt} \right]. \quad (35)$$

For the  $x_L \leq x \leq x_R$  range, the following is fulfilled:

$$I_{p(x_R,t)} - I_{p(W,t)} = \frac{Q_R}{\tau} + \frac{dQ_R}{dt} - AqP_R \cdot \frac{dx_R}{dt}. \quad (36)$$

$I_{p(x_R,t)}$ ,  $I_{p(W,t)}$  are calculated with (3) evaluated in  $x = x_R$  y  $x = W$ , as  $dp(x = x_R)/dx = 0$ . By the substitution of these conditions into (36), the following is obtained:

$$\left. \frac{dp(x,t)}{dx} \right|_{x=W} = \frac{1}{qAD} \left[ \frac{Q_R}{\tau} - \frac{dQ_R}{dt} - qAP_R \cdot \frac{dx_R}{dt} \right]. \quad (37)$$

The expressions for calculating  $Q_L$ ,  $Q_M$  and  $Q_R$ , are obtained by evaluating the  $p(x,t)$  integral for every proposed section. This is given by (38), (39) and (40).

$$Q_L = qAL_L(x,t) \sqrt{P_L^2 - P_0^2} \quad (38)$$

$$Q_M = qAL_M(x,t) (P_R + P_L) \tanh\left(\frac{x_R - x_L}{2L_M(x,t)}\right) \quad (39)$$

$$Q_R = qAL_R(x,t) \sqrt{P_R^2 - P_W^2} qA. \quad (40)$$

Finally, the expressions to calculate  $x_L$  and  $x_R$  are given by (41) and (42).

$$x_L \approx \sqrt{\frac{2\epsilon_s(V_{bi} + V_{j1})}{qN_D}} \quad (41)$$

$$x_R \approx \sqrt{\frac{2\epsilon_s(V_{bi} + V_{j2})}{qN_D}}. \quad (42)$$

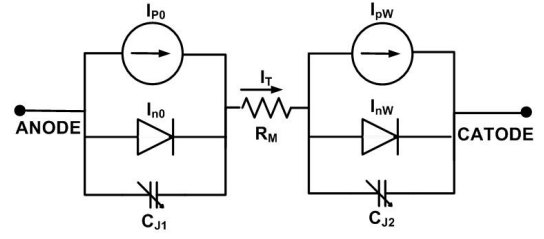


Fig. 8. Simplified electric equivalent to simulate the SiC power diode.

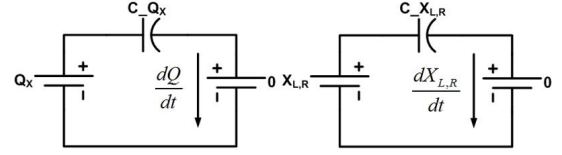


Fig. 9. Auxiliary circuits that simulate the nonlinear terms in Pspice.

## V. MODEL IMPLEMENTATION

In Fig. 9, the auxiliary electrical circuits used to solve the non-nonlinear terms:  $dQ_{ON,M,L,R}/dt$  and  $dX_{L,R}/dt$  presented in (30)-(31) and (35)-(37), are shown. The expressions (23), (28), (31) and (33) assure an ADE continuous time solution. This can allow for the easy implementation of auxiliary circuits that have fewer numerical convergence problems in the simulation.

## VI. SIMULATION RESULTS

To simulate the on-state rectifier circuits shown in Figure 10a, and for the dynamic phases, the chopper circuit shown in Figure 10b was used. In order to obtain simulation results for the model developed in Pspice, the power diode structure for an  $I_{D_{MAX}} = 5A$  was calculated. The main calculated parameters that were used in the implementation of the model can be seen in Table I.

In Fig. 11, the simulation results for the static behavior in the range of 25°C to 225°C are presented. The observed on-voltage presents a dependency with respect to temperature, which is indicative of an increment in the life time, and a decrement of the bandgap value. In Fig. 12, the comparison between the experimental results reported in [15] and the results of the Pspice-implemented model for the SiC power diode are shown.

In Fig. 13, the simulation results of the turn-on with the dependency on temperature are presented. When the temperature is increased the on-voltage decreases and the current conduction increases, which is in agreement with the theoretical behavior expected according to the on-state. In Fig. 14, the behavior for the inverse recovery in the turn-off is shown. The conditions previous to the commutation are  $ID = 5A$  and  $V_{ON} = 2.7V$ . As was observed in the simulation, when the temperature increases, the on-voltage decreases and the inverse current peak increases.

TABLE I  
MAIN PARAMETERS FOR SiC USED IN SIMULATION

Parameter	Symbol	Value
Active area	$A$	0.04cm <sup>2</sup>
Low doping region width	$W_B$	37μm
Life time (300K)	$\tau_n = \tau_p$	0.15μs
Electrons mobility (300K)	$\mu_n$	947cm <sup>2</sup> /V.s
Holes mobility (300K)	$\mu_p$	108 cm <sup>2</sup> /V.s

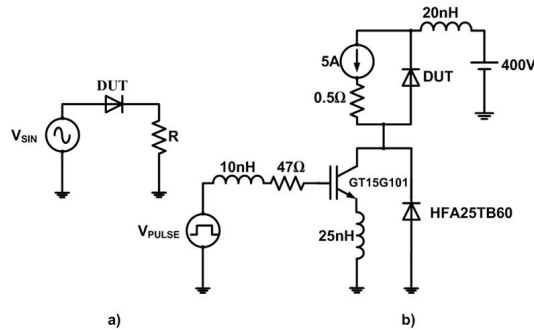


Fig. 10. Test circuit to simulate transient characteristics.

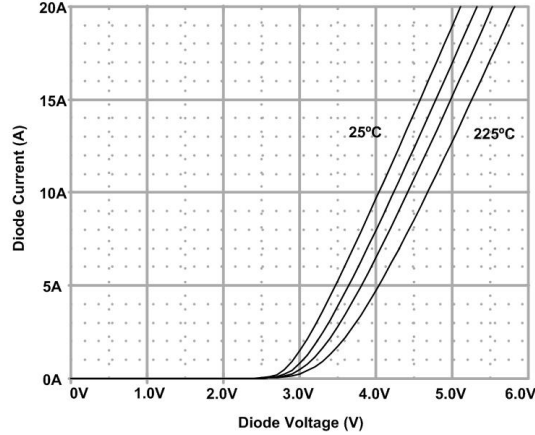


Fig. 11. Simulation result for the on-state of the power diode @ 5A.

In Fig. 15, a comparison between the experimental results reported in [15] and the results for the turn-off of the Pspice-implemented model for the SiC power diode are shown. As can be seen in this comparison, the model predicts the reverse recovery behavior accurately. Finally, Figure 16 shows the behavior of the resistor in the depletion regions ( $R_d$ ) and the resistor in the low doping region ( $R_{ld}$ ). Once the turn-off phase begins ( $t = 323.08\text{ns}$ ) the resistor in the low doping region decreases its value to the order of  $\text{m}\Omega$  and the resistor in the depletion regions increase its values to the order of  $\text{M}\Omega$ .

## VII. CONCLUSIONS

A methodology to solve the ADE from an empirical approximation based on calculating the ambipolar diffusion length, has been proposed. The model proposed in this paper has interesting advantages with respect to other models proposed in the literature, where the accuracy of the simulation results is highly related to the number of parameters used in the model. Simulation results in Pspice have been presented which allow validating the injected charges into the  $N^-$  region and they have been corroborated with the theoretically expected values. The model was simulated for a maximum conduction current of 5A in a temperature range of  $25^\circ\text{C}$  to  $225^\circ\text{C}$ . The model is easy to implement and takes into account the main phenomena that appears in silicon carbide.

## ACKNOWLEDGMENT

This work was supported by COFAA-SIP-I.P.N. and CONACYT.

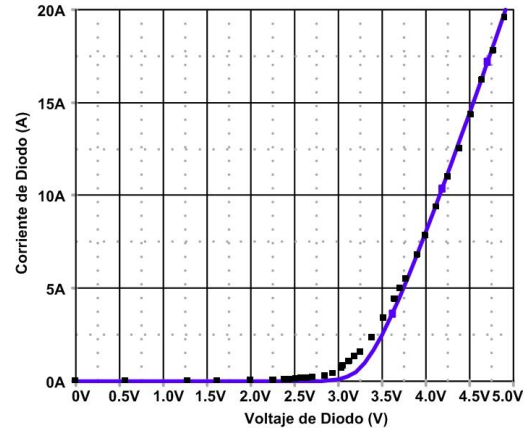
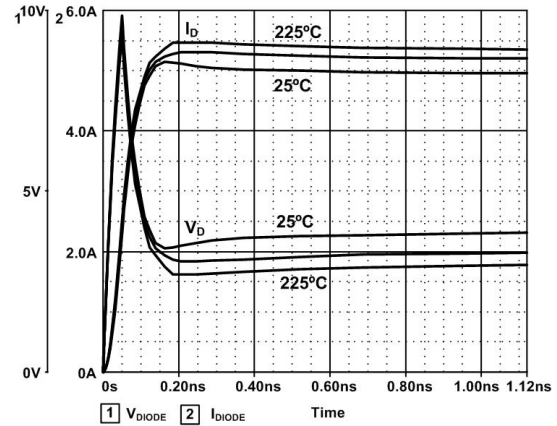
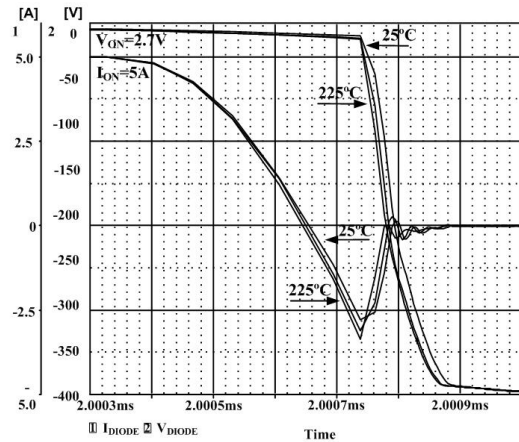


Fig. 12. Measured (Symbols) and model (Lines) on-state characteristics for the 5 A SiC power diode.

Fig. 13. Simulation for the turn-on phase,  $V_D \approx 2.7\text{V}$  and  $I_D \approx 5\text{A}$ .Fig. 14. Waveforms for the reverse recovery of the SiC power diode with  $I_D = 5\text{A}$ .



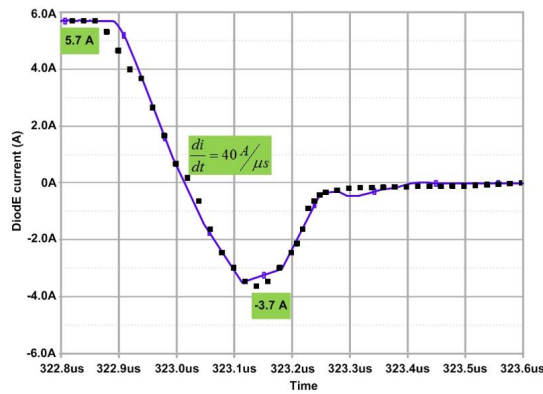


Fig. 15. Measured (Symbols) and model (lines) reverse recovery waveforms of the 5.7A SiC power diode for a  $di/dt = 40A/\mu s$ .

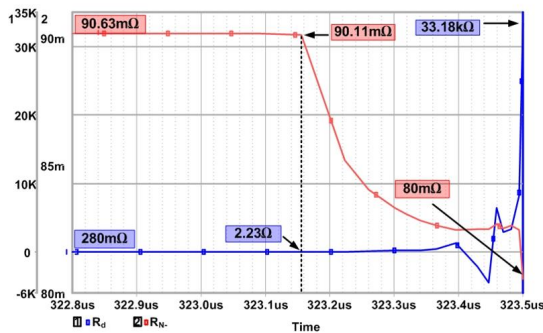


Fig. 16. Behavior of the resistors in the depletion ( $R_d$ ) and low doping regions ( $R_{ld}$ ).

## REFERENCES

- [1] J. L. Hudgins, G. S. Simin, E. Santi, and M. A. Khan, "An assessment of wide bandgap semiconductors for power devices," *IEEE Trans. Power Electron.*, Vol. 18, No.3, pp. 907-914, May 2003.
- [2] M. E. Levinshteina, P. A. Ivanova, M. S. Boltovetsb, V. A. Krivutsab, J. W. Palmour, M. K. Dasc, and Brett A. Hullc, "High-temperature (up to 773 K) operation of 6-kV 4H-SiC junction diodes," *Solid-State Electronics*, Vol. 49, No.7, pp. 1228-1232, Jul. 2005.
- [3] M. Holza, G. Hultschb, T. Scherga, and R. Rupp, "Reliability considerations for recent Infineon SiC diode releases," *Microelectronics Reliability*, Vol. 47, No. 9-11, pp. 1741-1745, Sep./Nov. 2007.
- [4] R. Singh, S.-H. Ryu, D. C. Capell, and J. W. Palmour, "High temperature SiC trench gate p-IGBTs," *IEEE Trans. Electron Devices*, Vol. 50, No. 3, pp. 774-784, May 2003.
- [5] J. H. Zhao, L. Fursin, L. Jiao, X. Li, and T. Burke, "Demonstration of 1789 V, 6.68mΩ-cm<sup>2</sup> 4H-SiC merged- PiN-Schottky diodes," *Electron Devices Letters*, Vol. 40, No. 6, Mar. 2004.
- [6] Y. Sugawara, D. Takayama, K. Asano, A. Agarwal, S. Ryu, J. Palmour, and S. Ogata, "12.7KV Ultra High Voltage SiC Commutated Gate Turn-off Thyristor:SiCGT," *Symposium on Power Semiconductor Devices & ICs*, pp. 365-368, 2004.
- [7] T. Kimoto, H. Kawano, and J. Suda, "1330 V, 67 mΩcm<sup>2</sup> 4H-SiC (0001) RESURF MOSFET," *Electron Devices Letter*, Vol. 26, No.9, pp. 649-651, Sep. 2005.
- [8] P. A. Ivanova, M. E. Levinshteina, J. W. Palmour, M. K. Dasb, and B. A. Hull, "High power 4H-SiC P-i-N diodes (10 kV class) with record high carrier lifetime," *Solid-State Electronics*, Vol. 50, No. 7-8, pp. 1368-1370, Jul./Aug. 2006.
- [9] S.-H. Ryu, S. Krishnaswami, B. Hull, J. Richmond, A. Agarwal, and A. Hefner, "10 kV, 5A 4H-SiC Power DMOSFET," *International Symposium on Power Semiconductor Devices & ICs*, pp. 1-4, 2006.
- [10] J. Zhang, P. Alexandrov, T. Burke, J. H. Zhao, "4H-SiC Power Bipolar Junction Transistor With a Very Low Specific ON-Resistance of 2.9 mΩ cm<sup>2</sup>," *Electron Devices Letter*, Vol. 27, No. 5, pp. 368-370, May 2006.
- [11] M. Su, K. Sheng, Y. Li, Y. Zhang, J. Wu, J. H. Zhao, J. Zhang, and L. X. Li, "430-V 12.4-mΩ cm<sup>2</sup> Normally OFF 4H-SiC Lateral JFET," *Electron Devices Letter*, Vol. 27, No. 10, pp. 834-836, Oct. 2006.
- [12] R. Kraus and H. J. Mattausch, "Status and Trend of Power Semiconductor Device Models for Circuit Simulation," *IEEE Transaction Power Electronics*, Vol. 13, No. 6, pp. 452-465, May 1998.
- [13] T. McNutt, A. Hefner, A. Mantootha, D. Berningb, and R. Singhb "Compact models for silicon carbide power devices," *Solid-State Electronics*, Vol. 48, No. 10-11 pp. 1757-1762, Oct./Nov. 2004.
- [14] M. E. Levinshtein , T. T. Mnatsakanov, P. A. Ivanov, R. Singh, J. W. Palmour, and S. N. Yurkov "Steady-state and transient characteristics of 10 kV 4H-SiC diodes", *Solid-State Electronics*, Vol. 48, No. 5, pp. 807-811, May 2004.
- [15] T. R. McNutt, A. R. Hefner Jr., H. A. Mantootha, J. L. Dulierec, D. W. Berningb, and R. Singh, "Physics-based modeling and characterization for silicon carbide power diodes," *Solid-State Electronics*, Vol. 50, No. 3, pp. 388-398, Mar. 2006.
- [16] M. Loulou, M. Abdelkrim, R. Gharbi, M. Fathallah, C. F. Pirri, E. Tresso, and A. Tartaglia "Modelling and analysis of a-SiC:H p-i-n photodetectors: Effect of hydrogen dilution on dynamic model," *Solid-State Electronics*, Vol. 51, No. 7, pp. 1067-1072, Jul. 2007.
- [17] S. Sze, *Physics of Semiconductor Devices*, John Wiley & Sons, 1981.
- [18] D. A. Neamen; *Semiconductor Physics and Devices*. University of New Mexico, USA. McGraw Hill, 2003.
- [19] A. S. Grove, *Physics and Technology of Semiconductor Devices*, Wiley, pp. 184-185, 1967.
- [20] A. T. Bryant, L. Lu, E. Santi, P. R. Palmer, and J. L. Hudgins, "Physical modeling of fast p-i-n diodes with carrier lifetime zoning, part I: Device model," *IEEE Transaction Power Electronics*, Vol. 23, No. 1, pp. 195-196, Jan. 2008.



**Leobardo Hernández** (M'05) received his B.S. in Electronics Communications Engineering and his M.S. in Microelectronics from the National Polytechnic Institute of México City, México in 1991 and 2001, respectively. He received his Ph.D. in Electronics Engineering in the area of Power Electronics from the National Center for Research and Technological Development (CENIDET), Cuernavaca, Morelos, México in 2009. Since 1992, he has been a full time Professor in the ESIME of the National Polytechnic Institute of Mexico. His research interests include the modelling of power devices, failure detection and characterization.



**Abraham Claudio** (S'96-M'00) received his B.S. and M.S. in Electrical Engineering from the Technological Institute of Laguna, México in 1983 and 1987, respectively. He received his advance studies degree (DEA) and his Ph.D. in Electrical Engineering (Power Electronics) from the National Polytechnic Institute of Grenoble (INPG), France in 1992 and 1995, respectively. Since 1987, he has been with the National Center for Research and Technological Development (CENIDET), Cuernavaca, Morelos, México as a full time Professor in the Power Electronics Group. His research interests include the characterization and modelling of power semiconductor devices, AC motor control, and fault detection.



**Marco A. Rodríguez** (S'09-M'10) received his B.S. in Electronics Engineering from the Technological Institute of Orizaba, Veracruz, México in 1997. He received his M.S. and Ph.D. in Electronics Engineering from the National Center for Research and Technological Development (CENIDET), Cuernavaca, Morelos, México in 2001 and 2009, respectively. Since 2001, he has been with the Autonomous University of Carmen City (UNACAR), Campeche, México as a full time Professor in the Department of Electronics Engineering. His current research interests include electronics circuit systems, the characterization and parameters extraction of power semiconductor devices, and fault detection in motor drives.



**Mario Ponce** was born in San Luis Potosi, S.L.P. on May 24, 1970. He received his B.E. from the San Luis Potosi Autonomous University in 1993. He received his M.S. in 1996 and his Ph.D. in 1999 from CENIDET. Since March 1999 he has been a full time Associate Professor and Researcher at CENIDET. He is Senior Member of the IEEE. He is also an AE of the IEEE Transactions on Power Electronics and he has worked as reviewer for the IEEE Transactions on Power

Electronics, the IEEE Transactions on Industrial Electronics, and the IEEE Transactions on Industry Applications.



**Alejandro Tapia** was born in Mexico City on November 10, 1986. He received his B.S. in Communications and Electronics Engineering from the National Polytechnic Institute of Mexico (IPN) in 2008. He is currently pursuing his M.S. in the Postgraduate and Research Section of the School of Mechanical and Electrical Engineering of the IPN, ESIME Culhuacan, Mexico City. His research interests include resonant converters, soft switching techniques and snubber topologies.