

Evaluation of $1/f$ Noise Characteristics for Si-Based Infrared Detection Materials

Hojun Ryu, Sein Kwon, Sanghoon Cheon, Seong Mok Cho, Woo Seok Yang, and Chang Auck Choi

Silicon antimony films are studied as resistors for uncooled microbolometers. We present the fabrication of silicon films and their alloy films using sputtering and plasma-enhanced chemical vapor deposition. The sputtered silicon antimony films show a low $1/f$ noise level compared to plasma-enhanced chemical vapor deposition (PECVD)-deposited amorphous silicon due to their very fine nanostructure. Material parameter K is controlled using the sputtering conditions to obtain a low $1/f$ noise. The calculation for specific detectivity assuming similar properties of silicon antimony and PECVD amorphous silicon shows that silicon antimony film demonstrates an outstanding value compared with PECVD Si film.

Keywords: Microbolometer, silicon antimony, resistor, noise.

I. Introduction

Infrared rays exist everywhere. Infrared rays were mentioned for the first time in a paper by Herschel in April of 1800 [1]. They affect many phenomena in our surroundings. Many studies have been conducted and many developments have been achieved on the use of infrared rays in various fields including military, medical, and industrial applications. Photon detection infrared sensors were developed in the 20th century. These detectors respond in the middle wavelength infrared range. Despite their high sensitivity, photon detectors require an expensive cryogenic cooling system. In order to respond in the mid-IR range, thermopile-, pyroelectric-, and bolometer-type detectors have competed in the commercial market. These days, the research trends regarding infrared sensors have shifted toward bolometric detection sensors. Among the many types of infrared detectors, uncooled microbolometers have been focused upon for their simple structure and outstanding performance. For a high-performance microbolometer, there has been a great deal of effort in seeking out bolometric materials [2]. Vanadium oxide is a well-known and widely used material for its high temperature coefficient of resistance (TCR) and low noise characteristics. However, it has the disadvantage that it is not suitable for a standard CMOS process. Studies on a-Ge:H or a-Si_xGe_{1-x}:H have been carried out in recent years [3]. Still, there has been no successful commercial use of these materials because of their process control problems. To achieve a high performance microbolometer, $1/f$ noise should be considered. Generally, materials having a high resistance have a high TCR and $1/f$ noise. Plasma-enhanced chemical vapor deposition (PECVD)-deposited amorphous silicon has been known to be a good candidate due to its thermal property and process compatibility. However, $1/f$

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noise is a serious problem for obtaining high detectivity. According to previous studies, sputtered silicon shows lower $1/f$ noise than PECVD-deposited silicon film [4], [5]. Therefore, we have tried to find a new bolometric material for a microbolometer with high TCR, low $1/f$ noise level, and relatively low electrical resistance for the advantage of read-out integrated circuit (ROIC) fabrication. In this work, we have investigated the $1/f$ noise characteristics of new bolometric materials and compared them with the well-known PECVD-deposited amorphous silicon.

II. Experimental Procedure

Silicon antimony thin films were fabricated using a co-sputtering method for a new bolometric material. To regulate the element composition, we maintained independent bias power control. We deposited silicon nitride onto a silicon wafer for electrical insulation, patterned new materials for various shapes, and formed aluminum electrodes on the materials. In the case of amorphous silicon, the resistance is located within a range of a few mega-ohms to tens of mega-ohms. At the point of ROIC fabrication, the lower resistance is more effective, but the TCR is proportional to the logarithm of the resistivity. A compositional analysis was also carried out through Auger electron spectroscopy and was confirmed using Rutherford backscattering spectroscopy (RBS). The microstructure of silicon antimony thin films was analyzed using transmission electron microscopy. TCR values were measured using samples located on a thermo-electric cooler in order to maintain a uniform temperature control via an HP4156 I-V meter. We measured the $1/f$ noise characteristics using an SR 760 dynamic signal analyzer for various shape patterns. Figure 1 shows a schematic diagram of the $1/f$ noise measurement apparatus.

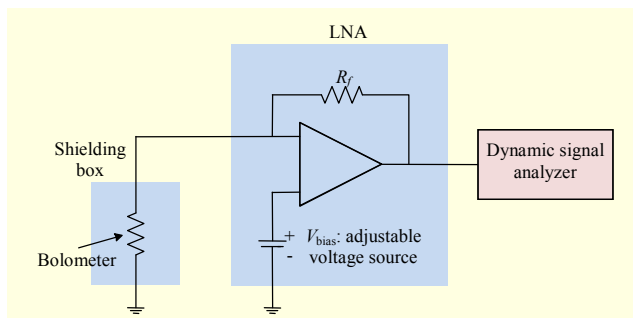


Fig. 1. Diagram of $1/f$ noise characteristic measurement system.

III. Results and Discussion

The optimum composition of a silicon antimony thin film having a high TCR is silicon alloy containing 40% antimony.

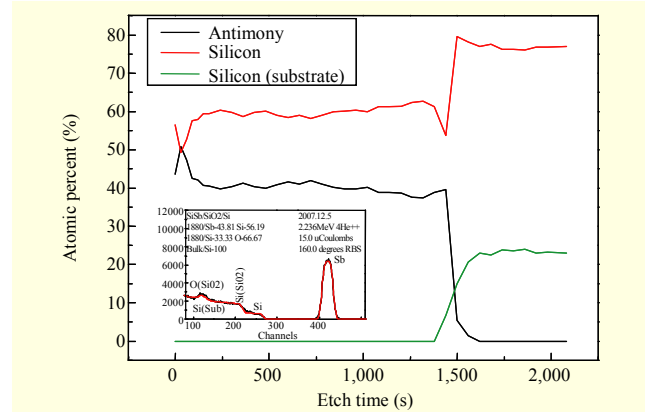
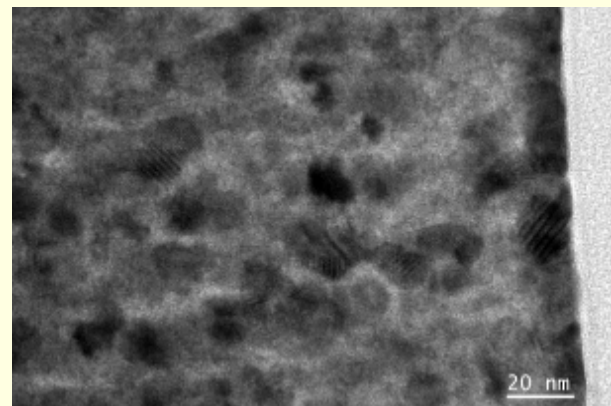
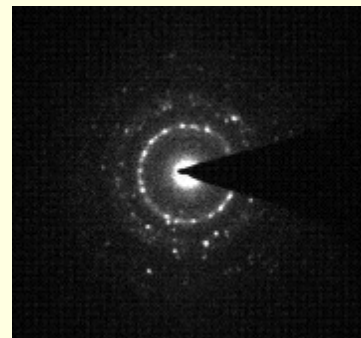


Fig. 2. Compositional analysis of $\text{Si}_{60}\text{Sb}_{40}$ film using AES and RBS.



(a)



(b)

Fig. 3. TEM photos of $\text{Si}_{60}\text{Sb}_{40}$ film: (a) bright field image and (b) diffraction pattern.

As we mentioned previously, the composition was confirmed using RBS, which is shown in Fig. 2. The inset graph shows an RBS spectrum for $\text{Si}_{60}\text{Sb}_{40}$ film, and it matches well with the AES data. The origin of $1/f$ noise is still ambiguous; however, it is known that $1/f$ noise levels increase according to heterogeneity, impurities, and other film defects. Therefore, the microstructure of this silicon antimony film was investigated. Transmission electron microscopy images are shown in Fig. 3.

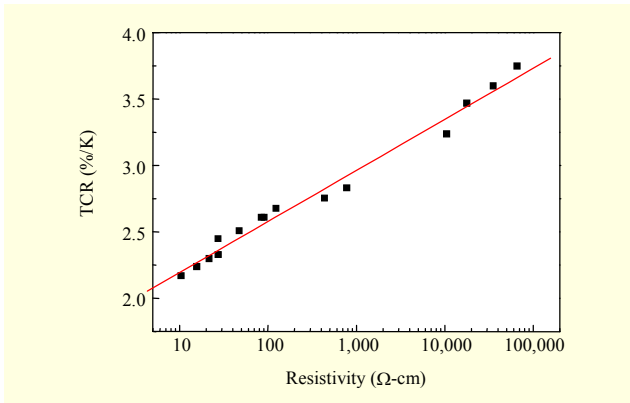


Fig. 4. TCR as a function of resistivity of silicon antimony alloy.

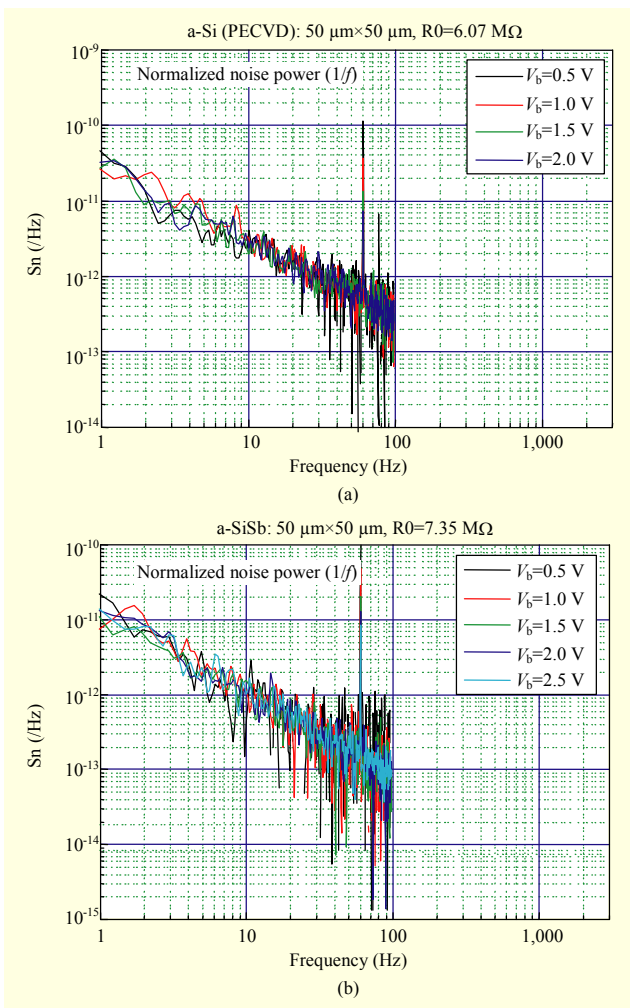


Fig. 5. The $1/f$ noise characteristics of (a) PECVD deposited silicon and (b) sputtered silicon antimony films.

In Fig. 3(a), a nano-size crystalline of a silicon antimony alloy is evenly distributed all over the film matrix. This can reduce the $1/f$ noise level of the new material. The size of the crystalline is under 20 nm, so in the x-ray diffraction patterns,

Table 1. Pattern size and resistivity of silicon antimony film for noise characterization.

	Width (μm)	Length (μm)	Area (μm^2)	Resistance factor
1	50	50,000	2,500,000	1,000
2		5,000	250,000	100
3		500	25,000	10
4		50	2,500	1
5		5	250	0.1
6	500	49,000	24,500,000	98
7		5,000	2,500,000	10
8		500	250,000	1
9		50	25,000	0.1
10	158	15,642	2,471,436	99
11		1,580	249,640	10
12		158	24,967	1
13		15.8	2,496	0.1
14		1,580	1,580	2,496,400
15		158	249,640	0.1

only the broad peaks are identified. In Fig. 3(b), rings in a diffraction pattern can be found. Therefore, silicon antimony thin film consists of finely formed crystalline. Figure 4 shows the TCR characteristics of deposited silicon antimony thin film. The measured TCR values are from 2%/K to 2.5%/K under the 100 $\Omega\text{-cm}$ range. Naturally, the TCR is proportional to the logarithm of resistivity, and materials having a high TCR value show high $1/f$ noise characteristics. Therefore, to obtain high figures of merit for a microbolometer, the high TCR and low $1/f$ noise need to be regulated. The $1/f$ noise characteristics were measured using an SR570 low-noise amplifier (LNA) and an SR760 dynamic signal analyzer as shown in Fig. 1.

Using a dynamic signal analyzer, the noise voltage was measured, and the current spectral density (S_{if}) was obtained from the value of the root mean square of $1/f$ noise voltage divided by the LNA gain. In (1), the relationship between the noise current spectra and $1/f$ noise parameter is given:

$$S_{if} = \frac{KI_b^2}{f^\alpha} (\text{A}^2/\text{Hz}), \quad (1)$$

where K is the $1/f$ noise parameter, and α is the power of f , the measuring frequency, which is usually close to 1. Using (1), the α of sputtered silicon antimony thin film is given as a range from 0.68 to 1.5 according to the frequency change when the bias current is set to 1 ampere for normalization of the compared data. To help explain the effects of the fabrication method of the film, we show the $1/f$ noise characteristics of sputtered silicon antimony and PECVD-deposited amorphous silicon films in Fig. 5. Using the same size area and similar

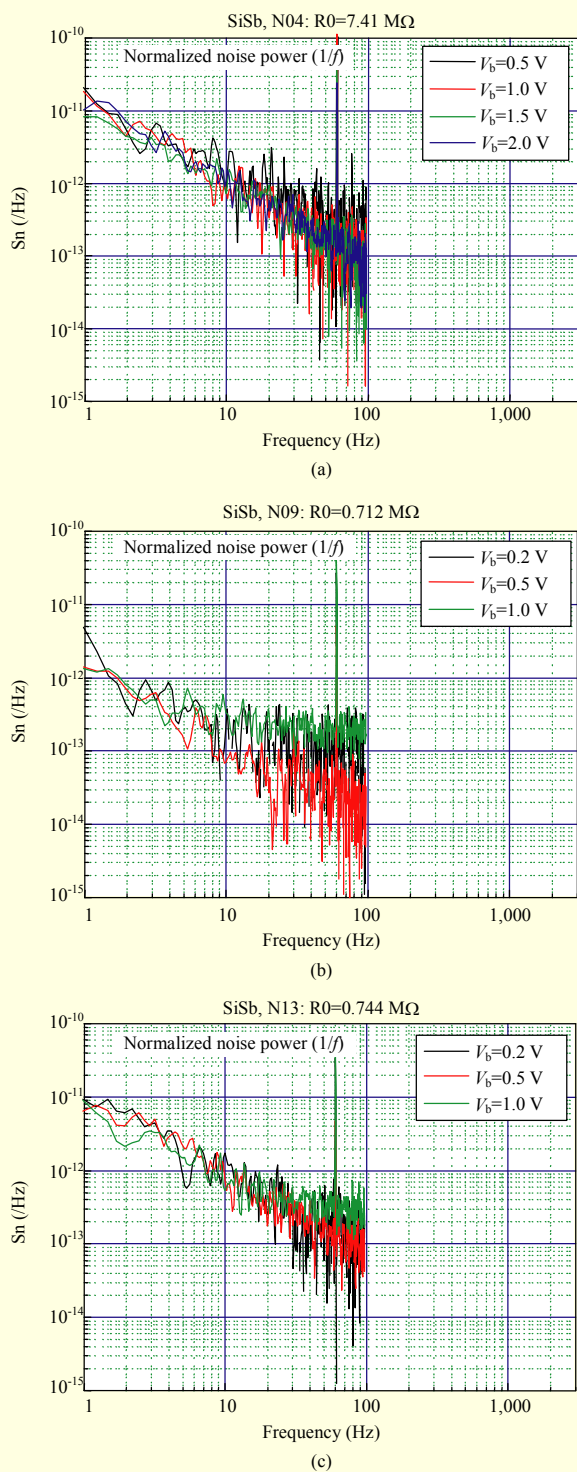


Fig. 6. The $1/f$ noise characteristics of sputtered silicon antimony film under various conditions.

resistivity, the $1/f$ noise level of silicon antimony is lower than that of PECVD-deposited amorphous silicon.

Furthermore, the resistance of silicon antimony is higher than that of PECVD amorphous silicon; therefore, the TCR of

Table 2. Microbolometer parameters for detectivity calculation.

Factor	Value
Emissivity (ϵ)	0.8
TCR (α)	2.24 to 2.73 (%/K)
Pixel area (μm^2)	44×46
Bandwidth	5,000 Hz
Starting time	0.0001 Hz
Thermal conductance (g)	5×10^{-8} (W/K)

silicon antimony is higher than PECVD amorphous silicon because they have same resistance. The $1/f$ noise level can be compared with the $1/f$ noise parameter K , which is proportional to the total noise level. The total $1/f$ noise voltage is given by (2), where I_b is the bias current, R is the resistance, and BW indicates the bandwidth. Thus, the $1/f$ noise is also proportional to $K^{1/2}$. The related equation noise and material parameter K are also shown in (2). The calculated K value changes from 0.4×10^{-10} to 224.9×10^{-10} under a sputtering condition.

$$V_{1/f,\text{rms}} = \sqrt{KI_b^2 R^2 \ln BW} \quad (\text{V}). \quad (2)$$

To compare the effect of volume change, we used sputtered silicon antimony films of various sizes as shown in Table 1. To control the measuring condition, the pixel resistance was changed with area. In this measurement, the resistance of a 50 micron by 50 micron pixel was set to 1, and the other size pixels were normalized by the resistance factor. Samples 4 and 13 were compared in terms of resistivity change. The effect of area change was also compared for samples 4 and 9 under the same resistivity. Comparing the noise levels in Figs. 6(a) and (c), there is no effect of resistivity change. When the total noise is the same as parameter K , and the resistivities have an inverse proportion relationship, a resistivity effect is not considered to exist. According to the nature of $1/f$ noise, there is no volume change effect.

There is only $1/f$ noise increment with a volume change effect in Figs. 6(b) and (c). As samples with well-defined areas, they could have the same resistivity. In this case, the area defined by the etching process is not perfect. Hence, the resistivity change causes a noise level difference between the samples.

The most important figure of merit of a microbolometer is detectivity for real applications. In many cases, the areas of detectors vary with their design rules. To compare the performance of the detectors without the area factor, the specific detectivity, which is considered an area factor, was used. For the compared films, which were fabricated using different deposition methods and adopted for the resistor layer of a microbolometer, the specific detectivity was calculated. In

the calculations, their common environmental conditions were set equally. The compared parameters of the films were set as shown in Table 2. The specific detectivity for devices with normalized area and bandwidth is given by

$$D^* = \frac{R_v \sqrt{A_d \cdot BW}}{V_{n,rms}} = \frac{V_{bias} \alpha \varepsilon \sqrt{A_d \cdot BW}}{g V_{n,rms}} \quad (3)$$

IV. Conclusion

The calculated results of detectivity are shown in Table 3 for PECVD deposited amorphous silicon and sputtered silicon antimony films having a similar TCR. The total noise voltage including $1/f$ noise of the PECVD-deposited film is 20 times higher than that of the sputtered film. Therefore, the detectivity of a device with sputtered silicon antimony as a resistor layer is much better than the detectivity when PECVD-deposited film is used. The new bolometric material for microbolometer application was fabricated using silicon and antimony. The microstructure of the sputtered silicon antimony film has evenly-distributed nano-sized crystalline, which produces good $1/f$ noise characteristics. Comparing the calculated detectivity when using the developed film as a resistor layer, the sputtered silicon antimony shows a better performance for microbolometer applications.

Table 3. Calculated detectivity of sputtered silicon antimony and PECVD-deposited silicon films.

	$V_{n,rms}$ (μ V)	TCR (%/K)	D^* ($\text{cmH}^{1/2}/\text{W}$)
PECVD	1656.8	-2.2	2.82E8
Sputter	89.2	-2.24	5.32E9

References

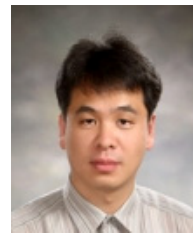
- [1] W. Herschel, "Experiments on the Refrangibility of the Invisible Rays of the Sun," *Phil. Trans. Roy. Soc. of London*, vol. 90, 1800, pp. 284-292.
- [2] P.L. Richards, "Bolometers for Infrared and Millimeter Waves," *J. Appl. Phys.*, vol. 76, no. 1, 1994, pp. 1-24.
- [3] M. Garcia et al., "IR Bolometers Based on Amorphous Silicon Germanium Alloys," *J. Non-Cryst. Solids.*, vol. 338-340, 2004, pp. 744-748.
- [4] M.H. Unewisse et al., "Growth and Properties of Semiconductor Bolometers for Infrared Detection," *Proc. SPIE*, vol. 2554, 1995, pp. 43-54.
- [5] P.W. Kruse and D.D. Skatrud, *Uncooled Infrared Imaging Arrays and Systems*, Academic Press, San Diego, 1997.



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