A Frequency Domain Equalization Algorithm for Fast Time-Varying Fading Channels

Le-Nam Tran, Een-Kee Hong, and Huaping Liu

Abstract: Conventional frequency domain equalization (FDE) schemes were originally devised for quasi-static channels. Thus, such equalization schemes could suffer from significant performance degradation in fast-fading channels. This paper proposes a frequency domain equalization algorithm to mitigate the effect of fast time-varying fading. First, a mathematical expression is derived to quantify the total interference resulting from the time variation of the channel. Then, the proposed approach attempts to eliminate the effect of time-variations of the channel. This cancellation allows efficient use of the classical FDE structures in fast time-varying fading environments, although they are built upon the quasi-static channel model. Simulation results of bit-error-rate performance are provided to demonstrate the effectiveness of the proposed algorithm.

Index Terms: Cancellation, direct sequence code division multiple access (DS-CDMA), fast time-varying fading channels, frequency domain equalization (FDE).

I. INTRODUCTION

The next-generation mobile communication systems are expected to support high data rates that usually require a bandwidth larger than the coherence bandwidth of the channel, resulting severe frequency-selective fading channels. In turn, the intersymbol interference (ISI) caused by severe frequency selectivity of the channel could limit the maximum transmission rate. As the time dispersion of the radio channel spans over a number of symbol durations, the design for high-speed reliable communications becomes challenging since the traditional antimultipath approaches such as multipath-diversity combining become inefficient to recover the transmitted signals [1].

To cope with severe frequency selectivity, the entire signal bandwidth could be divided into multiple sub-bands, each of which is narrow enough to assume a frequency-flat fading. This concept leads to the multi-carrier techniques, in particular, the orthogonal frequency-division multiplexing (OFDM) technique

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L.-N. Tran and E.-K. Hong are with the School of Electronics and Information, Kyung Hee University, South Korea, email: {nltran, ekhong}@khu.ac.kr.

H. Liu is with the School of Electronic Engineering and Computer Science, Oregon State University, U.S.A., email: hliu@eecs.oregonstate.edu.

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[2]. However, OFDM also has many disadvantages such as a high sensitivity to frequency offset, and to nonlinear distortion which requires expensive linear amplifiers due to the high peakto-average-power ratio (PAPR) of the transmitted signals.

An alternative solution to deal with frequency selectivity is to equalize the received signals. Equalization could be accomplished in either the time domain or the frequency domain. Generally, the classical time domain equalization (TDE) has a high computational complexity if the channel delay spread is large. The complexity of TDE can be lowered by transforming its operation in time domain to frequency domain. Although the concept of frequency domain equalization (FDE) appeared almost three decades ago [3], single-carrier frequency domain equalization (SC-FDE) has been investigated only recently as a promising approach for broadband wireless communications [4].

Basically, FDE was developed based on the correspondence between the circular convolution of two sequences in the time domain and the point-wise product of their DFT outputs in the frequency domain [5]. If the transmitted data are designed to be periodic, the received signals are the circular convolution between the transmitted signal and the channel impulse response (CIR). To maintain circular convolution, in addition to the cyclic data transmission format, the channel must be quasi-static, that is, the path gains are constant over a block interval. Most of present FDE structures are based on the quasi-static fading channel. The problem of FDE in time varying channels has been treated sparsely in different directions. For example, using the basis extension model (BEM), the authors in [6] presented iterative FDE with widely linear filtering. In [7], the windowing technique was utilized to transform the channel matrix into a banded form. Based on this simplified model, the iterative equalization was applied. However, the optimal window design is compli-

In this paper, rather than simplifying the channel matrix for efficient matrix inversion, we propose an equalization algorithm to mitigate the effect of time variations in fast time-varying fading channels. First, we derive a mathematical model to characterize the interference caused by time-variations of the channel. As shown later, this interference depends on both the transmitted data and CIR. Thus, the suppression of this interference is only possible if the transmitted data and CIR are available at the receiver. In practice, the channel can be estimated, typically by using the transmitted pilot signals, but the transmitted data cannot be known in advance. Hence, our equalization algorithm is performed through several steps. In the first step, the tentative estimates of the transmitted data are obtained by using the conventional FDE. From the second step, the interference caused by the time-variation of the channel is eliminated based on tentative estimates from the first step and the estimated CIR. Conventional FDE is then applied again to the resulting signals. The proposed method can be applied to any conventional communication systems, but we will focus on direct sequence code division multiple access (DS-CDMA) systems in our analysis and numerical examples as a study case.

The rest of this paper is organized as follows. Section II briefly reviews the background of FDE and their applications to DS-CDMA systems. The analysis of the effect of time-varying multipath fading channels on FDE and description of the proposed equalization algorithm are presented in Section III. The performances of our proposed approach are evaluated with computer simulations in Section IV, followed by conclusions in Section V.

II. BACKGROUND OF FREQUENCY DOMAIN EQUALIZATION

FDE has been investigated as one of the promising approaches for the CDMA-based 3G systems and beyond [8], [9] since the RAKE combining technique [1] exhibits poor performance in severe frequency selective fading channels. Before analyzing FDE in time-varying channels, we first briefly review the conventional FDE for quasi-static channels. For simplicity and convenient illustration purpose, we are interested in the frequency domain liner equalization for DS-CDMA systems as an example. It means that the proposed equalization algorithm which will be described in the next section is applicable to all wireless communications systems, including other equalization structures (e.g., hybrid decision feedback equalization (HDFE) [5], [10], [11]) and non-spread spectrum systems.

A. Frequency Domain Linear Equalization (FD-LE) for DS-CDMA Systems

The general FD-LE for DS-CDMA applications is illustrated in Fig. 1. At the transmitter, a block of M data modulated symbols $\{d_n\}$ is spread by a spreading sequence c_m with a processing gain SF to produce the spread signals s_n . Then, a known sequence of L chips is added to the spread signals to form a transmitted data block of size $P = M \times SF + L$ chips. This data extension known as unique word (UW) extension [5] is to make the transmitted data periodic, and thus the channel matrix become circulant. Another type of data extension that can play the same role is cyclic prefix extension [10]. However, in this paper, we employ UW extension because it is beneficial for channel estimation. The received signals are written as

$$r_n = \sum_{l=0}^{\rho-1} h_l s_{n-l} + \eta_n, \ n = 0, 1, \dots, P - 1$$
 (1)

where h_l , $l=0,1,\cdots \rho-1$ denotes the fading coefficient of the (l+1)th path, η_n is the zero-mean complex white Gaussian noise with variance σ_n^2 . In (1), the channel is assumed to be quasistatic (block fading model). Under the condition that $L\geq \rho$, we can express the frequency domain equivalence of (1) as

$$R_k = H_k S_k + N_k, \ k = 0, 1, \dots, P - 1$$
 (2)

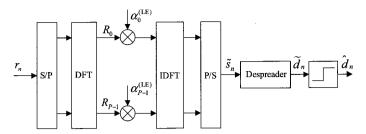


Fig. 1. FD-LE for DS-CDMA systems.

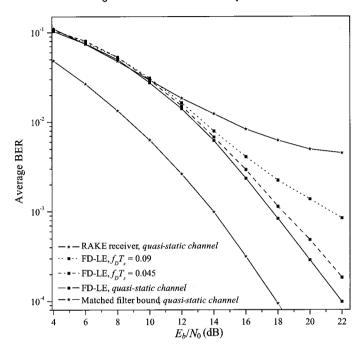


Fig. 2. Performance of FD-LE in quasi-static and time-varying fading channels.

where

$$R_{k} = \sum_{l=0}^{P-1} r_{n} e^{-j2\pi k n/P},$$

$$H_{k} = \sum_{l=0}^{\rho-1} h_{l} e^{-j2\pi k l/P},$$

$$N_{k} = \sum_{l=0}^{P-1} \eta_{n} e^{-j2\pi k n/P}$$

are the results of applying a *P*-point DFT to the received signals, CIR and additive noise, respectively. It is important to mention that (2) holds only in quasi-static channels.

The P-point DFT outputs of the received signals $\{R_k\}$ are weighted with linear equalizer coefficients $\{\alpha_k^{(\mathrm{LE})}\}$. The resulting signals are transformed back into the time domain by a P-point IDFT block. If the minimum mean-square error (MMSE) criterion is used, the FD-LE coefficients can be easily found as [8], [9]

$$\alpha_k^{(\text{LE})} = \frac{H_k^*}{|H_k|^2 + \sigma_n^2}, \ k = 0, 1, \dots, P - 1.$$
 (3)

The equalized chips \tilde{s}_n are passed through the despreader, followed by a threshold detector to detect the transmitted data.

B. Impact of Channel Variations on Performance of FDE

In this section, we provide the simulation results obtained with the FD-LE structure to demonstrate the impact of channel time-variations on the BER performance when the user speed increases. The detailed simulation parameters are given in Section IV.

Fig. 2 shows the BER of various anti-multipath approaches for quasi-static and time-varying channels. For RAKE combining, we consider RAKE combiner of 2 fingers. In quasi-static fading channels, while RAKE combining technique is not reliable due to the severe frequency selectivity of the channel, FD-LE achieves excellent performance. It is observed that the performances of FD-LE in time-varying channels are significantly deteriorated as the user speed increases. When the user speed v is 60 km/h, the maximum Doppler spread f_D is 167 Hz, producing a normalized Doppler frequency change (defined as the product of the block duration T_{block} and Doppler frequency f_D) of 0.045. This degree of mobility represents a slowly timevarying channel. Thus, the traditional FDE is still efficient to combat multipath distortion. However, the BER performance exhibits an error floor as v reaches to 120 km/h. In this case, the maximum Doppler spread is doubled and the normalized Doppler frequency becomes 0.09. This channel can be viewed as a fast time-varying channel [12], and the path gains are not constant over a data block.

III. PROPOSED FDE ALGORITHM FOR TIME-VARYING FADING CHANNELS

The FD-LE [8], [9] presented in Section II and many other traditional FDE structures (e.g., HDFE in [5], [10], [11]) were developed based on the assumption of quasi-static channel. When the data block duration is longer than the channel coherence time, the channel gain cannot be considered constant over one block. Consequently, FDE algorithms derived based on (2) will suffer from significant performance degradation in fast time-varying channels.

For OFDM systems, time-varying fading causes a loss of subcarrier orthogonality, which results in inter-carrier interference (ICI). While the impact of ICI caused by channel variations on the performance of OFDM systems has been studied extensively [12]–[17], data transmission in time-varying channels with FDE has received limited attention. In this section, we mathematically analyze the effect of channel time-variations on the performance of FDE schemes. Based on such analysis, we develop a equalization scheme to improve the performance of FDE in time-varying channels.

A. Analysis of the Impact of Time-Varying Fading Channels on Classical FDE

For multipath time-varying channel model, the received signal is written by

$$r_n = \sum_{l=0}^{\rho-1} h_l(n) s_{n-l} + \eta_n. \tag{4}$$

An P-point DFT is applied to $\{r_n\}$ to transform the received signals into the frequency domain. To simplify notation, we only

consider the mathematical expression for a certain kth frequency bin of the received signal in our analysis, which is given by

$$R_{k} = \sum_{n=0}^{P-1} r_{n} e^{-j2\pi kn/P}$$

$$= \sum_{n=0}^{P-1} \sum_{l=0}^{\rho-1} h_{l}(n) s_{n-l} e^{-j2\pi kn/P} + \sum_{n=0}^{P-1} \eta_{n} e^{-j2\pi kn/P}.(5)$$

For each block, we can decompose $h_l(n)$ into two components, a time-invariant part and a time-variant part, as

$$h_l(n) = \omega_l + \psi_l(n) \tag{6}$$

where

• ω_l denotes the average channel gain of the lth path within one block. This term represents the constant (time-invariant) component of CIR in one data block. If P is the block size, ω_l can be written by

$$\omega_l = \frac{\sum_{n=1}^{P} h_l(n)}{P}.$$
 (7)

• $\psi_l(n)$ is the difference between channel coefficient at time n and the average value ω_l of the lth path. This value measures the deviation of the channel coefficient at time n from the average value ω_l . In other words, $\psi_l(n)$ measures the degree of change of the CIR. Note that, $\psi_l(n)$ can be computed using (6) as $\psi_l(n) = h_l(n) - \omega_l$ if the estimates of $h_l(n)$ are available.

From (5) and (6), the received signal in the frequency domain can be written by

$$R_{k} = \sum_{n=0}^{P-1} \sum_{l=0}^{\rho-1} \{\omega_{l} s_{n-l} + \eta_{n}\} e^{-j2\pi kn/P} + \sum_{n=0}^{P-1} \sum_{l=0}^{\rho-1} \psi_{l}(n) s_{n-l} e^{-j2\pi kn/P}.$$
(8)

The first term of the right-hand side of (8), Ω_k , can be viewed as the frequency response of the received signal in quasi-static channels. According to (2), we can simplify Ω_k as

$$\Omega_k = H_k S_k + N_k, \ k = 0, 1, \dots, P - 1$$
 (9)

where $H_k=\sum_{l=0}^{\rho-1}\omega_le^{-j2\pi kl/P}$ and $N_k=\sum_{n=0}^{P-1}\eta_ne^{-j2\pi kn/P}$ are the DFTs of the average value of CIR and additive noise, respectively. Let us define

$$\Psi_k^{(l)} = \sum_{n=0}^{P-1} \psi_l(n) s_{n-l} e^{-j2\pi kn/P}$$
 (10)

which is the DFT of the product of the two sequences $\{\psi_l(n)\}$ and $\{s_{n-l}\}$. The second term Δ_k in (8) can be written by

$$\Delta_k = \sum_{l=0}^{\rho-1} \sum_{n=0}^{P-1} \psi_l(n) s_{n-l} e^{-j2\pi kn/P} = \sum_{l=0}^{\rho-1} \Psi_k^{(l)}$$
 (11)

resulting in a more compact form of (5) which is expressed as

$$R_{k} = \underbrace{H_{k}S_{k} + N_{k}}_{\Omega_{k}} + \underbrace{\sum_{l=0}^{\rho-1} \Psi_{k}^{(l)}}_{\Delta_{k}}, \ k = 0, 1, \dots, P-1.$$
 (12)

In (12), Δ_k is considered as the interference resulting from the channel variation. If the channel is assumed to be constant within one block period (i.e., $\psi_l(n)=0, \, \forall n$), Δ_k will vanish. For time-varying channels, Δ_k destroys the corresponding equivalency between circular convolution in the time domain and multiplication in the frequency domain, and thus degrades the performance of FDE. The value of Δ_k is dependent on the severity of the channel fluctuation; Δ_k increases as the mobile speed increases.

Our goal is to eliminate this interference, but this requires a priori knowledge of the transmitted signals s_n , as well as the channel coefficients. In the following, we propose an equalization algorithm which is accomplished through several steps to reduce the effect of time variations of the channel.

B. The Proposed FDE Algorithm

For time-varying channels, FDE is not conceptually applicable due to the presence of Δ_k in (12), since the frequency response of the received signal is no longer equal to the point-wise product of the frequency responses of the transmitted signal and the CIR. It is desirable to cancel Δ_k so that FDE can be efficiently exploited. From (11), the computation of Δ_k requires the followings:

• The values of $\psi_l(n)$: These values can be obtained through channel estimation which generates estimates $\hat{h}_l(n)$. An estimate of $\psi_l(n)$ is then given by

$$\hat{\psi}_l(n) = \hat{h}_l(n) - \hat{\omega}_l \tag{13}$$

where the average channel gain $\hat{\omega}_l$ can be estimated using (7).

• The values of the transmitted sequence s_{n-l} : In practice, these values are not known at the receiver in advance. However, we can make use of the tentative signal estimates that are obtained by applying conventional FDE algorithms to the received signals over time-varying channel, ignoring the effect of time-variant components.

Our proposed method is carried out through several steps as follows:

- Step 1: Apply FDE for time-varying channels to yield tentative symbol estimates.
 - Any existing FDE structures (e.g., FD-LE in Fig. 1 or HDFE in [11]) can be employed.
- Step 2: Compute $\hat{\Delta}_k$ from the tentative symbol estimates obtained in step 1.

$$\hat{\Psi}_{k}^{(l)} = \sum_{n=0}^{P-1} \hat{\psi}_{l}(n)\hat{s}_{n-l}e^{-j2\pi kn/P}, \ l = 0, 1, \dots, \rho - 1$$

$$\hat{\Delta}_{k} = \sum_{l=0}^{\rho-1} \hat{\Psi}_{k}^{(l)}.$$
(1)

(14)

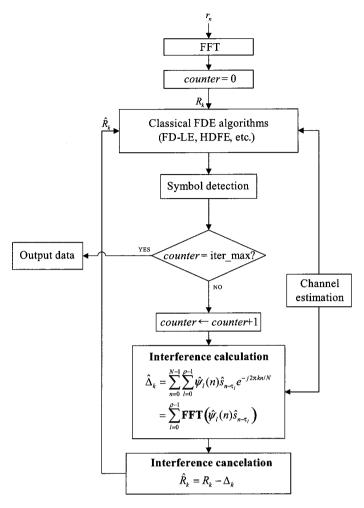


Fig. 3. Signal processing flow of proposed FDE for time-varying channels.

• Step 3: Subtract $\hat{\Delta}_k$ from R_k in (12), which yields

$$\hat{R}_k = R_k - \hat{\Delta}_k. \tag{15}$$

If the estimate of Δ_k is perfect, \hat{R}_k is equal to Ω_k . Since perfect data detection in step 2 is impossible, only part of the total interference will be removed.

• Step 4: Apply FDE to \hat{R}_k computed in step 3 to obtain symbol estimates again.

This step is very similar to step 1. Because the impact of Δ_k is partially reduced, better symbol estimates can be achieved. Therefore, the overall performance of FDE is improved. The signal processing flow of steps from 1 to 4 is depicted in Fig. 3.

We also notice that the number of phases in the proposed algorithm can be increased by applying the *turbo principle* [18]. Namely, steps 2 to 4 can be repeatedly carried out to further enhance the performance. The method used in [19] can be helpful to account for the improved reliability of signal detection after each phase of the proposed algorithm. However, such a method requires high computational complexity, and thus is not considered in this paper.

Table 1. Taped-delay-line parameters for vehicular environment.

Tap	Delay (ns)	Average power	Doppler spectrum
1	0	-2.5	Classic
2	271	0	Classic
3	8943	-12.8	Classic
4	13008	-10.0	Classic
5	17073	-25.2	Classic
6	20054	-16.0	Classic

Table 2. Simulation parameters.

Parameters	Selected values	
FFT size	1024	
Spreading factor	16	
Modulation	QPSK	
Carrier frequency	3 GHz	

IV. PERFORMANCE COMPARISON

In this section, we evaluate the performance of the proposed algorithm for FDE in time-varying channels to demonstrate the effectiveness of the proposed algorithm to mitigate the effect of fast time-varying fading. We also examine the robustness of the proposed algorithm with practical channel estimation. Although we focus on DS-CDMA applications as a case study, the proposed algorithm can be applied to most wireless communication systems.

A. System Parameters

The channel B of the vehicular environment as described in Recommendation ITU-R M.1225 [20] is considered. The chip rate is selected to be 3.6864 Mbps as in CDMA2000. The delay between multipaths is assumed to be an integer multiple of a chip interval (1/3.6864 Mcps $\approx 271~\rm ns$), so a modified version of the channel model is used. The power delay profile for the six independent paths of the modified channel B is listed in Table 1.

We consider the performance of an uncoded DS-CDMA system using QPSK modulation. A 16-ary Walsh code is used for spreading. The FFT size is 1024; thus the number of symbols (including unique word extension) in one transmission block is 1024/16=64 symbols. Consequently, the block duration is $T_{\rm block}=0.28$ ms. The maximum Doppler shift is calculated assuming a carrier frequency of 3 GHz. Simulation parameters are summarized in Table 2. The channel simulator is built based on the JTC channel model in [21].

B. Simulation Results with Ideal Channel Estimation

The UW length for FDE is required to be longer than the length of the CIR. The modified channel model in Table 1 has an excess delay spanning 20054/271 = 74 chips. The UW of 80 chips selected from a PN sequence is inserted into each data block. The performances of FD-LE combined with the proposed equalization algorithm and the conventional FD-LE (i.e., FD-LE without canceling the effect of channel variation) are compared in Fig. 4.

In Fig. 4, the legend 'no cancellation' denotes the system performance when the conventional FD-LE is applied to time-varying channels, and 'practical cancellation' represents the

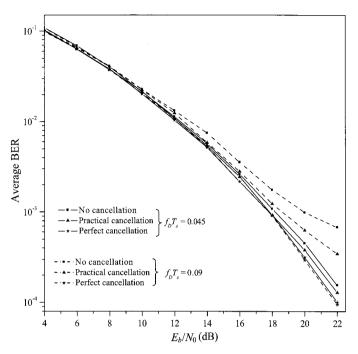


Fig. 4. Performance of the proposed algorithm applied to FD-LE with ideal channel estimation, ${\sf UW}=80$ chips.

performance of the proposed algorithm scheme where the cancellation is performed with practical data detection (detected data from the first step). For comparison purpose, we also consider the ideal case labeled by 'perfect cancellation,' in which the channel coefficients and the transmitted data are known a priori at the receiver. In this ideal case, the interference term can be reconstructed accurately and cancelled perfectly. Accordingly, we can infer from Fig. 4 that the performance of the proposed FDE in this ideal case is independent of Doppler frequency, and is equal to that of traditional FDE applied to quasi-static channel. It is also noted that a higher performance gain of the proposed equalization over the conventional FD-LE can be achieved as the user mobility increases and the proposed algorithm can eliminate the error floor. When the normalized Doppler frequency is 0.09, a performance gain of about 2.2 dB is observed between between 'no cancellation' and 'practical cancellation' at the BER = 7×10^{-4} .

C. Simulation Results with Practical Channel Estimation

C.1 Channel Estimation

In the proposed algorithm, the channel gain (fading coefficient) at every data symbols within one block must be known. An additional benefit of UW is that it can be used as pilot signals to estimate the channel. The time-varying channel estimation in our work is performed through two steps. First, the channel gains at UW positions are estimated using the multi-stage serial interference cancellation approach (M-SIC) as described in [22]. Second, the channel gains at the data field are obtained by interpolating the estimated channel coefficients at the UW positions. To ensure an accurate estimation using the M-SIC approach, the UW length is increased to 160 chips (a little more than twice of the channel length) at the cost of a lower bandwidth efficiency. A comprehensive study of interpolation method for channel es-

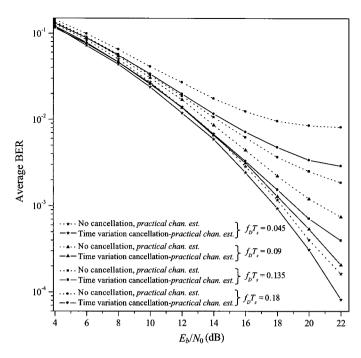


Fig. 5. Performance of the proposed algorithm applied to FD-LE with practical channel estimation (UW = 160 chips), and different degrees of mobility.

timation can be found in [23]. When computing the channel estimates at the data field of each block, we use the estimated channel gains at the UW position of the previous and current blocks. The estimated channel gains at the data fields are given by using a simplified *sinc* interpolation as below

$$\hat{h}(l,n) = \sum_{i=1}^{2} \hat{h}(l,n_i) \frac{\sin(2\pi f_D T_c(n_i - n))}{2\pi f_D T_c(n_i - n)}$$
(16)

where T_c is the chip duration, and f_D is the maximum Doppler frequency. For the mobile speed estimation, the simple method in [24] can be employed. In this paper, the value of mobile speed is assumed to be known at the receiver. In (16), $\hat{h}(l,n_i)$ is the estimated channel gain of the lth path at the UW portion, n_i indicates the center position of the ith UW, and n is the position of each chip in the block.

C.2 Performance of the Proposed Algorithm with Practical Channel Estimation in Time Varying Channel

Fig. 5 plots the BER performances of the proposed equalization algorithm with practical channel estimation in time varying channels. Several degrees of mobility are considered where the normalized Doppler frequency is up to $0.18~(v=240~{\rm km/h})$. It is observed that the proposed algorithm provides a remarkable gain over the traditional method with practical channel estimation in high mobility scenarios.

C.3 Impact of Spreading Factor

In Fig. 6, we evaluate the proposed equalization algorithm with different values of spreading factor (SF) to investigate the effect of SF on the proposed algorithm. The normalized Doppler frequency is set to be $0.09 \ (v=120 \ \text{km/h})$. Although our approach always outperforms the traditional FDE scheme, its per-

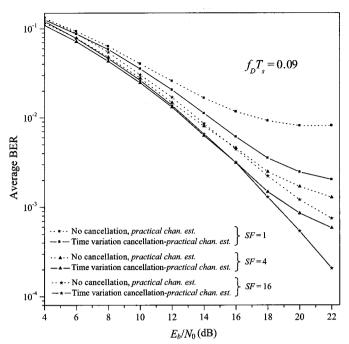


Fig. 6. Performance of the proposed algorithm applied to FD-LE with practical channel estimation ($f_DT_s=0.09$, UW = 160 chips), and different values of spreading factor (SF).

formance is degraged as the spreading factor becomes small. This is understandable and can be explained as follows. For FDE, the equalization is performed in frequency domain and detection is performed in time domain. The interference expressed in (11) depends on time-varying coefficients and detected chips. The accuracy of detected chips in time domain detection relies on the spreading factor. Namely, a small value of SF results in less accuracy of detected chip, and thus the interference is not correctly recovered. This eventually causes a performance degradation on the performance of the proposed method.

V. CONCLUSIONS

We have proposed an effective algorithm to cancel the time-variant component of the received signals for robust operation of SC-FDE over fast time-varying fading channels. We first derived the mathematical expressions to model the effect of channel time-variations on the performance of FDE and then provided the multi-step equalization scheme. The proposed scheme improves the performance of conventional FDE by canceling the effect of fast time-varying fading channel. In the proposed scheme, the tentative data estimates are obtained with the conventional FDE and then the interference term caused by the time-variation of the channel is quantified and canceled, followed by further equalization on the signal after interference cancellation.

To demonstrate the robustness of our proposed algorithm, we have evaluated the performance of the proposed interference cancellation scheme combined with the existing FD-LE scheme in different time-varying fading channels by computer simulations, where the performance with perfect interference cancellation is used as the baseline performance. For relatively high Doppler spreads, the interference cancellation method can pro-

vide about 2.2 dB gain over the traditional FD-LE algorithms without the cancellation of time variation components at a BER of 7×10^{-4} . The multi-step FDE algorithm proposed in this paper is a promising solution to achieve robust performance in time varying fading environments since it can reduce the irreducible error floor that appears in traditional FDE schemes.

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Le-Nam Tran received the B.S. degree in Electrical Engineering from Ho Chi Minh National University of Technology, Vietnam in 2003. Since 2004, he has been supported by "Korean Government IT Scholarship Program" to follow the M.S. degree in Kyung Hee University, Kyunggi, Korea, and received his M.S. Electrical Engineering in 2006. He is working toward the Ph.D. degree in electrical engineering at the same university. He received the Best Paper Award from IITA in August 2005. His current interests include mobile communication systems, equalization

techniques, spread spectrum and CDMA schemes, multiuser MIMO systems.



Een-Kee Hong received the B.S., M.S., and Ph.D. degrees, all in electrical engineering, from Yonsei University, in 1989, 1991, and 1995, respectively. He was senior research engineer at SK Telecom from September 1995 to February 1999 and visiting senior engineer at NTT DoCoMo from October 1997 to December 1998. He is currently Associate Professor at Kyung Hee University, South Korea. His research interests are in physical layer in wireless communication, spectrum engineering, and cross-layer optimization. He was awarded the Best Paper Award, Institute

of Information Technology Assessment, and Haedong Best Paper Award, KICS in 2005.



Huaping Liu received his B.S. and M.S. degrees from Nanjing University of Posts and Telecommunications, Nanjing, P. R. China, in 1987 and 1990, respectively, and his Ph.D. degree from New Jersey Institute of Technology, Newark, NJ, in 1997, all in electrical engineering. From July 1997 to August 2001, he was with Lucent Technologies, New Jersey. He joined the School of Electrical Engineering and Computer Science at Oregon State University in September 2001, and is currently an Associate Professor. His research interests include ultra-wideband

schemes, MIMO OFDM systems, communication techniques for multiuser time-varying environments with applications to cellular and indoor wireless communications. He currently serves as an Associate Editor for the IEEE Transactions on Vehicular Technology and IEEE Communications Letters.