# Electrode Thickness Optimization at Full Color OLED and Analysis of **Power Consumption**

# Sung-Joon Park\*, Ok-Tae Kim\*\* and Hee-Je Kim\*

Abstract - The operating condition of the OLED (organic light-emitting diode) is very sensitive to electrode thickness properties. The electrode thickness is a significant issue in the construction of OLEDs because of its transparency, high conductivity and high efficiency as an injector into organic materials. We carried out a systematic study to optimize the electrode thickness conditions in Indiumtin oxide (ITO), Molybdenum (Mo) and Aluminum (Al). Further, we measured electrode thickness under standard conditions [ITO 1500 Å, Mo 2600 Å, Al 1500 Å]. We also evaluated power consumption. In addition, we analyzed substrate uniformity with IVL measurement results. From these results, it is known that the electrode thickness should be optimized in order to accomplish optimal power efficiency.

Keywords: Electrode thickness, organic light-emitting diode, power consumption

### 1. Introduction

Nowadays there is a great interest in OLEDs (organic light-emitting diode) since their performance remarkably improved for application in flat panel display since 1987[1].

OLEDs have been shown to have sufficient brightness, range of color and operating lifetimes for use as a practical alternative technology to the LCD based full-color flatpanel display. Furthermore, OLEDs also have rapid response time, enabling the addressing of a simple matrix to be effectively used even for displaying fast moving pictures. Yet, each pixel current for driving and full-color problems still exist.

The ideal OLED must have lower power, higher uniformity and higher contrast ratio in order for the full color to be used effectively. The basic structure of the OLED, shown in Fig. 1, has not changed substantially since the initial demonstration of its high efficiency and low voltage electro-luminescence (EL) from an organic thin film.

A multi-layer structure consisting of a hole transport layer, emission layer and electron transport layer allows the achievement of bright electro-luminescent emission in the visible spectral region at low driving voltages. Fig. 2 describes the so-called barrier ribs in the panel structure [2]. Each layer was deposited by the vacuum evaporation method under a vacuum of  $< 10^{-5}$  Torr.

Received February 9, 2004; Accepted May 4, 2004

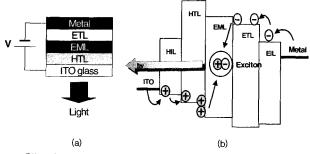


Fig. 1 Typical device architecture of an OLED

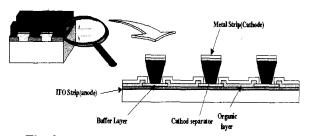


Fig. 2 Cross-sectional view of a passive OLED

The first layer consists of a hole transporting material such as N, N'-diphenyl-N, N-bis (3-methylphenyl)1-1'biphenyl-4, 4'-diamine (TPD), and the second of a lightemitting, electron transporting material such as tris-(8hydroxyquiniline) aluminum (Alq<sub>3</sub>)[3].

The top electron-injecting electrode typically consists of Mg-Ag or Li-Al. The bottom hole-injecting electrode is typically composed of a thin film of transparent semiconductor indium tin oxide (ITO).

Light is emitted through this electrode when ITO is biased positive with respect to the top electrode. It was

Dept. of Electrical Engineering, Pusan National University, Korea. (co2laser@chol.com)

Dept. of Electrical Engineering Kyungpook National University, Korea. (otkim@orgio.com)

discovered that degradation of OLED's occurs in air on a time scale of a few hours. Charge conduction in these ostensibly insulating organic electro-luminescent materials requires electric fields typically in the range of active 2~5MV/cm. Such operating conditions, coupled with a reactive, low work function top electrode, which is required to ensure efficient injection of electrons into the organic, indicated that even simple encapsulation in an insert atmosphere using an epoxy-sealed glass package for example, can extend the useful device lifetime [4]. Also, remaining degradation modes after encapsulation include the formation and growth of non-emissive "dark spot defects." With small particles and scratches on the surface obtained through the sputtering process, the spikes and bulk bring about a significant decrease in resistance and in the path to leak current [5~6]. Also, the ITO film thickness on glass substrate is one of the important methods to build up reliability and performance of the panel. The electrode in OLEDs has been reported to be an important factor in influencing the electrical and luminescent properties.

In this study, we investigated the effect of OLED electrode thickness. We carried out an electrode thickness study to optimize the processing condition in OLEDs.

The optimal thickness of the various constituent OLED electrodes has been identified and discussed.

# 2. OLED's design

We developed a passive matrix of 1.98-in. Average brightness and resolution of the sample panel are 50cd/m² and 120×160, respectively, and it drives with PWM (Pulsewidth-modulation) and PFM (Pulse-frequency-modulation). We developed a sample module that allows scanning dutyratio to select freely. Average scanning duty is 1/60 cycle. A stack type column of anode pattern was formed in the active area by the conventional photo-lithography technique. Prior to the formation of cathode separators, a series of organic layers formed on the top of ITO by the vapor deposition in the vacuum. Eventually, the organic layers were thermally deposited on the substrate with ITO film. Glass size was 33.4×47.1mm. The deposed Al sealing line was 0.38mm. Dot pitch was 0.226× 0.255mm.

We fabricated a stack type panel under standard conditions [ITO 1500Å, Mo 2600Å, Al 1500Å]. We changed the standard conditions of electrode thickness.

# 3. Effects of Anode, Bus and Cathode Electrode Thickness

In order to precisely measure the electrode thickness, we used probe-station. Data-line resistance and scan-line

resistance were measured by probe-station. The thickness of ITO, Al and Mo electrodes are summarized in Table 1. We fabricated two sample panels of each condition.

**Table 1** Electrode thickness in samples measured by probe-station

| cardition | Test thickness |       |      | Thidness | measured by SEM | Data | Scan               |                   |                       |
|-----------|----------------|-------|------|----------|-----------------|------|--------------------|-------------------|-----------------------|
|           | nro(A)         | Mt(Å) | A(Å) | ſľO(Å)   | Mt(Å)           | A(A) | Pesistance<br>(k3) | Pesistance<br>(Ω) | remark                |
| 1         | 1500           | 2800  | 1500 | 1500     | 2390            | 1740 | 66                 | 159               | Standard              |
| 2         | 1500           | 2800  | 1000 | 1500     | 2350            | 880  | 67                 | 144               |                       |
| 3         | 1500           | 1500  | 1500 | 1500     | 1345            | 1590 | 11.52              | 184               |                       |
| 4         | 1500           | 700   | 1500 | 1500     | 765             | 1530 | 12:35              | 270               | Maximum<br>Resistance |
| 5         | 1000           | 1500  | 1000 | 1000     | 975             | 885  | 11.6               | 208               |                       |
| 6         | 1000           | 2800  | 1500 | 1000     | 2460            | 1455 | 7.1                | 159               | -                     |

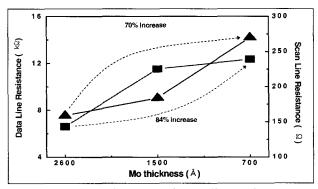


Fig. 3 Data-line resistance and scan-line resistance as a function of the Mo thickness

As shown in Table 1, the conditions for the electrode thickness measurement were as follows; [ITO: 1000 to 1500 Å, Mo: 700 to 2600 Å and Al; 1000 to 1500 Å].

As demonstrated in Table 1, condition 1 is the standard condition. The highest resistance is obtained from condition 3. The dominant factor is the Mo thickness. Fig. 3 illustrates a contour plot of data and scan resistance value. As the Mo thickness was changed from 2600 Å to 700 Å, its resistance increased to 84%. The ITO thickness hardly has any effect on data-line resistance. When the ITO thickness has diminished from 1500 Å to only 1000 Å, data-line resistances have barely increased 6%.

As we examined so far, the Mo thickness is the main factor providing an effect to resistance not only in the case of data-line but also in scan-line. The same figure also demonstrates scan resistance characteristics when the Mo thickness decreased. In other words, data-resistance and scan-resistance depend on Mo thickness. On the other hand, the effect of ITO and Al are scarcely influenced by scan-line resistance of the OLED's panel.

# 4. Effects of Power Consumption

Table 2 demonstrates that as the thickness of the bus-line

(Mo) electrodes thinned, the power consumption increased. When one pixel current was flowing  $250[\mu\text{A}]$ , the maximum was 2057mW and the minimum was 23mW. The maximum increased by 150% from 23mW. Power consumption rate according to thickness of the bus-line electrode (Mo) is shown in Fig. 4.

Table 2 Power consumption caused by bus-line resistance

| condition | Test trickness |       |      | Pesistance incressed in standard<br>basis |                       | Rower incressement (milif |                         | Reservoracingsion |                   |                   |                       |
|-----------|----------------|-------|------|---|-----------------------|---------------------------|-------------------------|-------------------|-------------------|-------------------|-----------------------|
|           | ITC(A)         | Mt(A) | A(ā) | Detaresistance<br>(ks)                    | Scanresistance<br>(C) | 50µAbasis                 | 250 <sub>µ</sub> Abaeis | 1,000             |                   | Ma                | bВ                    |
| 2         | 1500           | 2800  | 1000 | 01  | -15                   |                           |                         | <b>(#</b> )       |                   | (4)               |                       |
| 3         | 1500           | 1500  | 1600 | 492                                       | 25                    | 25                        | 626                     | Designed<br>value | Observe<br>dvalue | Designe<br>dvalus | Observ<br>ed<br>velue |
| 4         | 1500           | 700   | 1500 | 575                                       | 111                   | 82                        | 2057                    |                   |                   |                   |                       |
| 5         | 1000           | 1500  | 1000 | 5   | 44                    | 38                        | 999                     | 220               | 340               | 1300              | •                     |
| 6         | 1000           | 2600  | 1600 | 0.5                                       | 0                     | 1                         | 23                      |                   |                   |                   |                       |

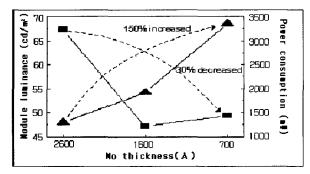


Fig. 4 Module brightness and power consumption as a function of Mo thickness

That is a major factor in optimizing line current toward column direction during driving. We also calculated the voltage drop.

We obtained (condition 4),

data line - Red:  $250[\mu A] \times 10^{-6} \times 5.75[k\Omega] \times 10^{3} = 1.44[V]$ Green:  $250[\mu A] \times 10^{-6} \times 5.75[k\Omega] \times 10^{3} = 1.44[V]$ Blue:  $250[\mu A] \times 10^{-6} \times 5.75[k\Omega] \times 10^{3} = 1.44[V]$ scan line -  $(250+250+250)[\mu A] \times 120 \times 10^{-6} 10^{-6} \times 111[\Omega] = 10[V]$ 

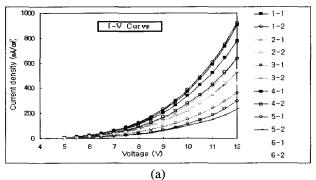
Also, maximal power amplification is,  $\{(250\times1.44+250\times1.44+250\times1.44)[\mu A]\times10^{-6}\times120\times2\}$  (dataline)+ $\{(250+250+250)[\mu A]\times10^{-6}\times120\times2\times10[V]$  (scan-line) = 2057[mW]

As we can see, in condition 2, the panel scan-resistance is observed to reduce the resistance of the scan-electrode once the thickness of the Al electrode is reduced, because Mo and Al electrodes improve contact resistance. Therefore, the scan-line resistance is diminished. When reducing Mo thickness, the rate of power consumption increases rapidly. Due to the increased power consumption, it is too difficult to apply mobile applications. This is applied to home appliances such as refrigerators, airconditioners and washing machines.

As the Mo thickness was reduced from 2600Å to 700 Å, the power consumption was changed from 1990[mW] to 3000[mW]

#### 5. IVL Characteristics Results

Current-voltage (I-V) and luminance-voltage (L-V) characteristics were measured with Keithley source-measurement unit 236 and Yokokawa electronics illuminance-Meter 3298. We observed voltage (V) and current density (J) characteristics of the OLED device, which are summarized in Fig. 5 (a). And, Fig. 5 (b) shows the plots of the luminance versus current-density. In this study, the 1-1 (see Fig. 5) curve of the peak demonstrates that standard condition is only composed of ITO, Mo and Al electrode.



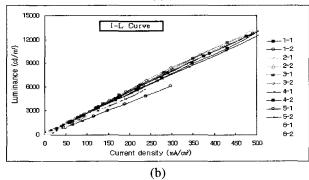


Fig. 5 (a) Current density-voltage characteristics of samples

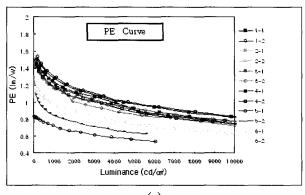
(b) Dependence of OLED luminance-voltage characteristics of samples

The relative luminescence and photo-efficiency of the sample panel is indicated in Fig. 6 (a).

The output spectrum of OLED with each sample, measured from the substrate surface, is shown in Fig. 6 (b). The emission spectra from the surfaces are similar to conventional ones.

However, there is a slight curve at 510nm due to differences in the transparency by ITO thickness. The 1-1 sample (standard panel) quantum efficiency is higher than the others. A function of current-density of the OLED with

each electrode thickness is displayed in Fig. 7 (a). The more the resistance of a panel's electrode is increased, the worse the I-V characteristic is. A luminescence characteristics of the OLED compared with that of a thick ITO electrode are shown in Fig. 7 (b), indicating that there is a dependency on ITO thickness. Also, it is relatively independent of bus-line resistance.



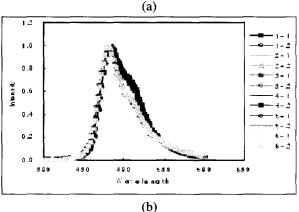


Fig. 6 (a) Dependence of the Efficiency (E)-Current density (J) Characteristics on the thickness of electrode

(b) Spectra of the light from a full color OLED through the top electrode and the glass substrate

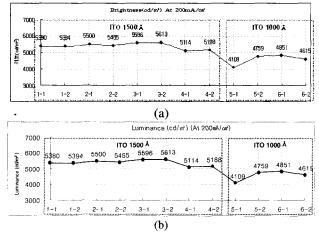


Fig. 7 (a) Current density and (b) brightness characteristic

The luminance of this sample device is 13,000 cd/m<sup>2</sup> at 10.8V with 481 mA/cm<sup>2</sup>. In our IVL plots, it was confirmed that this is the best characteristic of the sample's standard condition [No. 1].

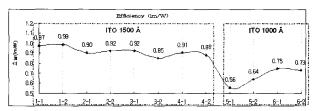
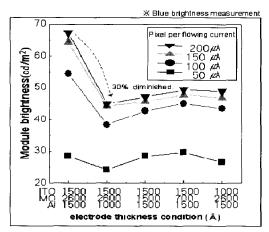


Fig. 8 Efficiency characteristic of OLED samples

The reason why both I-V and L-V characteristics are simultaneously increased after optimizing is thought to be that the electrode thickness is very optimistic. From the efficiency characteristic of the OLED sample, it can be seen that the relative power efficiency in Fig. 8 shows the expected stable flatness with increasing ITO thickness.



**Fig. 9** Dependence of OLED Luminance (L)-electrode density characteristics on the pixel current

In addition, when module driving, the brightness is decreased by the increased line-resistance. Fig. 9 illustrates the module luminance and the electrode thickness obtained by varying the pixel current from  $50[\mu\text{A}]$  to  $200[\mu\text{A}]$ .

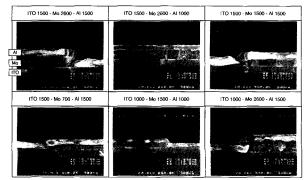


Fig. 10 Scanning electron micrographs showing the electrode.

The electrode surface samples were examined by SEM. Fig. 10 shows SEM images of ITO, Mo, and Al. These results suggest that the data resistance and scan resistance are determined by Mo thickness. We have taken the view that ITO and Al thickness are irrelevant.

#### 6. Results and Discussion

We have shown that it was possible to improve the operating conditions of the OLED through appropriate electrode thickness. Furthermore, we have proposed optimistic electrode thickness in 1.8" passive mobile applications. We've determined that the most significant factor is Mo's thickness.

As a result, if there is any problem with the thickness reduction of Mo, it is impossible to apply mobile applications because of higher power consumption. However, it is applied to digital appliances such as airconditioners, refrigerators, microwaves and so on.

For optimal power efficiency and brightness, the thickness of ITO, Mo, and Al are 1500Å, 2600Å, and 1500Å, respectively. At these optimal configurations, a luminance of 13,000 cd/m² is obtained at a current density of 481 mA/cm².

# Acknowledgement

The authors are grateful to the researchers at LG Elite and LG Electronics Inc.

# References

- [1] C. W. Tang. S. A. Vanslyke, *Appl. phys. Lett.* 51 (1987)913.
- [2] J. H. Burroughes, D.D.C. Bradley, A.R. Brown, R. N. Marks, K. Mackay, R.H. Friend, P. L. Burns, A. B. Holms, Nature 347(1990)539.
- [3] P. E. Burrows, G. Gu, V. Bulovic, Z. Shen, S. R. Forrest, and M. E. Thompson "Achieving Full-Color Organic Light-Emitting Devices for Lightweight, Flat-Panel Displays", *IEEE Trans. Electron device*, vol. 44, pp.1188-1998, 1987.
- [4] S. A VanSlyke, C. H. Chen and C. W. Tang, "Organic electroluminescence devices with improved stability", *Appl Phys. Lett.*, vol. 69, pp.2160-2162, 1996
- [5] S. W. Kim et al. / "2.4in. monochrome small molecular OLED display for mobile application", *Current Applied Physics* 2. pp.335-338, 2002
- [6] Y. Kijima, N. Asai, N. Kishii, S.-i. Tamura, *IEEE Trans. Electron Dev.* vol. 44(8), pp 1222-1228, 1997



# Sung-Joon Park

He received his B.S. degree in Electrical Engineering from Dongeui University in 2000. He received his M.S. degree in Electrical Engineering from Pusan National University in 2002. He is currently working toward his Ph.D. degree in Electrical

Engineering at Pusan National University.



### Ok-Tae Kim

He received his B.S. and M.S. degrees in Electrical Engineering from Kyungpook National University, Korea in 1995 and 1997, respectively. He worked at the LG Electronics Institute of Technology from 1997-2003. Currently, he is employed at Kyung-

book National University.



## Hee-je Kim

He received his B.S. and M.S. degrees in Electrical Engineering from Pusan National University, Korea in 1980 and 1982, respectively. He joined the Plasma & Laser Lab of the Korea Electro-Technology Institute in 1983 as a Research Engineer. He went to

Kyushu University, Hukuoka, Japan in 1985 where he received his Ph.D. degree from Kyushu University, Hukuoka, Japan in 1990. As of 1995, he has been a Professor at the School of Electrical Engineering, Pusan National University.