

The proposed PV inverter system in this paper is integrated DVR system to mitigate the voltage sag and swell at grid side. Power generated by PV is supplied to the grid through the shunt inverter while the series inverter as DVR operation injects the sufficient amount of voltage to the sensitive load as voltage compensation.

4. Proposed Operation of PV Inverters

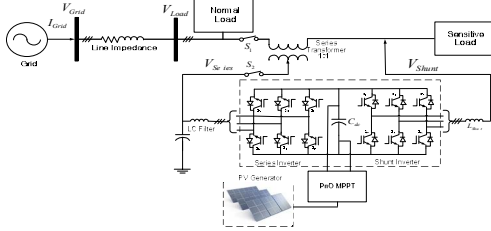


Fig. 3. Proposed PV converter with voltage compensation.

The proposed PV converter has the configuration of UPQC with back to back inverter. It consists of two inverters connected in series and shunt. The series connection inverter is the DVR configuration to mitigate the under voltage for sensitive load. The shunt inverter will inject the maximum active power during normal condition or when the series inverter is in on mode.

5. Simulation results

TABLE I System Parameters

Part of Circuit	Descriptions	Value
Grid	Main Voltage	220;180;150 V
	Frequency	220;240;270 V
Under voltage Load	Sensitive Load Active	80 kW
	Sensitive Load Reactive	60 kVar
Over voltage Load	Sensitive Load Active	90 kW
	Sensitive Load Reactive	60 kVar
Converter DC	DC Capacitor (Cdc)	20 mF
PV System	Max Power	110 kW
	Open Circuit Voltage	675 V
	Short Circuit Current	168 A
	V _{mpp}	684 V
Series Connection	I _{mp}	180 A
	Transformer	1:1
	Filter (C _{se} and L _{se})	50μF, 1mH
Shunt Connection	PI 1, PI 2, and PI 3	20
	Lshunt	2mH
PWM	PI 1 & PI 2(Islanded)	20
	PI 1 & PI 2(Power Mode)	200
PWM	Sampling Frequency	5 kHz

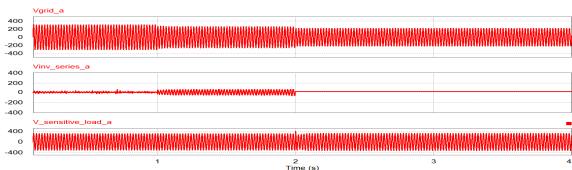


Fig. 4. Under-voltage simulation: grid, series inverter, and sensitive load.

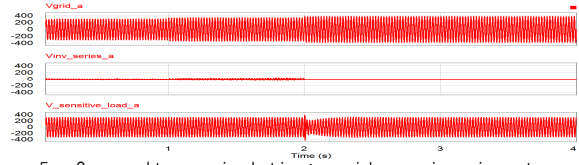


Fig. 5. Over-voltage simulation: grid, series inverter, and sensitive load.

Fig. 4 shows the voltage during under voltage condition. Before $t < 1s$, the grid voltage is in normal condition, then no action from series inverter, while shunt inverter delivers active power to the grid only. Once the under voltage is detected ($1s < t < 2s$) but still above 0.8pu, the series inverter injects the voltage to the sensitive load. However, the under voltage is below 0.8pu ($2s < t < 4s$), the series inverter gives up the voltage compensation. Then the sensitive load is disconnected from the line grid and the shunt inverter supplies the power to the sensitive load directly.

Fig. 5 shows the simulation results of the grid voltage, the load. During normal condition, $t < 1s$, no over voltage from grid only shunt inverter injects active power to the grid. Time runs from 1 to 2s, the over voltage is occurred to 1.09pu and the series inverter compensates the voltage grid to follow standard magnitude voltage. Both of S1 and S2 are in on position. Moreover, when the over voltage is above 1.1pu ($2s < t < 4s$), the shunt inverter will take care of sensitive load. In this condition, islanded mode will be transferred from grid connected.

6. Conclusions

Power quality related to submarine cable can be solved by the proposed method. Under the normal mode, shunt inverter injects power to the grid without any voltage compensation. In voltage compensation mode, the under voltage or over voltage are compensated by the series inverter and shunt delivers power to the grid simultaneously. In the stand alone mode, sensitive load will be fed directly from the shunt inverter without connected to the grid. This kind of technology can be applied to the isolated islands which is supplied power through the submarine cable.

참고 문헌

- [1] R. C. Dugan, M. F. McGranaghan, and H. W. Beaty, Electrical Power Systems Quality. New York: McGraw-Hill, 1996.
- [2] IEEE for Interconnecting Distributed Resources With Electric Power Systems, IEEE Standard 1547-2003, 2003.
- [3] A. Ghosh and G. Ledwich, Power Quality Enhancement Using Power Devices. Boston, MA: Kluwer, 2002.
- [4] P. R. Sanchez, E. Acha, J. E. O. Calderon, V. Feliu, and A. G. Cerrada, "A versatile control scheme for a dynamic voltage restorer for power quality improvement," IEEE Trans. Power Del., vol. 24, no. 1, pp. 277 - 284, Jan. 2009.
- [5] A. M. Rauf and V. Khadkikar, "An enhanced voltage sag compensation scheme for dynamic voltage restorer," IEEE Trans. Ind. Electron., vol. 62, no. 5, pp. 2683 - 2692, May 2015.