

Effects of substrate temperature on the performance of $\text{Cu}_2\text{ZnSnSe}_4$ thin film solar cells fabricated by co-evaporation technique

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ABSTRACT

Despite the success of $\text{Cu}(\text{In,Ga})\text{Se}_2$ (CIGS) based PV technology now emerging in several industrial initiatives, concerns about the cost of In and Ga are often expressed. It is believed that the cost of those elements will eventually limit the cost reduction of this technology. One candidate to replace CIGS is $\text{Cu}_2\text{ZnSnSe}_4$ (CZTSe), fabricated by co-evaporation technique. Co-evaporation technique will be one of the best methods to control film composition. This type of absorber derives from the CuInSe_2 chalcopyrite structure by substituting half of the indium atoms with zinc and other half with tin. Energy bandgap of this material has been reported to range from 0.8eV for selenide to 1.5eV for the sulfide and large coefficient in the order of 10^{14}cm^{-1} , which means large possibility of commercial production of the most suitable absorber by using the CZTSe film. In this work, Effects of substrate temperature of $\text{Cu}_2\text{ZnSnSe}_4$ absorber layer on the performance of thin films solar cells were investigated. We reported on some of the absorber properties and device results.

Key Words: solar cell, thin film, $\text{Cu}_2\text{ZnSnSe}_4$ (CZTSe), Co-evaporation