## Properties of Dy-doped La<sub>2</sub>O<sub>3</sub> buffer layer for Fe-FETs with Metal/Ferroelectric/Insurator/S1 structure

Jong-Hyun Im, Kwi-Jung Kim, Shin-Woo Jeong, Jong-Ill Jung, Hui-Seong Han, Ho-Seung Jeon, and Byung-Eun Park School of Electrical and Computer Engineering, University of Seoul

Abstract: The Metal-ferroelectric-semiconductor (MFS) structure has superior advantages such as high density integration and non-destructive read-out operation. However, to obtain the desired electrical characteristics of an MFS structure is difficult because of interfacial reactions between ferroelectric thin film and Si substrate. As an alternative solution, the MFS structure with buffer insulating layer, i.e. metal-ferroelectric-insulator-semiconductor (MFIS), has been proposed to improve the interfacial properties. Insulators investigated as a buffer insulator in a MFIS structure, include Ta<sub>2</sub>O<sub>5</sub>, HfO<sub>2</sub>, and ZrO<sub>2</sub> which are mainly high-k dielectrics. In this study, we prepared the Dy-doped La<sub>2</sub>O<sub>3</sub> solution buffer layer as an insulator. To form a Dy-doped La<sub>2</sub>O<sub>3</sub> buffer layer, the solution was spin-coated on p-type Si(100) wafer. The coated Dy-doped La<sub>2</sub>O<sub>3</sub> films were annealed at various temperatures by rapid thermal annealing (RTA). To evaluate electrical porperteies, Au electrodes were thermally evaporated onto the surface of the samples. Finally, we observed the surface morphology and crystallization quality of the Dy-doped La<sub>2</sub>O<sub>3</sub> on Si using atomic force microscopy (AFM) and x-ray diffractometer (XRD), respectively. To evaluate electrical properties, the capacitance-voltage (C-V) and current density-voltage (J-V) characteristics of Au/Dy-doped La<sub>2</sub>O<sub>3</sub>/Si structure were measured.

Key Words: MFIS, Dy-doped La<sub>2</sub>O<sub>3</sub>, insulator, ferroelectric