

**Intervening Firing Method and Passive Filter Design for Harmonic Elimination and Reactive Power Compensation in Three-Phase Thyristor Phase-Controlled Converters Supplying a DC Motor**

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**Abstract:** This paper presents a method for harmonic elimination and reactive power compensation using an intervening firing method and passive power filter with is suitable to compensate rapidly changing loads and reactive power. The proliferation of three-phase thyristor phase-controlled converter of DC motor drives into a power system has the potential to increase the harmonic levels in the power system. The design procedure of an intervening firing method and passive power filter capable of reducing the voltage and current harmonics produced by converter supplied from a source having internal large inductive impedance is offered. The analysis uses the orCAD PSpice to model three-phase thyristor phase-controlled converter of DC motor drives as well as the system.

**Key words:** intervening firing method, passive power filter, reactive power, harmonic levels

**1. INTRODUCTION**

The proliferation of three-phase thyristor phase-controlled converter of DC motor drives into a power system has the potential to increase the harmonic levels in the power system. Among the different three-phase loads connected to the distribution system. DC motor drives are the significant importance.

Three-phase thyristor phase-controlled converter of DC motor drives generated the current total harmonic distortion ( $THD_i$ ) is normally in the rang of 100%. Different techniques have been implemented to reduce both current and voltage harmonics. These techniques depend on using auxiliary circuits, active or passive connected in parallel at the points of common coupling and intervening firing method. The use of active electronic devices such as active filters, the electronic voltage regulator and the adaptive VAR compensator (AVC) can cause further distortion in the distribution system.

The passive circuits used to reduce the distribution system current and voltage harmonics is to use shunt capacitors, however, this may generate unexpected harmonic voltages due to the series resonance [5].

Intervening firing method [1][4] was an improved commutation arrangement for an AC to DC energy converters. The improved arrangement uses turn-off capable commutation to enable modification of the operating waveforms in the phase-controlled converters. An intervening firing method is made to the leading phase when the output is less than one-half of the maximum value. The disclosure includes mathematical consideration of source machine power factor, output waveform ripple content, and other refinements of the commutation arrangement.

This paper presents the design of passive harmonic filter[5] and the use of intervening firing technique[1][4] with is suitable to compensate rapidly changing loads and reduce the harmonic current and compensate of the reactive power in three-phase controlled converter for DC motor drives in power distribution system. This design follows harmonic standard of the Power Board (Thailand), IEC 1000-2-2 -1990 standard at the voltage 415/240 V and IEC 146 Semiconductor Converter. The experiment was done by simulating power distribution that has large DC motor as the load of the system

**2. DESCRIPTION OF MODEL S STEM**

Harmonic current from three-phase thyristor phase-controlled converter of DC motor drives is a harmonic source in factory that flow into the power system and cause of the power quality problem. To solving this problem using harmonic current filters to filter harmonic current in some frequency away from the system for stopped flowing of harmonic current into the power system as shown in Fig 1

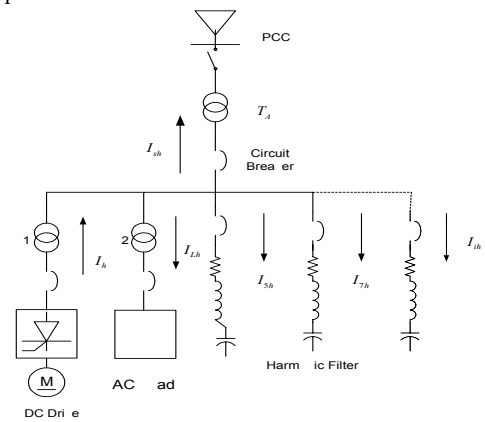


Fig 1 DC motor drive system

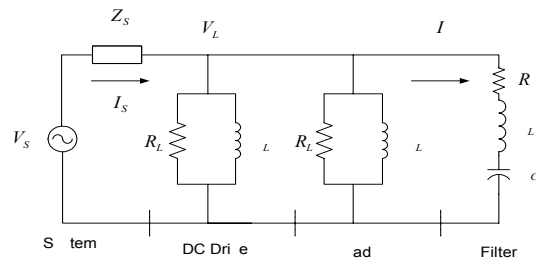


Fig 2 Equivalent circuit of DC motor drive system

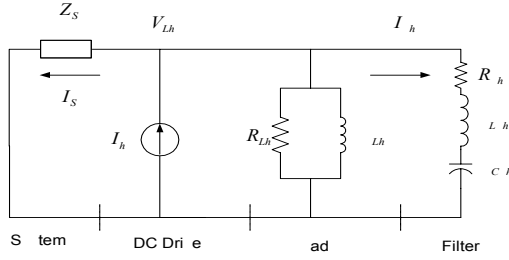


Fig 3 Equivalent circuit of DC motor drive system at harmonic frequency

Consider this model from point of common coupling (PCC) into power system network that substitute by impedance with have resistance series with reactance. The resistance have relate with reactance in form of  $\omega L/R$ . The impedance at harmonic frequency derive form equation

$$Z_s = \frac{V_{sys}^2}{MVA_{SC}} \quad (1)$$

$$Z_{sh} = R_s + jh \omega L_s \quad (2)$$

where  $Z_s$  is the power system impedance ( $\Omega$ )

$MVA_{SC}$  is the short circuit power (MVA)

$Z_{sh}$  is the impedance at harmonic frequency ( $\Omega$ )

$R_s$  is the resistance of the power system ( $\Omega$ )

$L_s$  is the reactance of the power system ( $\Omega$ )

An harmonic analyzing will model transformer with impedance that consist of resistance series with leakage reactance that vary with harmonic order number, and the resistance value is constant when don't add result form skin effect, so at the harmonic frequency, the transformer impedance have a value

$$Z_{Tr} = \frac{\%Z}{100} \times \frac{V^2}{KVA \times 1000} \quad (3)$$

$$R_{Tr} = P_c \times \frac{V^2}{(KVA \times 1000)^2} \quad (4)$$

$$Z_{Trh} = R_{Tr} + jh \omega L_{Tr} \quad (5)$$

where  $Z_{Tr}$  is the impedance of the transformer ( $\Omega$ )

$\%Z$  is the impedance at harmonic frequency of the transformer ( $\Omega$ )

$V$  is rated voltage of the transformer (V)

$R_{Tr}$  is the resistance of the transformer ( $\Omega$ )

$P_c$  is the winding loss (kVA)

The linear model system consist of resistance and reactance that find form real power (P) and reactive power (Q) of load

$$R_L = \frac{V_{sys}^2}{P_1} \quad (6)$$

$$L = h \cdot \frac{V_{sys}^2}{Q_1} \quad (7)$$

where  $R_L$  is load resistance at harmonic frequency ( $\Omega$ )

$P_1$  is real power of load (W)

$Q_1$  is reactive power of load (VAR)

$L$  is load reactance at harmonic frequency ( $\Omega$ )

## 2.1 Reactive power compensation

The tuning proportions reactive power of harmonic current filter must concern with flowing of harmonic current in the power system that limited international harmonic standard

$$Q_{com,h} = Q_{com} \times \frac{I_h}{\sum_{i=1}^n I_i} \quad (8)$$

where  $Q_{com}$  is compensate reactive power (VAR)

$I_h$  is harmonic current order  $h$  (A)

## 2.2 harmonic passive filters

The working of harmonic passive filter comprise of 3 components, Capacitor, Inductor and the resistance in form of inductance. These components connected in series resonant for making harmonic passive filter having lower impedance at resonant frequency that called series resonant. At tuning frequency, harmonic passive filter have a impedance equal with a resistance because of at resonant frequency the value of capacitive reactance have a value equal inductive reactance but pole inverse, so the value of reactance can to wipe out. The calculation value of the capacitance (C) inductance (L) and resistance (R) using equation

Table 1 Tuning point for this experiment

Range of Tuning Point	Lower Limit	Upper Limit
Order 5 <sup>th</sup>	4.5	4.9
Order 7 <sup>th</sup>	6.5	6.9

$$C = \frac{1}{2\pi f_c} \quad (9)$$

$$L = \frac{C}{2\pi f_n^2} \quad (10)$$

$$R = \frac{L \omega_n^2}{Q} \quad (11)$$

## 2.3 Intervening firing method

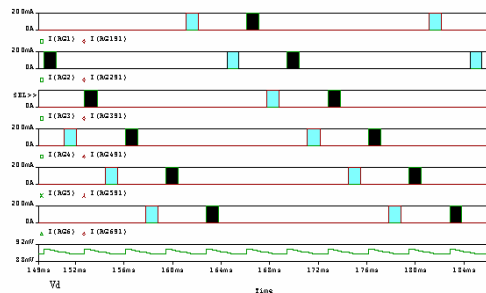


Fig 4 Intervening firing method at  $\alpha = 80^\circ$ ,  $\beta = 20^\circ$

Output voltage  $V_d$ ,  $V_{ripple}$  and input reactive power ( $Q_i$ ) find from equation

$$v_d(t) = v_d + \sum_{n=1}^{\infty} \sqrt{2} V_n \sin(n\omega t + \psi_n) \quad (12)$$

$$V_{ripple} = \sqrt{\sum_{n=1}^{\infty} V_n^2} \quad (13)$$

$$P = 3V_{an} I_1 \cos \phi_1 \quad (15)$$

$$|Q_i| = 3V_{an} I_1 |\sin \phi_1| \quad (16)$$

### 2.4 harmonic standard of the Power board of Thailand

For apply with nonlinear load that using in Thailand factory for 1 phase and 3 phase refer with standard of G.5/3-1976: Engineering Recommendation but proving level of voltage that suitable for supplying voltage level in Thailand

### 3. Experimental

The experiment starting with model the power system 12 kV, 50 Hz, 2000 MVA, /R ratio =10 as shown in Fig 1 supplying 6 pulse phase-controlled converter that using for DC motor drive rated 2 HP, 130 V, 1800 rpm, series DC motor. The converter is operated from a 3 phase transformer 2000 kVA, 12kV/100 V, 50 Hz, Loss 30 kW, %Z=6 The rated armature current of the motor is 15 A. The motor parameters are  $R_a = 1.429\Omega$ ,  $L_a = 15.01mH$ . The experiment test at 50 % rated load and tune firing at 80 degree, intervening firing at  $\beta=30$ . Measuring harmonic current that flowing in the power system and compare with international harmonic standard.

Table 2 Harmonic current measured form ac-side of 6 phase controlled converters at 50% rated load

Harmonic Order	Magnitude( % $I_1$ )	Harmonic Order	Magnitude( % $I_1$ )
5	26.21	19	6.64
7	18.31	23	5.85
11	12.01	25	5.01
13	9.78	29	4.69
17	7.84	31	4.01

### 4. Result

Table 3 Harmonic current analysis from converter flow into the system transformer and power system

Order	Low Voltage Side		High Voltage Side	
	Harmonic Source (A)	Transformer (A)	System (A)	Power Board Limit (A)
5	30.87	26.67	4.49	10
7	21.71	18.65	3.10	8
11	11.90	9.60	1.70	7
13	8.60	7.55	1.23	6
17	5.31	4.63	0.87	2
19	4.10	3.57	0.58	1
23	3.03	2.60	0.47	-
25	2.43	2.07	0.35	-
29	2.12	1.82	0.25	-
31	1.82	1.54	0.23	-
Total	204.20	175.12	5.58	-

Table 4 Harmonic voltage measured from low voltage side of system transformer

Low voltage side					
h	voltage	%	h	voltage	%
1	380	100	19	7.28	1.92
5	14.43	3.80	23	6.48	1.71
7	14.12	3.71	25	5.62	1.48
11	11.53	3.03	29	5.65	1.49
13	10.61	2.79	31	5.15	1.36
17	8.48	2.23			

Table 5 Harmonic voltage measured from high voltage side of system transformer

High voltage side					
h	voltage	%	h	voltage	%
1	12000	100	19	55.76	0.46
5	110.36	0.92	23	49.64	0.41
7	108.02	0.90	25	42.98	0.36
11	88.21	0.74	29	43.23	0.36
13	81.21	0.68	31	39.41	0.33
17	64.88	0.54			

Table 6 Harmonic voltage of the system

Low voltage side		High voltage side	
$V_{total}$ (Volt)	381.20	$V_{total}$ (Volt)	12002.22
$THD_v$ (%)	7.94	$THD_v$ (%)	1.92

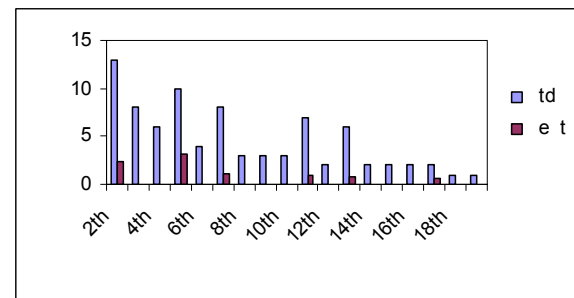


Fig 5 Comparison between harmonic current standard of the Power Board (Thailand) and the experiment

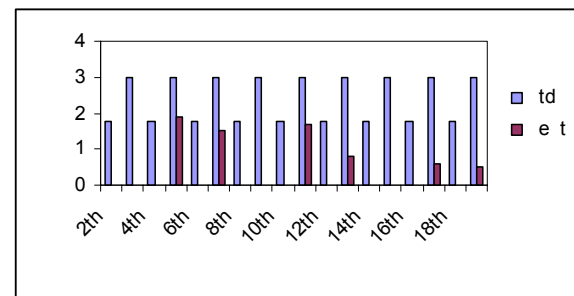


Fig 6 Comparison between harmonic voltage standard of the Power Board (Thailand) and the experiment

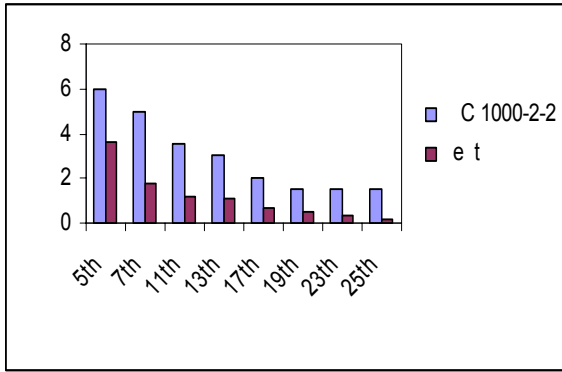
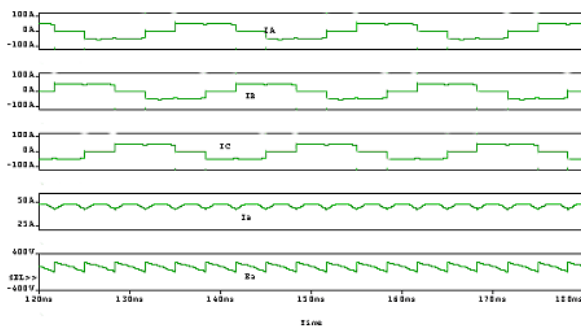
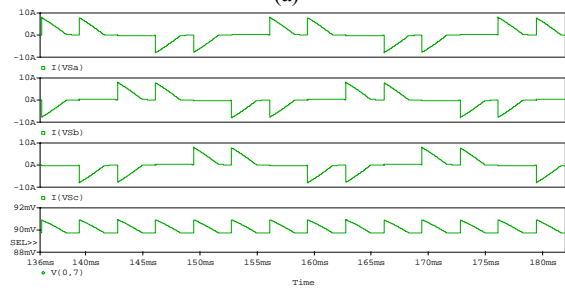


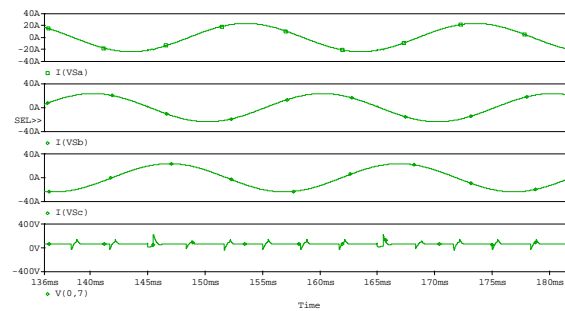
Fig 7 Comparison between harmonic voltage standard of the IEC 1000-2-2 -1990 standard



(a)



(b)



(c)

Fig 8 Comparison between firing  $\alpha = 80$  (a), intervening at  $\alpha = 80, \beta = 30$  (b), and with passive filter(c)

## 5. Conclusions

Harmonic elimination of DC output voltage and reactive power compensation of input current that generated form 6

pulse phase-controlled converter by using intervening firing method every 1/6 period and using harmonic passive filter can reduce harmonic generation of DC output voltage and reactive power generation of input current. And when compare with the Power Board of Thailand standard and IEC 1000-2-2 -1990 standard, the result is a good tend and can improve the input power factor of the system.

## References

- [1] D.L. Lafuze, Turn-off switch phase control with improved ripple and power factor U.S. Patent No.5, 198, 972, 1993.
- [2] N.Mohan, T.M. Undeland, and W. P. Robbins, Poer Electronics : Converter, Application, and Design-2<sup>nd</sup> Edition, John Wiley & Sons, Inc., 1995, pp. 138-153
- [3] J. Lazar Park-vector Theory of Line-commutated Three-Phase Bridge Converters Serial editor Volume 1, Omikk Publisher Budapest, 1987, pp. 14-66
- [4] I. G. Park and J. T. oon, Charecterizing the double Firing Method For three-phase thyristor phase controlled converter, in Proc. IECON 96 , 1996, pp. 689-694
- [5] Artite Pattanapongchai and Piphat Laohasongkram Harmonic passive filter study and design by using three-phase thyristor phase-controlled converter Engineering journal, Khonekean University issue 1(61-77) January-March 2003 page 61-77

## Appendix

### Snubber Circuits for pulse phase controlled converter

The reverse-recovery currents generated in thyristors when they are reverse biased may result in unacceptably large overvoltages because of series inductance

$$C_s = \frac{0.6I_d}{V_{LL}}$$

$$R_s = 20 \frac{V_{LL}}{I_d}$$

where  $I_d$  is output dc current

$V_{LL}$  is voltage input line-line

total energy loss in each snubber

$$snubber = 3C_s V_{LL}^2$$