

Training an Artificial Neural Network for Estimating the Power Flow State

Alireza Sedaghati

Faculty of Electrical Engineering and Robotics, Shahrood University of Technology

(E-mail: ars@shahrood.ac.ir)

Abstract: The principal context of this research is the approach to an artificial neural network algorithm which solves multivariable nonlinear equation systems by estimating the state of line power flow. First a dynamical neural network with feedback is used to find the minimum value of the objective function at each iteration of the state estimator algorithm. In second step a two-layer neural network structures is derived to implement all of the different matrix-vector products that arise in neural network state estimator analysis. For hardware requirements, as they relate to the total number of internal connections, the architecture developed here preserves in its structure the pronounced sparsity of power networks for which state the estimator analysis is to be carried out. A principal feature of the architecture is that the computing time overheads in solution are independent of the dimensions or structure of the equation system. It is here where the ultrahigh-speed of massively parallel computing in neural networks can offer major practical benefit.

Keywords: Power flow; State estimator; Neural network

1. INTRODUCTION

Of the numerous different application are as of neural networks that have been developed or proposed previously, those related to numerical analysis appear to be mainly in optimization in which scalar objective function is to be minimized [1-4]. The neural network architectures proposed for linear or nonlinear optimization with or without constraints include. Hopfield type networks [1] and layered networks with feedbacks, which implement the steepest descent algorithm [2-4].

The principal aim of the development in numerical analysis methods which this paper seeks to report is that of deriving a neural network which implements the state estimator algorithm [5] for solving multivariable nonlinear equation systems. When implemented in hardware, the architecture achieves ultrahigh-speed computation. Individual multiplications in all of the matrix vector product operations, which arise implemented in parallel. A principal feature of the architecture is that the computing time overheads in solution are independent of the dimensions or structure of the equation system, which is solved.

The main step in being able to exploit the parallel processing which neural networks offer is that of solving, as a minimization problem, the linearized equation system to which state estimator method [5] leads at each iteration. Dynamic neural network with feedback provide means of implementing this form of unconstraint function minimization. The main context of the research reported is that of state estimator analysis in a power system. Whilst the computing overheads in analysis are nowadays unlikely to be at issue, there is scope for further advances in on-line forms of power flow measurements in digital computer control and monitoring for power system.

The main steps of the research reported here are:

- (a) Using a dynamical neural network with feedback to find the minimum value of the objective function at each 'the state estimator algorithm' iteration.
- (b) Deriving two-layer neural network structures to implement all of the different matrix-vector products that arise in neural network state estimator analysis.

2. THE LINE POWER FLOW STATE ESTIMATOR

The instrumentation around the bus can be divided into injections, bus metering, and line flow measurements. Each of the measurements monitors all three phases in order to calculate positive sequence values. The following assumptions are made:

1. balanced three-phase flow conditions are present, and the system is in steady-state operation,
2. accuracy of metering is known (i.e. the meter reading are accurate to 1%, 2%, etc. of the true physical value),
3. the full-scale range of each meter is known (e.g. 0-100MW, 0-50MVar, etc.).

The errors in converting the analog quantities to digital signals for the data link to the central computer are known 0.25% error, 0.1% error, and so on due to discrimination. Each measurement of line flow in any remote terminal unit, in complex form:

[measurement bus i on-line m in MW + j MVar] = S_{mi} (1)
has a specified weighting factor inversely proportional to its accuracy.

$$W_i = \frac{50 \times 10^{-6}}{[c_1 |S_{mi}| + c_2 (FS)]^2} \quad (2)$$

In Eq. (2), c_1 is the accuracy expressed as a decimal of the real and reactive flow, typically 0.01 or 0.02, c_2 is the transducer and analog-to-digital converter accuracy in decimal form, typically 0.0025, 0.005 and FS is the full-scale range of both the MW and MVar readings.

The factor 50×10^{-6} is selected to normalize subsequent numerical quantities. The absolute value of the measurement is used in the denominator, so the weighting factor W_i is recomputed each time a new measurement is made.

The weighted-least-squares state estimator is to find the best state vectors \mathbf{X} , which minimizes the performance index of M measurements, J :

$$J = \sum_{i=1}^M W_i |S_{mi} - S_{ci}|^2 \quad (3)$$

where S_{mi} is a power flow measurement and S_{ci} is a calculated value based on the state \mathbf{X} . The power flow calculated at bus i is

$$S_{ci} = E_i[Y_{ik}^*(E_i^* - E_k^*) + Y_{ii}^*E_i^*] = E_i \left[\frac{E_i^* - E_k^*}{Z_{ik}^*} + Y_{ii}^*E_i^* \right] \quad (4)$$

The measured numerical values S_{mi} are equated flow from bus voltages to be calculated:

$$S_{mi} = E_i \left[\frac{E_i^* - E_k^*}{Z_{ik}^*} + Y_{ii}^*E_i^* \right] \quad (5)$$

The power flow measurement depends primarily on the difference in voltage from bus i to bus k , so the equivalent measured differences are:

$$E_{mi} - E_{mk} = E_{mik} = \frac{Z_{ik}^*}{E_{mik}^*} S_{mi}^* - Z_{ik}^* Y_{ii}^* E_i \quad (6)$$

Rewriting Eq. (6) in matrix form yields:

$$E_M = [E_{m1} - E_{mp} - E_{m2} - E_{mN} \dots]^T = H^{-1} S_M^* - K \quad (7)$$

where H is an M^*M diagonal matrix, K is a vector, and E_M is vector of measured differences associated with each power flow measurement. Even though the bus voltages on the right hand side of Eq. (7) are unknown, E_M , can be considered to be derived from S_M . A matrix formulation of the state estimator performance index is

$$J = \sum_{i=1}^M W_i |S_{mi} - S_{ci}|^2 = [S_M - S_c]^* W [S_M - S_c] \quad (8)$$

where S_M is an M vector of the measurements, W is a diagonal weighting matrix, and S_c is the calculated power flow vector. In Eq. (8) substitute the measurement vector, Eq. (7) and a calculated power flow:

$$S_c = (HE_c + HK)^* \quad (9)$$

Substituting Eq. (9) into Eq. (8) yields:

$$\begin{aligned} J &= [S_M - S_c]^* W [S_M - S_c] \\ &= [HE_M + HK - (HE_c + HK)]^* \\ &\quad \times W [HE_M + HK - (HE_c + HK)] \\ &= [E_M - E_c]^* H^* W H [E_M - E_c] \end{aligned} \quad (10)$$

The calculated voltage difference vector can be expressed in terms of the bus voltages, E_{BUS} , and the slack voltage E_r :

$$E_c = AE = BE_{BUS} + bE_r \quad (11)$$

In Eq. (11), A is a double branch to bus incidence matrix, in other words, 1 and 1 in each row, corresponding to a measurement along a directed branch element. When line flows are measured on each end, the series element appears twice. The order of A is M^*N , where N is the number of buses, A is partitioned into a bus voltage part, B , and a slack bus vector b .

$$A = \begin{bmatrix} 1 & -1 & 0 & 0 & \dots & \dots & 0 \\ 0 & 1 & 0 & 0 & \dots & \dots & 0 \\ -1 & 0 & 1 & 0 & \dots & \dots & \dots \\ \dots & \dots & \dots & \dots & \dots & \dots & \dots \\ \dots & \dots & \dots & \dots & \dots & \dots & -1 \end{bmatrix}_{\text{meas.} \times \text{buses}} = [Bb] \quad (12)$$

Substituting Eq. (11) into Eq. (10) yields:

$$J = [E_M - BE_{BUS} - bE_r]^* H^* W H [E_M - BE_{BUS} - bE_r] \quad (13)$$

The product H^*WH is assumed to be constant [5] and Eq. (13) is minimized with respect to the bus voltage vector:

$$\begin{aligned} \frac{\partial J}{\partial E_{BUS}} = 0 &= -B^T H^* W H [E_M - BE_{BUS} - bE_r] \\ &= -[E_M - BE_{BUS} - bE_r]^* H^* W H B = 0 \end{aligned} \quad (14)$$

Either of the conjugate forms may be used to solve for E_{BUS} in order to minimize the performance index:

$$[B^T H^* W H B] E_{BUS} = B^T H^* W H [E_M - bE_r] \quad (15)$$

Let $D = H^*WH$, a diagonal matrix of order M , and the iterative line flow state estimator is

$$E_{BUS}^{k+1} = [B^T D B]^{-1} B^T D [E_M^k - bE_r] \quad (16)$$

where $k+1$ is the next iteration, and E_M^k is evaluated using the previous value of E_{BUS} . The algorithm to solve for the bus voltage vector in the state estimator is

- (1) Guess an initial E_{BUS}^k . A flat start sets all bus voltages equal to the slack bus.
- (2) Use the measurement data to calculate the initial voltage difference:

$$E_M^k = H^{-1} S_M^* - K$$

where both H and K depend on E_{BUS}^k .

- (3) Minimize J with respect to the bus voltage vector, E_{BUS}^k and find E_{BUS}^{k+1} .
- (4) Repeat steps 2 and 3 until convergence to within a desired error tolerance. The tolerance is usually expressed in terms of change in bus voltages between iterations:

$$|E_{BUS}^{k+1} - E_{BUS}^k| \leq \epsilon$$

- (5) If the result of the last equation is okay, stop the iteration, otherwise use $k \rightarrow k+1$ and go to step 2.

Observe that the approximation of constant $H^*WH = D$ does not affect the accuracy of converged results. The matrix D appears as an approximation to a gradient multiplication on the right-hand side of Eq. (15).

3. DYNAMICAL GRADIENT SYSTEM

Using J^p for the objective function (8) to be minimized at the algorithm iteration step p .

$$\begin{aligned} J^p &= [E_M^p - BE_{BUS}^p - bE_r]^* H^{*p} W H^p \\ &\quad \times [E_M^p - BE_{BUS}^p - bE_r] \end{aligned} \quad (17)$$

On differentiating Eq. (17) with respect to E_{BUS}^p , the gradient of J^p with respect to E_{BUS}^p is given by (using $H^{*p}WH^p = D = \text{constant}$)

$$\frac{\partial J^p}{\partial E_{BUS}^p} = -B^T D E_M^p + B^T D B E_{BUS}^p + B^T D b E_r \quad (18)$$

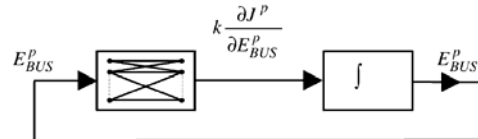


Fig. 1 Continuous time form of steepest-descent minimization.

Conventional iterative algorithms for minimizing a scalar objective function J^p have their basis in solving for the necessary condition in which the gradient vector $\partial J / \partial E_{BUS}^p$ is equal to zero at the minimum point. In the steepest-descent algorithm:

$$E_{BUS}^p(n) = E_{BUS}^p(n-1) - \eta \frac{\partial J^p}{\partial E_{BUS}^p} E_{BUS}^p(n-1) \quad (19)$$

In Eq. (19), n identifies the iteration step of the steepest-descent minimization procedure. Scalar coefficient η is often referred to as the 'step length' of the method. On rearranging Eq. (19):

$$E_{BUS}^p(n) - E_{BUS}^p(n-1) = -\eta \frac{\partial J^p}{\partial E_{BUS}^p} E_{BUS}^p(n-1) \quad (20)$$

Starting from an initial condition $E_{BUS}^p(0)$, Eq. (20) provides a means, by which the value $E_{BUS}^p(n)$ is updated progressively

at the each iteration until convergence:

$$\frac{\partial J^p}{\partial \mathbf{E}_{BUS}^p} = 0 \quad (21)$$

Eq. (20) has the form of a difference equation in which n can be interpreted as a discrete time step counter. On that basis, $\mathbf{E}_{BUS}^p(n) - \mathbf{E}_{BUS}^p(n-1)$ provides a finite difference approximation to the first order derivative of \mathbf{E}_{BUS}^p with respect to time. Interpreting the derivative in this way in the continuous time domain, leads to the following first order differential equation of the steepest-descent algorithm:

$$\frac{\partial \mathbf{E}_{BUS}^p}{\partial t} = k \frac{\partial J^p}{\partial \mathbf{E}_{BUS}^p} \quad (22)$$

In the Eq. (22), k is a negative scalar constant. An outline is shown in Fig. 1 of a dynamic neural based on feedbacks, which solves the differential equation system of Eq. (22). The evaluation of the gradient vector $\partial J^p / \partial \mathbf{E}_{BUS}^p$ of Eq. (18) is achieved by means of a neural network like ref. [6].

4. MATRIX VECTOR PRODUCTS IN NEURAL NETWORKS

Each of the several matrix vector products involved in forming the gradient vector $\partial J^p / \partial \mathbf{E}_{BUS}^p$ is implemented in two-layer feed forward networks in which the processing function for each node is a linear one. The combination of networks required to form the gradient vector $\partial J^p / \partial \mathbf{E}_{BUS}^p$ is best derived by via the Eqs. (18) and (22). It can be formed Fig. 2. The network in Fig. 2 forms O_1 and O_2 in the output layer from inputs I_1 and I_2 , where

$$\begin{bmatrix} O_1 \\ O_2 \end{bmatrix} = \begin{bmatrix} J_{11} & J_{12} \\ J_{21} & J_{22} \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \end{bmatrix} \quad (23)$$

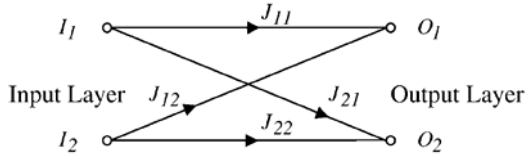


Fig. 2 Two-layer neural network.

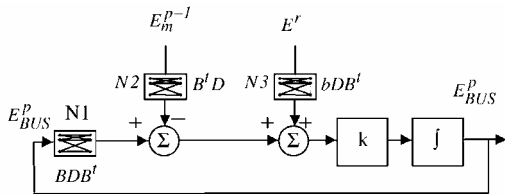


Fig. 3 Design of neural network for finding the state estimator correction vector \mathbf{E}_{BUS}^p

The weights of the cross-connections between input and output layer nodes correspond in value to the off diagonal element of the matrix. The pattern of cross-connections in the network derives directly from the nonzero off diagonal element pattern in the matrix. The combination of networks required to form the gradient vector $\partial J^p / \partial \mathbf{E}_{BUS}^p$ is best derived by breaking down the task into four distinct parts. Working from the extreme right-hand side of Eq. (18), the first matrix-vector product term to be evaluated is $\mathbf{E}_{BUS}^p [\mathbf{B}^t \mathbf{D} \mathbf{B}]$.

Inputs to nodes in the input layer of a two layer neural network of the form in Fig. 3 are elements of vector \mathbf{E}_{BUS}^p [$\mathbf{B}^t \mathbf{D} \mathbf{B}$] matrix is convenient to identify this neural network by N1 in Fig. 3.

Finally, integrating the gradient vector multiplied by k gives the $\mathbf{E}_{BUS}^p(n)$. The closed loop in Fig. 3 formed by feeding $\mathbf{E}_{BUS}^p(n)$ back into the input layer of neural network N1 settles to a value for which the quadratic function J^p is zero.

Fig. 4 gives the digital or discrete time form of the array of neural network of Fig. 3. The time delay operator in the steepest descent minimization is denoted by z^{-1} and the time step identifier by n . As in Eq. (20), the correction vector at n th. Steepest-descent step is formed in Fig. 4 from

$$\mathbf{E}^p(n) = \mathbf{E}^p(n-1) + k \Delta t \frac{\partial J^p}{\partial \mathbf{E}_{BUS}^p} \quad (24)$$

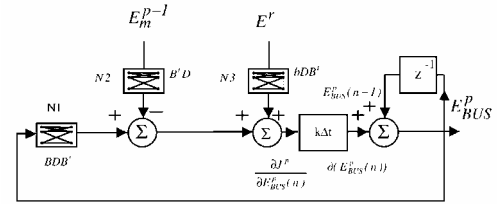


Fig. 4 A numerical form of neural network architecture for the state estimator algorithm.

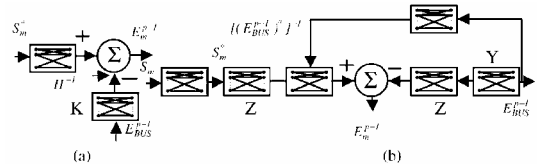


Fig. 5 A part of the neural network for the power flow state estimator algorithm.

The time n is incremented in successive passes of the principal loop in Fig. 4 until a convergence criterion is fulfilled. Convergence of the loop in the discrete form of Fig. 4 corresponds to the dynamic feedback loop in the continuous time system of Fig. 3 setting to a steady solution. The term of \mathbf{E}_m^{p-1} seen in Fig. 4 is not measurement matrix obtained directly from an energy system therefore S_{mi} must be used instead of \mathbf{E}_m^{p-1} with the help of Eqs. (6) and (7). The overall neural network for the state estimator algorithm is shown in Fig. 5. The global feedback loop relates to the state estimator algorithm in which the step counter p is incremented at the each iteration. The local feedback loop is that of the steepest descent minimization of quadratic function J^p at each the state estimator iteration. It can be obtained the overall the neural network state estimator algorithm in Fig. 6 by substituting Fig. 5 into Fig. 4. The term of F in Fig. 6 is a shift operator advances the solution from one state estimator iteration step to the next. After stopping the whole algorithm, \mathbf{E}_{BUS}^p is used to calculate 'the best estimate' for line flows by using Eq. (5). If the convergence is obtained at p th. iteration, the best estimate for line flows will be (flowing from i to k bus).

$$S_{mik} = E_i^p \left[\frac{E_i^{*p} - E_k^{*p}}{Z_{ik}^*} + Y_{ii}^* E_i^{*p} \right] \quad (25)$$

In the local feedback loop in which the quadratic function

J^p is minimized at each the state estimator iteration, checking for convergence in the loop is based on the criterion:

$$|\text{Re}(d(\mathbf{E}_{\text{BUS}}^p(n)))| \leq \varepsilon_1 \quad (i = 1, 2, \dots, N-1) \quad (26)$$

$$|\text{Im}(d(\mathbf{E}_{\text{BUS}}^p(n)))| \leq \varepsilon_1 \quad (i = 1, 2, \dots, N-1) \quad (27)$$

Through the global feedback loop of Fig. 6, the overall the state estimator solution is advanced through successive the state estimator given in ref. [5] iterations to convergence. Checking the convergence of this loop is based on:

$$|\text{Re}(\Delta \mathbf{E}_{\text{BUS}}^p)| \leq \varepsilon \quad (i = 1, 2, \dots, N-1) \quad (28)$$

$$|\text{Im}(\Delta \mathbf{E}_{\text{BUS}}^p)| \leq \varepsilon \quad (i = 1, 2, \dots, N-1) \quad (29)$$

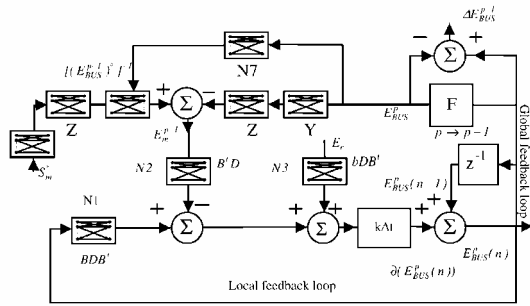


Fig. 6 The neural network state estimator algorithm.

For a sparse power network where there are few branches connected to each power network node, the structure of neural networks N1, N2, N3 is also sparse one. There are few connections to each neural network node.

5. OBTAINING THE NEURAL NETWORKS

Let us assume that real and reactive line flows are measured at both ends of three transmission lines on the power transmission system with three-bus and three-line in Fig. 7. The set of power flow measurements, using a Sbase MVA floating voltage base are $S_{m12}, S_{m21}, S_{m13}, S_{m31}, S_{m23}$ and S_{m32} . Z_{31}, Z_{23} and Z_{12} are complex impedances of the lines. The subscripts ik of S_{mik} are shown the direction of power flow measurement from bus i to bus k . The line charging and shunt terms are negligible. Consider each line flow measurement to perform with c_1 accurate meters with S_{base1} MVA, S_{base2} MVar full-scale (FS) range, and c_2 transducer errors. The weighting factor for measurement from bus i and bus k computed using Eq. (2) is

$$W_{ik} = \frac{1}{L_{ik}^2}, \quad L_{ik} = \left[\frac{c_1}{S_N} |S_{mik}| + \frac{c_2}{S_N} (\text{FS}) \right] \quad (30)$$

$$= c_1^* |S_{mik}| + c_2^*$$

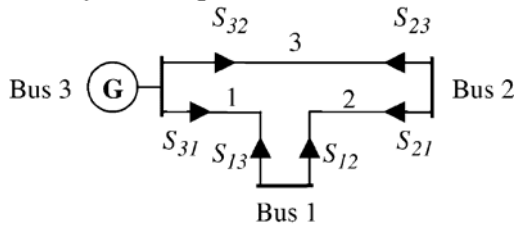


Fig. 7 The three-bus system.

All voltages be 1.0 in the product H^*WH , so the diagonal weighting matrix is

$$D = \text{diag}[d_{31}, d_{13}, \dots, d_{32}] \quad (31)$$

$$= \text{diag} \left[\frac{W_{31}}{|Z_{13}|^2}, \frac{W_{13}}{|Z_{13}|^2}, \frac{W_{12}}{|Z_{12}|^2}, \frac{W_{21}}{|Z_{12}|^2}, \frac{W_{23}}{|Z_{23}|^2}, \frac{W_{32}}{|Z_{23}|^2} \right]$$

$$B^t = \begin{bmatrix} -1 & 1 & -1 & 1 & 1 & -1 \\ 0 & 0 & 0 & -1 & 0 & 0 \end{bmatrix} \quad (32)$$

c_1^* and c_2^* are constant, S_N is selected to normalize subsequent numerical quantities. The most important neural networks of Fig. 6 are N1, N2 and N3 and their weightings calculations are also important. In this section, firstly we will find the weighting coefficients of N1, N2 and N3 for the three-bus system and then obtain the general form of the neural networks with the help of the result of the example. Let

$$C = B^t D B = \begin{bmatrix} d_{31} + d_{13} + d_{12} + d_{21} & -d_{12} - d_{21} \\ -d_{12} - d_{21} & d_{12} + d_{21} + d_{23} + d_{32} \end{bmatrix} \quad (33)$$

In general form of the elements of C :

$$C(i, i) = \sum_{j=1, (i \neq j)}^N (d_{ij} + d_{ji}) \quad (34)$$

$$C(i, j) = -(d_{ij} + d_{ji}) \quad (35)$$

Neural network N1 has $2(N-1)$ input nodes and $4N-3$ output nodes in Fig. 8. Weighting coefficients for interconnections in N1 are given by the diagonal and off-diagonal element values of matrix C . The matrix product $B^t D$ is

$$B^t D = \begin{bmatrix} -1 & 1 & 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 & 1 & -1 \end{bmatrix}$$

$$D = \begin{bmatrix} -d_{31} & d_{13} & d_{12} & d_{21} & 0 & 0 \\ 0 & 0 & -d_{12} & d_{21} & d_{23} & -d_{32} \end{bmatrix} \quad (36)$$

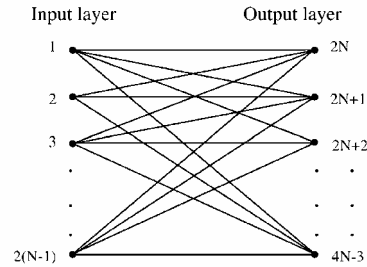


Fig. 8 General structure of two-layer neural network N1.

If a measurement is at the near end of a line connected to bus i , d_m is positive, and negative for a measurement at the far end of a line to this bus. Assume that $X^p = B^t D E_m^p$ and using Eq. (36):

$$X_i^p = \sum_{j=1}^{n-1} (d_{ij} \text{ or } -d_{ji}) E_{mij}^p \quad (37)$$

Neural network N2 has $2M$ input nodes and $2(N-1)$ output nodes. The matrix product bDB^t is

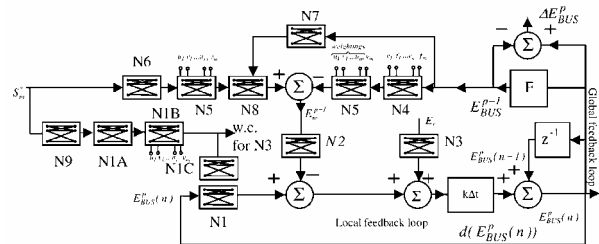


Fig. 9 Overall neural networks for the power flow state estimator.

$$\begin{aligned}
 bDB^t &= \begin{bmatrix} 1 \\ -1 \\ 0 \\ 0 \\ -1 \\ 1 \end{bmatrix} \text{diag}[d_{31}, d_{13}, \dots, d_{32}] \\
 &\times \begin{bmatrix} -1 & 1 & 1 & -1 & 0 & 0 \\ 0 & 0 & -1 & 1 & 1 & -1 \end{bmatrix} \\
 &= \begin{bmatrix} -d_{31} & -d_{13} \\ -d_{23} & -d_{32} \end{bmatrix}
 \end{aligned} \tag{38}$$

Assume that $Q = bDB^t E_r$ and using Eq. (38)

$$Q_i = E_r \sum_{j=1}^{n-1} (d_{ij} \text{ or } -d_{ji}) \tag{39}$$

Neural network N3 has two input nodes and $2(N-1)$ output nodes. Let us assume that

$$E_i^p = g_i^p + jh_i^p \tag{40}$$

$$Y_{ii} = e_{ii} + jf_{ii} \tag{41}$$

$$Z_{ik} = u_{ik} + jv_{ik} \tag{42}$$

$$S_{mik} = a_{ik} + jb_{ik} \tag{43}$$

It is applied to Eqs. (40)–(43) to Eq. (6) to obtain N4, N5, N6, N7 and N8 neural networks. To calculate the right hand side of Eq. (6), firstly

$$E_i^p Y_{ii} = g_i^p e_{ii} - h_i^p f_{ii} + j(g_i^p f_{ii} + h_i^p e_{ii}) \tag{44}$$

is obtained and then Eq. (44) is multiplied with Z_{ik} . N4 have $2M$ input nodes and $2M$ output nodes. The weightings of N5 are real and imaginer parts of Z_{ik} 's. Both N5 and N6 have $2M$ input nodes and $2M$ output nodes. Neural network N7 has $2(N-1)$ input nodes and $2(N-1)$ output nodes. Neural network N8 has $2M$ input nodes and $2M$ output nodes. Neural network N9 has $2M$ input nodes and $2M$ output nodes. The overall neural network for the state estimator is given in Fig. 9.

S_m and E_r are power flow measurements. It is interesting to quantify the total computation time of the neural system of Fig. 9. For this purpose, NG is used for the total number of the state estimator iterations and t_{N1} – t_{N9} are used for the computation times in solution in neural networks N1–N9, respectively. If $NL(p)$ is the number of iterations in the local loop which minimizes quadratic function J^p at each state estimator step and T is the total computing time of the complete architecture of Fig. 9.

$$\begin{aligned}
 T &= NG(t_{N2} + \max(t_{N5} + t_{N4}, t_{N8} + t_{N7})) \\
 &+ \sum_{p=1}^{NG} NL(p) t_{N1}
 \end{aligned} \tag{45}$$

In Eq. (45)

$$\max(t_{N5} + t_{N4}, t_{N8} + t_{N7}) = t_{N5} + t_{N4} \text{ if } \tag{46}$$

$$(t_{N5} + t_{N4}) > (t_{N8} + t_{N7})$$

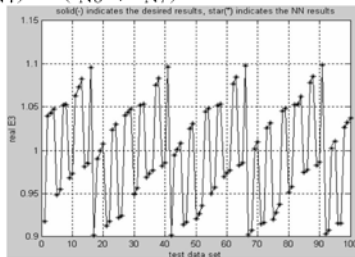


Fig. 10. The real part of the voltage of the bus 3

And

$$\max(t_{N5} + t_{N4}, t_{N8} + t_{N7}) = t_{N8} + t_{N7} \text{ if } \tag{47}$$

$$(t_{N8} + t_{N7}) > (t_{N5} + t_{N4})$$

t_{N1A} – t_{N1C} , t_{N3} – t_{N5} , and t_{N9} are not used in Eqs. (45)–(47) as the times are only valid for the measurements. The total computing time, T , is independent from the times.

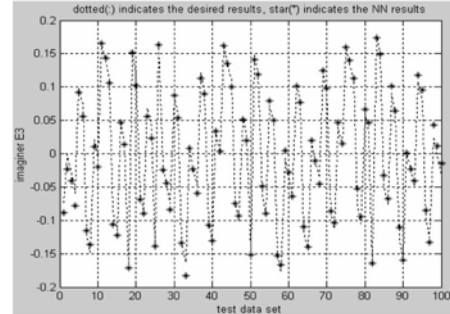


Fig. 11 The imaginer part of the voltage of the bus 3.

6. CONCLUSION

Using the procedure developed here, power system the power flow state estimator analysis maps into an array of neural networks of general structure. Network sparsity is Fig. 11. The imaginer part of the voltage of the bus 3. preserved completely. In each of the neural networks in the complete the state estimator architecture, the pattern of cross-connections between input layer and output layer nodes is that of the sparse Z , Y , H , A and D matrices of the state estimator formulation. The complete neural network architecture is robust.

For hardware requirements, as they relate to the total number of internal connections, the architecture developed here preserves in its structure the pronounced sparsity of power networks for which state estimator analysis is to be carried out. Several different neural network chips are now available mainly in forms intended for particular areas of application. More general systems now at an advanced stage of development have recently been reported [7].

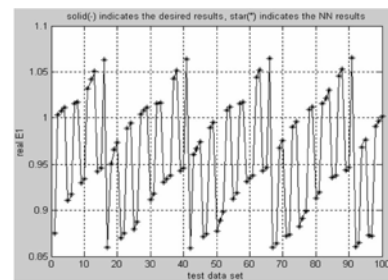


Fig. 12 The real part of the voltage of the bus 1.

The principal context of the research reported here is that of real time control in integrated power network system with particular reference to the SCADA functions. It is here where the ultrahigh-speed of massively parallel computing in neural networks can offer major practical benefit.

7. THE RESULTS OF THE SAMPLE SYSTEM AND THE EVALUATIONS

In this paper, the three-bus and three-line power transmission system shown in Fig. 7 is considered and studied. The line impedance values of the sample system, using 100MVA

floating voltage base are given in Table 1. An each line-flow measurement is performed with $c_1 = 0.02$ or 2% accurate meters with 100MW, 100MVar full-scale range, and $c_2 = 0.005$ transducer errors.

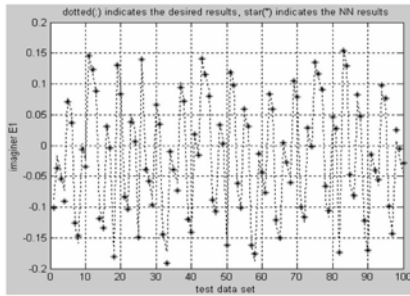


Fig. 13 The imaginer part of the voltage of the bus 1.

Table 1

Line number	Buses	Impedance (pu)
1	1-3	$Z_{13} = 0.02 + j0.06$
2	1-2	$Z_{12} = 0.08 + j0.24$
3	2-3	$Z_{23} = 0.06 + j0.18$

It is purposed to realize by using a multi-layer feed-forward NN architecture the estimation of the bus voltages without measuring at some buses where it is difficult to measure the active and reactive powers in the power systems in this paper.

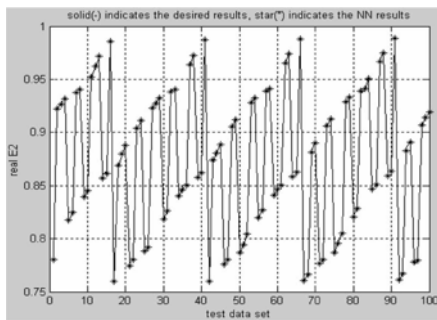


Fig. 14 The real part of the voltage of the bus 2.

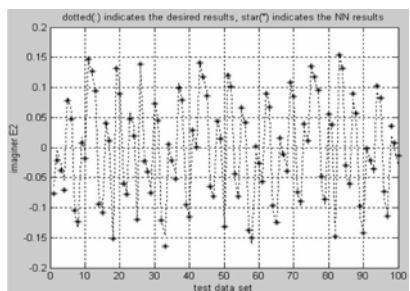


Fig. 15 The imaginer part of the voltage of the bus 2.

The two-layer neural network (NN) structure is trained for data set of the number of one thousand by changing the amplitude of bus 3 (slack bus) in the range (0.9 pu) and (1.1 pu) and the angle of bus 3 in the range of (.10.) and (10.) as random. The NN structure is trained by using the Levenberg–Marquardt algorithm. After training, the performance of the NN is shown by applying the test data set of the number of one hundred. While this data set is made up, the active and reactive powers of the buses 2 and 3 are applied as the NN inputs and the real and imaginer parts of the voltages of the buses 1, 2 and 3 are obtained as the NN outputs.

Consequently, the NN structure has eight inputs (i.e. the active and reactive parts of S_{12} , S_{21} , S_{13} and S_{23}), six outputs (i.e. the real and imaginer Fig. 15. The imaginer part of the voltage of the bus 2. parts of E_1 , E_2 and E_3), one hidden layer, and 32 neurons in the hidden layer. Thus, it is shown that E_1 , E_2 , and E_3 values are estimated without measuring the active and reactive power of bus 3 (slack bus) and the measurement is saved.

As it is shown in Figs. 10–15, the estimation performance of the NN for the real parts of the bus voltages is nearly perfect and the estimation performance of the NN for the imaginer parts of the bus voltages is fairly well. Our ongoing researches in the next stage are the power flow state estimator analysis of the large power systems and integrated ac–dc power systems via the more different multi-layer NN architecture. In near future, it is studied to make up for a different paper by our.

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