A Study on Performance Requirements for Advanced Alarm System

Duk-Hyun Seong*,Gwi-Sook Jang** Jae-Hoon Jeong*,Young-Rok Sim*,Jong-Hyun Ko*,Geun Ok Park**,Jung-Seon Kim* Samchang Enterprise Co.* Korea Atomic Energy Research Institute** <u>sdh1331@samchang.com</u>

1. Introduction

A design goals of advanced alarm system is providing advanced alarm information to operator in main control room. To achive this, we applied computer based system to Alarm System. Because, It should apply data management and advanced alarm processing(ie. Data Base Mangegment System and S/W module for alarm processing). These are not impossible in analog based alarm system. And, preexitance research examples are made on digital computer[1][2][3][4][5].

We have digital systems for test of advanced alarm system(See Table 1) and have tested and studied using by test equipment in the view point of the system performance, stability and security.

In this paper, we discribed about general software architecture of preexitance research examples. Also, CPU performance and requirements of system software that served to accommodate it, stability and security.

2. Preexitance research

This section is discribed about software architecture of preexitance research and their advantage. It have structure that data and alarm process are seperated. Specially, data is descripted by logic language(predefined), and analized by paser at excution time. And it made out text mode.

2.1 Profitable of Preexitance System

S/W architecture of preexitance instances are very profitable in following side.

- Independency for specification tool.
- Software Development and Maintenance
- Conciseness of the implementation of the requirements for alarm logic
- Conciseness of the indedent re-view for alarm logic

It has independent for a specification tool. Because its data is made up of text. So it needs simple text editor when demanded data exchange. Basically, It is good software structure that structure that data and procedure are seperated structure. Because, it is concise and best method that can minimize error of software[7][8]. Naturally, development of the software and maintance come to be easy.

There are others profit like as, software developer doesn't need to understand alarm logic and alarm logic developer doesn't need to understand software. Only, they achieve V&V in each area. These advantage is applied as it is to independent re-viewer.

3. Requirements for Advance Alarm System

In this section, we describe about requirements of low-level system services in the view point of the performance, stability and security.

3.1 CPU Performance

Because of advantages that describe in front, it is appropriate that apply structure such as preexitance research example to advanced alarm system. So we had performance test using serveral CPU(and serveral operating system), to confirm CPU performance necessary to accommondate it. For test, we used 300 simple logic(eg. S000>0.4 & S001>0.5 & S002 >0.3 & S003 >0.6, etc) and 1, 000 input data. Table 1 is result of test.

Table 1 Result of Performance Test

CPU	OS	Languag e	Compiler	Performance (ms)
TI TMS320C40 (30MHz)	N/A	Assembl y & C	Asm30 5.11, Cl30 5.11	176~178
Intel Pentium I (150MHz)	MS- DOS	С	Borland Trubo C 2.01	27.47
HP PA-7000 (440MHz)	HP-UX	С	HP C Compiler	4.367~4.654
Intel Pentium IV (2.6GHz)	Win- XP	С	MS C Compiler 12.0	2.498~2.692

It shows low performance in the case of TMS320C40 and Pentium I processor. Because the their character is so low clock to the alarm logic processing. Advanced alarm system needs high performance CPU.

3.2 System Software Requirements for Real-Time Multitasking

Advanced alarm system should be process various functions in concurrent(Figure 1). So, it needs system software which satisfy multi-tasking and real-time characters for stability and security of advanced alarm system.



Figure 1 Functions of Advanced Alarm System

System software is satified multi-tasking and realtime, needs following features basically.

- semaphore/memory locking
- IPC(shared memory, signal, event etc.)
- Pre-emptition. priority scheduling

Specially, in case of signal for IPC, have been apt to lose. So, system software should be supply solution for signal lost. And it must deny nesting interrup by same signal.

3.2 Hardware Requirement for System Software in Stability and Security

Important feature for stability of system software, it is important that protect system software from application software. What is called, Protection-mode. Process protection is method that separate excution area of system software and application software, and guarantee this by hardware in CPU. If application approaches to system software area, CPU breeds interrupt for protection and system software is protected from un-permitted applicaton software.

The other important feature for stability and security of system is memory management function. Usually, this function is implemented by built-in MMU(Memory Management Unit) to CPU. MMU is important in the viewpoint of the system stability. Because, it offers protection function between process. Main reason which raises a software failure is the approach to the not to be permitted process area. Such problem is more serious when directly memory approach possible like C or Assembly. And, such weakness is fatal in the side of system security. Because, advanced alarm system used commercial network. System hacking instances are examined by us in commercial network, there are many instance that use weakness of memory management like buffer over flow. Because, hackers can approach to system software using buffer over flow.

3. Conclusion

In the case of preexitance researchs, they have same software structure that data and procedure are seperated. We described advantage of seperated structure and confirmed serveral features to apply advanced alarm system using by several digital system.

The features are:

- CPU performance for alarm logic parsing
- System software requirements for real-time multitasking
- Hardware requirement for System software in stability and security

As result of test, we need system software that have functions of IPC for real-time mult-tasking. Specially, It should be have anti-signal lost and same signal blocking functions. And we need CPU that have functions of process protection and memory management unit.

We're going to apply upper features to advanced alarm system design. And we are expect that advanced alarm system may become more than system in performance, stability and security side.

REFERENCES

[1]KAERI/RR-1608/95, Development of the Operational Information Processing Platform, KAERI

[2]KAERI/RR-1503/94, The Development of Basic Technology for Instrumentation and Contorl, KAERI

[3]KAERI/RR-1754/96, The Development of Basic Technology for Instrumentation and Contorl, KAERI

[4]KAERI/CM-110/96, Development of Dynamic Alarm Processing System Algorithm and Evaluation of Alarm System Reliability, KAERI

[5]KAERI/RR-2125/2000, Integrated ADIOS-INGENPRO Operator Advisory Support System, KAERI

[6]KAERI/TR-2764/2004, Development of Alarm Cause Tracking System for Korea Standard Nuclear Power Plant, KAERI

[7]Eric S. Raymond, The Art of UNIX Programming, Addison-Wesley Professional, 2003

[8]TMS320C4x User's Guide, Rev. C, Texas Instrument, 1999 [9]Donald Ervin Knuth, The Art of Computer Programming, Addison-Wesley, 1997

[10]KEAEI/RR-1901/98, Development of MMIS Design Technology for Integral Reactor, KAERI.