

# Electromigration and Electrochemical Migration in Electronic Package

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## **Electromigration and Electrochemical Migration in Electronic Package**

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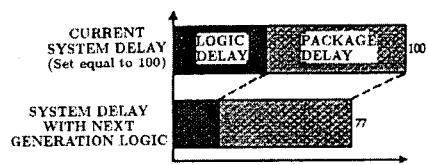
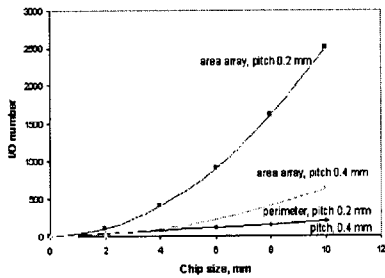
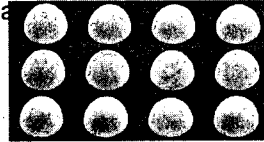
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Introduction

### Application to flip chip

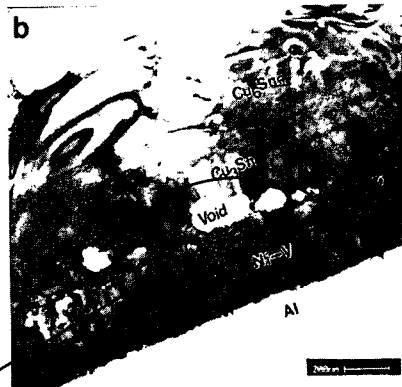
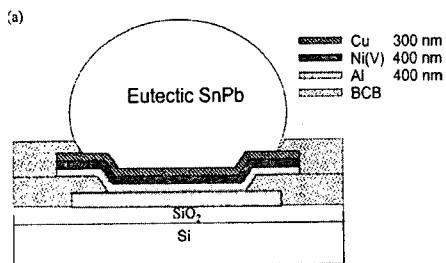
- Smaller size : Increased I/O #(area array) enables more signal and functionality
- Improved performance : Low electrical delay
- Improved reliability
- Low cost : regardless # of I/O



50% IMPROVEMENT IN LOGIC PERFORMANCE FOR A SYSTEM RESULTS IN MUCH SMALLER OVERALL GAINS, UNLESS PACKAGE DELAY IS ADDRESSED

Introduction

### Void formation of flip chip solder bump



Kirkendal voids

Liu, et al., JAP (2000)

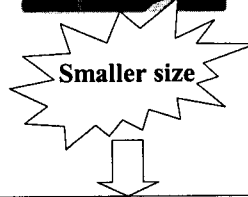
Introduction

## Flip chip size

International Technology Roadmap for Semiconductors, 1999 edition

Bump Pitch	Passivation Opening
250 $\mu\text{m}$	85 $\mu\text{m}$
200 $\mu\text{m}$	80 $\mu\text{m}$
150 $\mu\text{m}$	65 $\mu\text{m}$

2005 year



Higher current density

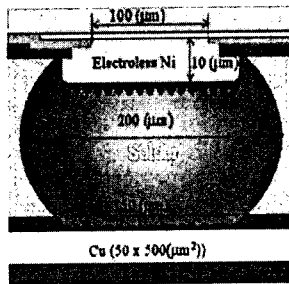
Table 60 Current Limits of 63Sn/37Pb Flip Chip Solder Bumps

Bump Pitch	Passivation Opening	Current Limits for 100,000 hour MTF at Average Bump Temperatures of		
		100°C	90°C	80°C
250 $\mu\text{m}$	85 $\mu\text{m}$	75 mA	110 mA	165 mA
200 $\mu\text{m}$	80 $\mu\text{m}$	66 mA	97 mA	145 mA
150 $\mu\text{m}$	65 $\mu\text{m}$	45 mA	65 mA	95 mA

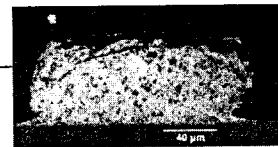
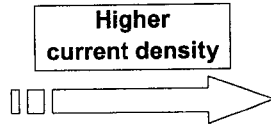
International Technology Roadmap for Semiconductors, 1999 edition

Introduction

## Electromigration in solder bump



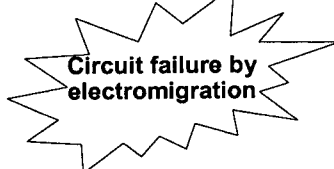
Smaller size



Choi, et al., JAP (2003)

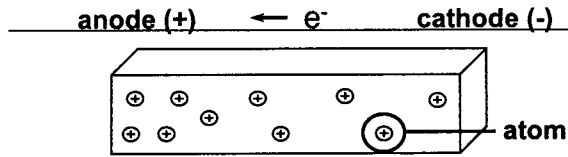


S. Brandenburg, et al. (1998)



Introduction

## What is electromigration?



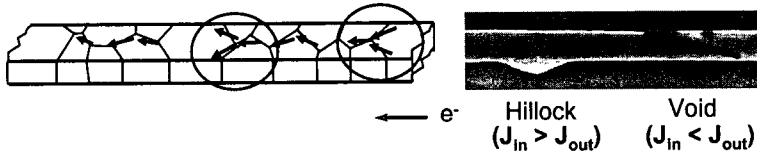
- Atom transport due to momentum exchange between conduction electrons and metal ions
- Atoms migrated toward electron flow direction



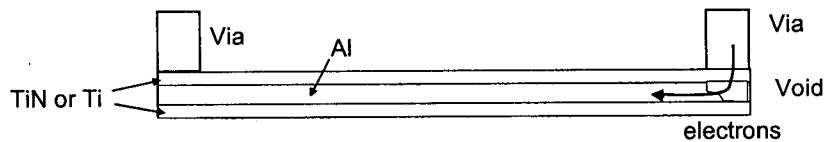
Introduction

## EM site – flux divergence in interconnects

- Electromigration-Induced Failures
  - EM damage occurs at the site of flux divergence ( $J_{in} \neq J_{out}$ )
  - Sites of flux divergences in lines:
    - 1) Local grain structure variation (grain boundary triple points, grain clusters - bamboo grains)

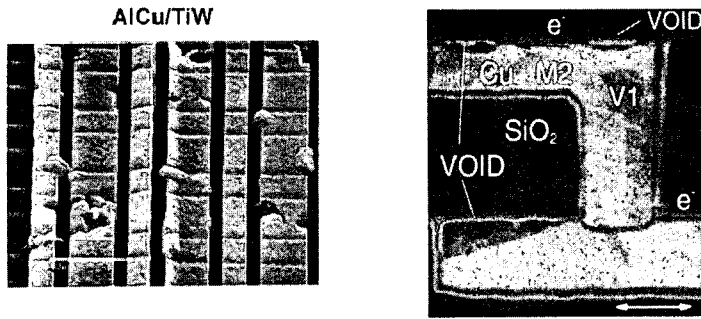


- 2) At the vias (materials are discontinuous due to W via or barrier layer (TiN, Ta)) => Most dominant failure site



Introduction

EM failure in chip interconnect



C. K. Hu, et al., APL(2001)

EM failure is critical problems in chip interconnects

Introduction

Electromigration of eutectic SnPb in flip-chip

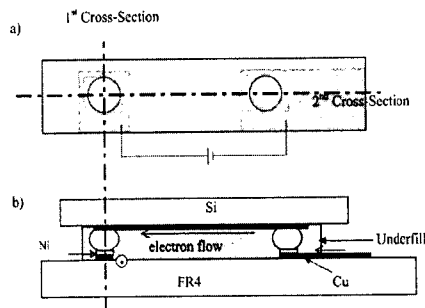


FIG. 1. Schematic diagram of the electromigration test sample (a) plane view and (b) side view.

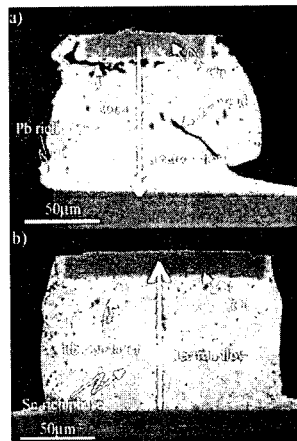


FIG. 6. SEM micrographs of (a) second cross section of cross sectioned solder ball and (b) Cross section of the entire solder ball in the underfill.

K. N. Tu, et al., JAP(2001)

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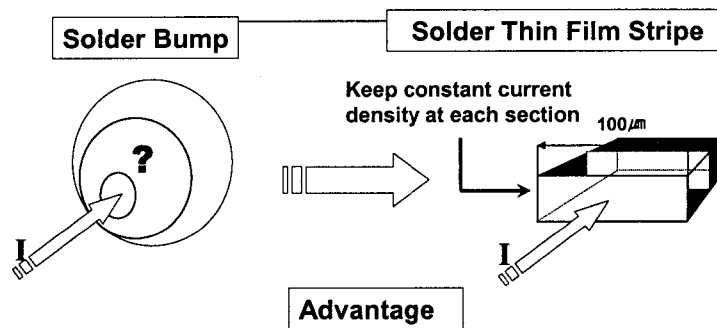
II. Electromigration of solder

III. Electrochemical Migration

IV. Summary

*Electromigration of solder*

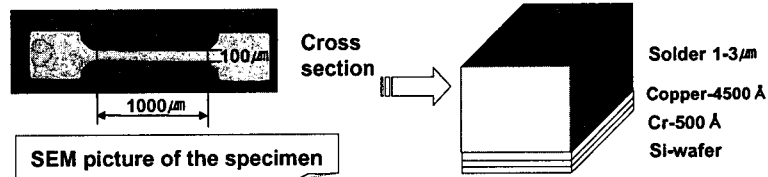
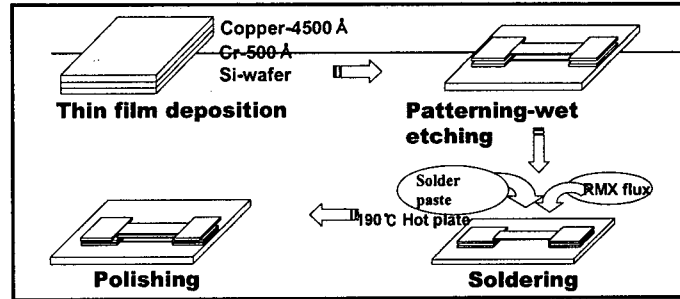
◆ Reason for making stripe specimen



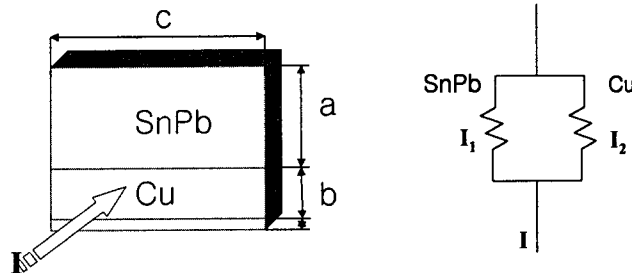
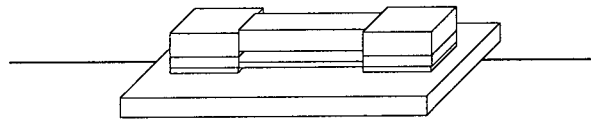
- Easy to observe the microstructure change
- Keep constant current density & joule heating at each section
- Easy to fabricate



### ◆ Fabrication of the stripe specimen



### ◆ Current density calculation

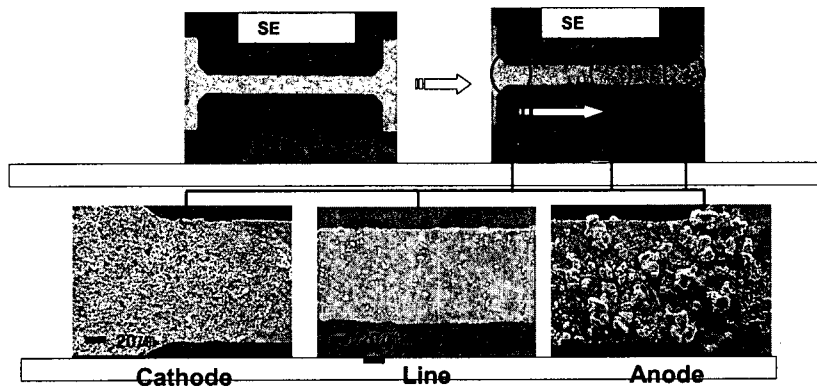


- The copper has 1/10 of resistivity in comparison to SnPb
- +
- Consider the thickness of each film
- +
- Calculate the resistance of each line & Current density of the SnPb line

Electromigration of solder

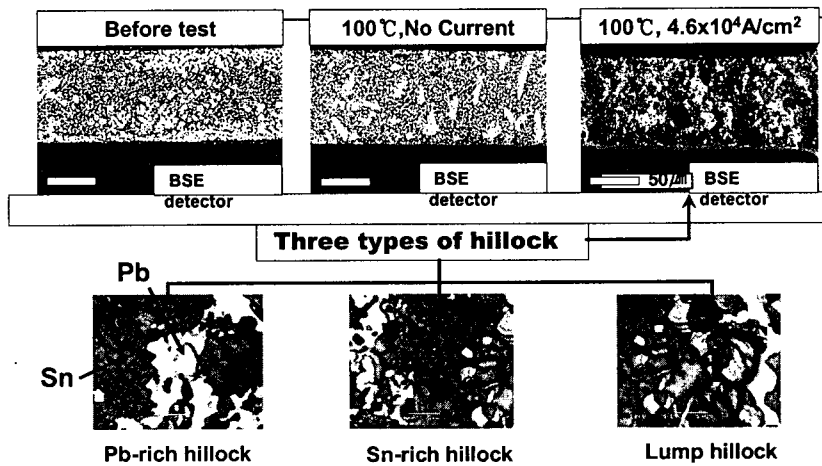
### ◇ Hillocks and voids formation

(100°C, 8.7 X10<sup>4</sup>A/cm<sup>2</sup> for 5 days)

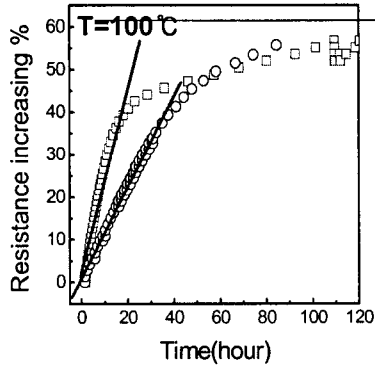


Electromigration of solder

### ◇ Microstructure change



◆ Different current density of EM in eutectic SnPb



- $8.7 \times 10^4 \text{ A/cm}^2$
- $4.6 \times 10^4 \text{ A/cm}^2$

□ Resistance increasing % =  $\frac{\Delta R}{R_{initial}} \times 100$

□ Flux equation of electromigration

$$J_{flux} = C \left( \frac{D}{kT} \right) Z^* q \rho j = C \left( \frac{D_0}{kT} \right) \exp \left( -\frac{E_a}{kT} \right) Z^* q \rho j$$

Assumption  $\Rightarrow$  Slope  $\propto J_{flux}$   
(Resistance increasing %/hour)

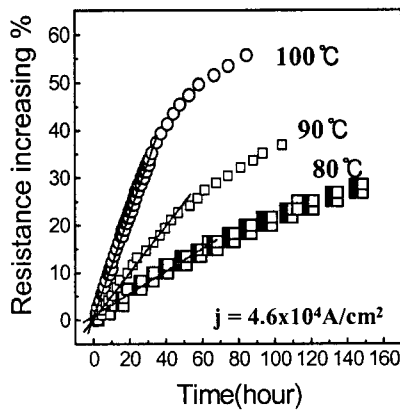
(Hummel R. international Materials Reviews (1999))

If constant temperature

$$\frac{J_1}{J_2} = \frac{j_1}{j_2} \approx \frac{Slope_1}{Slope_2} = \frac{2.3}{1.1} \cong 2.1$$

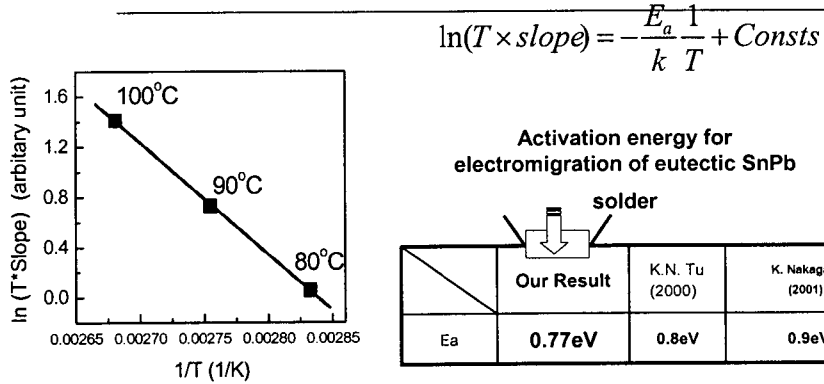
◆ Different temperature of EM in eutectic SnPb

Temperature T - variation



$$\ln(T \times slope) = -\frac{E_a}{k} \left( \frac{1}{T} \right) + Consts$$

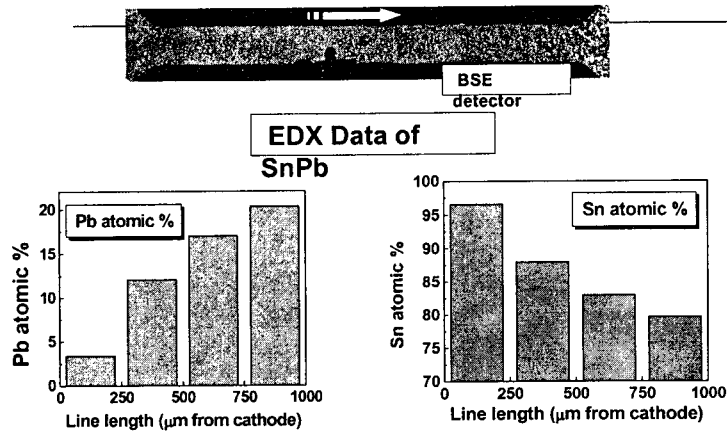
### ◇ Activation energy of eutectic SnPb



□ Can obtain the activation energy of EM with resistance change

### ◇ Dominant diffusing element in eutectic SnPb

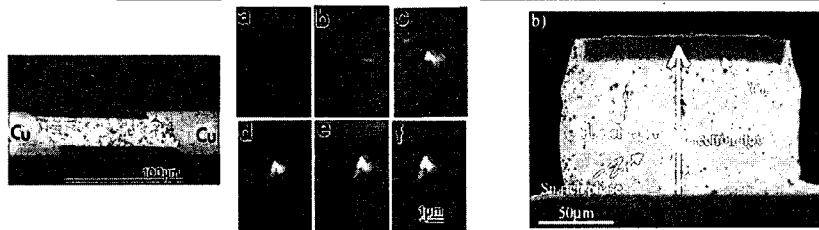
(100°C, 8.7x10<sup>4</sup>A/cm<sup>2</sup> for 5 days)



□ Pb is dominant diffusing element at 100°C

Electromigration of solder

◇ Dominant diffusing element compared to previous results



K. N. Tu, et al., APL(1999)

K. N. Tu, et al., JAP(2001)

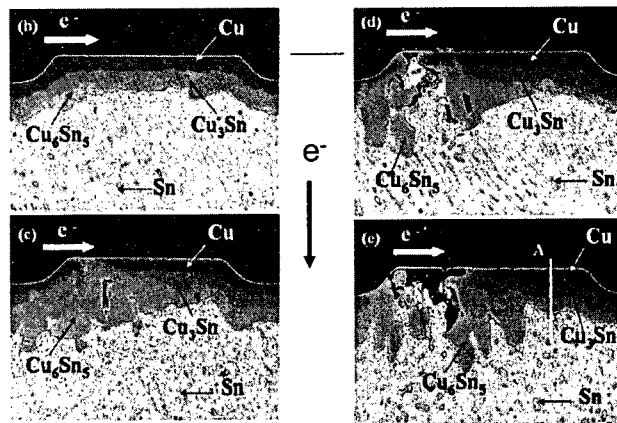
Stripe specimen

Flip chip specimen

	Our results	K.N. Tu		S. Brandenburg	K.N. Tu
Test temperature	100°C	R.T	Test temperature	150°C	100~120 °C
Dominant diffusing element	Pb	Sn	Dominant diffusing element	Pb	Pb

Electromigration of solder

◇ Dissolution of Cu between Cu UBM and SnPb solder

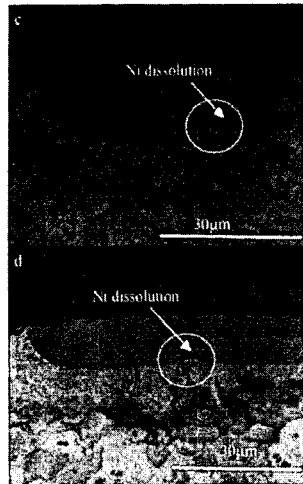


J. W. Nah, et al., JAP(2003)

- Failure due to Cu dissolution into SnPb solder
- Occurrence at the cathode significantly

Electromigration of solder

◇ Dissolution of Ni between Ni UBM and SnPb solder



□ Ni dissolution into SnPb solder as well as Cu dissolution

(T. Y. Lee, et al., JAP (2001))

Electromigration of solder

◇ Lead free solder

Eutectic SnPb solder



Lead free solder

□ Lead free alloy candidate

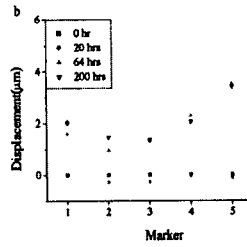
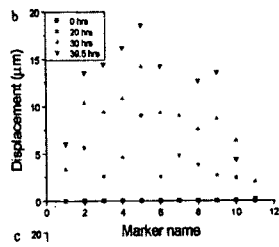
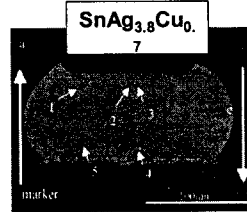
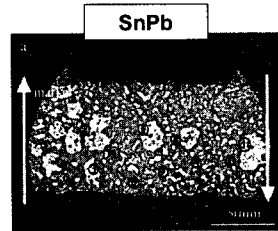
Alloy	Melting Range (C)	Metal Cost/Lb (as of 2/3/99)	Density at 25C (lbs/cubic in)	Metal Cost per in <sup>3</sup>	Patent (Yes/No)
63Sn / 37Pb	183	\$2.37	0.318	\$0.75	No
42Sn / 58Bi	139	\$3.44	0.316	\$1.09	No
77.25Sn / 20In / 2.8Ag	179-189	\$30.06	0.267	\$8.02	Yes
91Sn / 9Zn	199	\$3.23	0.263	\$0.85	No
91.85Sn / 3.4Ag / 4.8Bi	208-215	\$6.24	0.272	\$1.70	Yes
90Sn / 7.5Bi / 2Ag / 0.5Cu	186-212	\$5.09	0.273	\$1.39	No
96.25Sn / 2.5Ag / 0.8Cu / 0.55b	213-219	\$5.48	0.267	\$1.46	Yes
95.5Sn / 4Ag / 0.5Cu*	217-218	\$6.55	0.269	\$1.76	No*
95Sn / 3.5Ag / 1.5In	218	\$8.15	0.268	\$2.18	No
93.5Sn / 3.5Ag / 3Bi	216-220	\$5.92	0.269	\$1.59	No
96.5Sn / 3.5Ag	221	\$6.32	0.268	\$2.33	No
99.35Sn / 0.7Cu	227	\$3.48	0.264	\$0.92	No
95Sn / 55b	232-240	\$3.37	0.263	\$0.88	No

\* Some SnAgCu alloy compositions are covered by patents, however, this composition can generally be considered free and clear.

A Bench mark process for the lead free assembly of mixed technology PCB's (α-metal Chris basteki 1999)

Electromigration of solder

Lead free solder & SnAg<sub>3.8</sub>Cu<sub>0.7</sub>

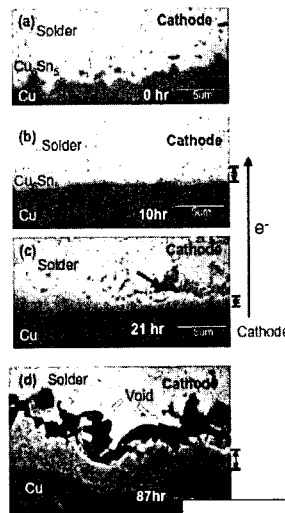
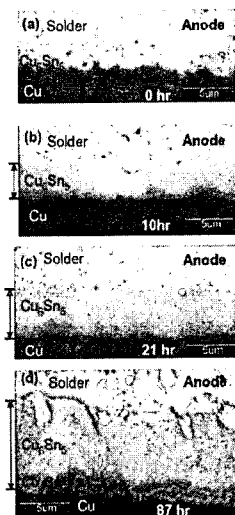


K. N. Tu, et al., JAP(2001)

Effect of EM in SnAg<sub>3.8</sub>Cu<sub>0.7</sub> is much smaller than that in eutectic SnPb

Electromigration of solder

Current polarity of IMC formation between Cu and SnAgCu

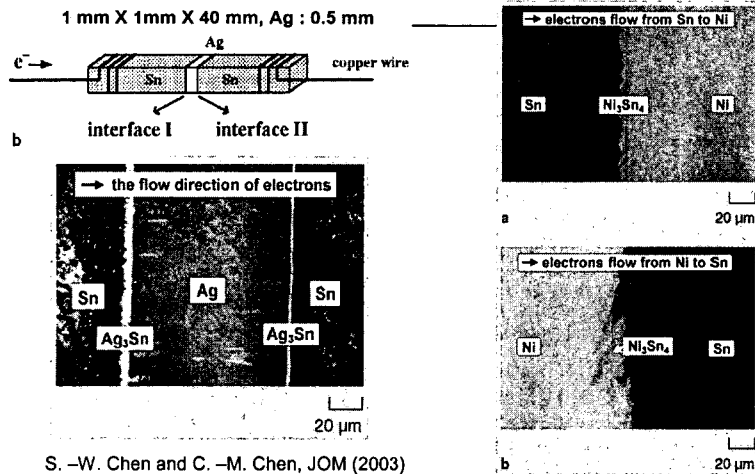


IMC thickness at cathode < at anode

K. N. Tu, et al., ECTC(2002)

Electromigration of solder

◇ Current polarity of IMC formation between Ag or Ni and Sn



S. -W. Chen and C. -M. Chen, JOM (2003)

□ Different thickness of IMC formation at cathode and anode

◇ Contents

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II. Electromigration of solder

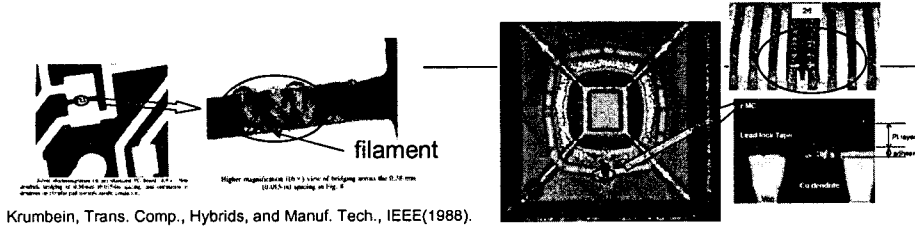
III. Electrochemical Migration

IV. Summary

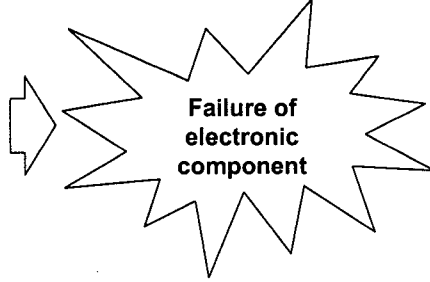


Electrochemical migration

◇ Electrochemical migration (ECM)



**Filament formation** under an applied DC voltage bias in small pattern design & harsh environments of temperature & humidity



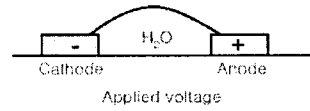
Electrochemical migration

◇ Electromigration vs. Electrochemical migration

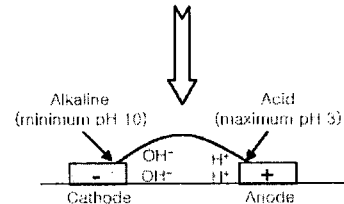
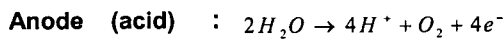
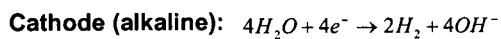
	손상 형상	
금속 내부	손상 경로	금속 사이 절연체
전류, 온도	발생 인자	전압, 온도, 습도
반도체내 금속배선 패키지 솔더 조인트	발생 전자부품	인쇄회로기판 표면 및 내부

## Mechanism of ECM

1. Water adsorption on board from the humidity in the air.



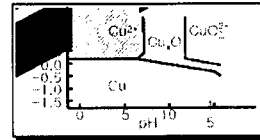
2. Changes in pH in the vicinity of the electronics.



When applied voltage = minimum 2 V DC

3. Metal ion formation at anode

Potential-pH equilibrium diagram (Pourbaix diagram)



## Mechanism of ECM

Reaction	Mechanism diagram	Acceleration factors
1. Water adsorption and diffusion		<ul style="list-style-type: none"> <li>Amount of vapor</li> <li>Temperature</li> <li>Material quality</li> </ul>
2. Changes in pH due to the electrolysis of water (acidization)		<ul style="list-style-type: none"> <li>Voltage</li> <li>Amount of water vapor</li> <li>Temperature</li> </ul>
3. Copper elution and copper ion diffusion (diffusion)		<ul style="list-style-type: none"> <li>Voltage, water vapor</li> <li>Material quality</li> <li>pH, impurity ions</li> <li>Amount of dissolved oxygen</li> </ul>
4. Electron transfer and ionic migration occurrence (reduction)		<ul style="list-style-type: none"> <li>Voltage</li> <li>Material quality</li> <li>ph, impurity ions</li> </ul>

*Electrochemical migration*



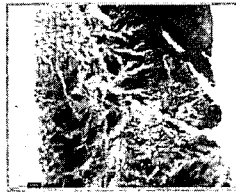
**Migration metal – Sn, Ag, Cu, etc.**

• **Significant growth potential-metals with loosely bond oxides**

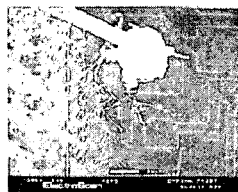
- Silver, tin

• **Lesser growth potential**

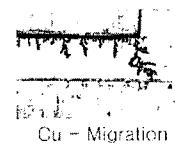
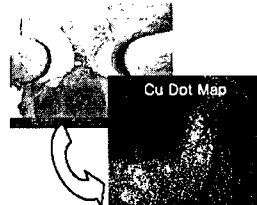
- Copper, gold, zinc, cadmium



Sn – Migration



Ag – Migration



Cu – Migration

*Electrochemical migration*

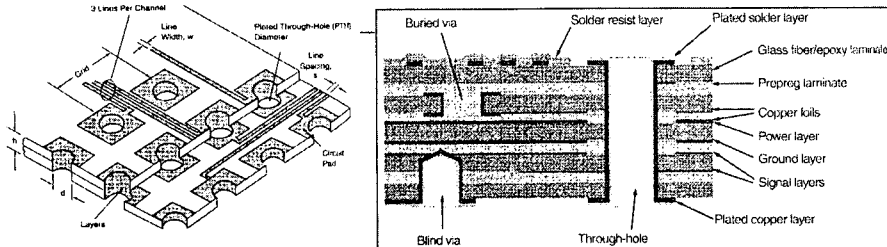


**Metallic electromigration**

- Not only silver, but also gold, copper, nickel, tin, lead**
- Migrated gold resistive shorts**
  - Failure due to bridging of narrow circuit spacing by dendritic filaments of gold in IC devices and hybrid microcircuit systems
- The copper migration in PWB**
  - Emanating from the positively biased conductors (anode)
  - Growing along the reinforcing glass fibers
  - Filament formation

Electrochemical migration

▶ A typical printed circuit board configuration



• Line Width & Line spacing

: 한 layer 위에서 pattern을 이룬 전도체의 선폭과 선 간격

• Plated Through- Hole

: layer와 layer 사이의 electrical contact 형성을 위해 drilling 후 도금처리

• Micro Via

: 상,하 두 layer 간의 electrical contact을 위해 Via 형성

Electrochemical migration

▶ PCB parameters status

Current technology 2002/2003

Attributes	Conventional	Leading Edge	State of the Art
Conductor Width and Conductor Space (min) $\mu\text{m}$	125	75	
Plated Hole Diameter (min) $\mu\text{m}$	250	200	
Microvia Diameter (min) $\mu\text{m}$	N/A	100	

IPC National Technology Roadmap Electronic Interconnections 2002/2003

◆ Failure mechanism in PCB

PCB site

**CAF (Conductive anodic filament)**

Dendritic growth

Connector corrosion

Trace corrosion

Lead Pad corrosion

Intermetallic formation

**PTH barrel fatigue**

Fiber/Resin debonding

Tg limitation

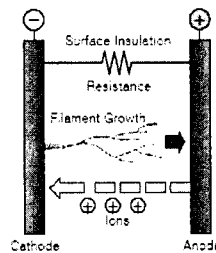
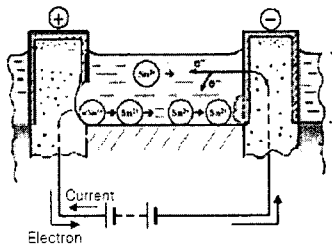
Delamination

Laminate Plasticization

◆ Dendrite Growth Migration

- Migration occurs when moisture adheres between electrodes made of materials such as copper, solder, or silver. As voltage is applied, the electrons carrying the Coulomb charge created by the ionization of the positive electrode flow towards the negative electrode. The charged electrons is reduced at the negative electrode and extends back toward the positive electrode.

◆ Pattern of Ion migration



Electrochemical migration

## ◇ Conductive anodic filament (CAF)



Anode

•CAF

-A type of electrochemical migration

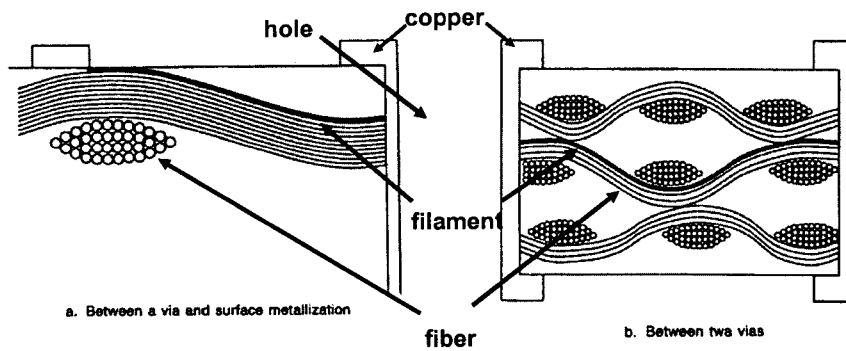
-Consist primarily of metallic conductive salt

-Being transported across a nonmetallic substrate under the influence of an applied electric field

Electrochemical migration

## ◇ Mechanism of filament formation

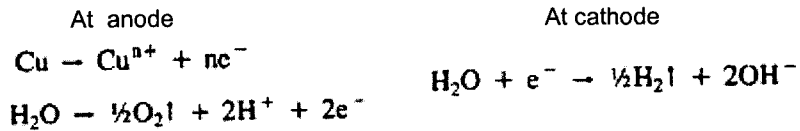
### 1. Electrochemical reaction leading to loss insulation resistance



◇ Mechanism of filament formation

2. Electrochemical reaction leading to loss insulation resistance

• Cu ion



\*Filament : copper salt (containing chloride or bromide) & copper ion

※ Water-soluble flux (WSF- glycols, polyglycols, glycol esters, poly glycol surfactants, polyols, glycerine 등) 가 PWB 제조 공정중에 resin과 fiber 사이에 스며들어 Salt 형성

◇ Mechanism of filament formation

1. Delamination at the glass fiber-epoxy resin interface (path creation)

- Epoxy resin 과 glass fiber의 TCE Mismatch
- Epoxy resin (thermal coefficient of expansion : 60ppM/ °C)
- Glass fiber (thermal coefficient of expansion : 5ppM/ °C)

2. Moisture absorption의 차이

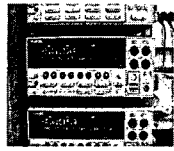
- Glass fibers 와 epoxy resin의 (temp, RH) delamination이 심해짐

3. 가공중 발생하는 delamination

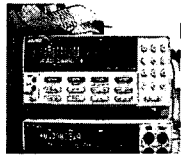
- 가공시 발생하는 물리적 충격에 의한 crack 발생

Electrochemical migration

### ECM test system



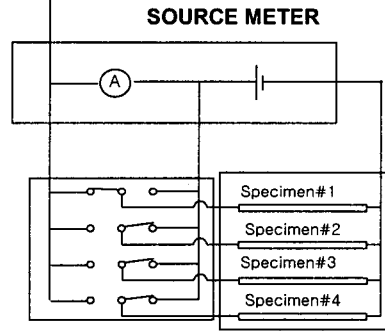
SOURCE METER



SWITCH MODULE



TEMP&HUMID CHAMBER



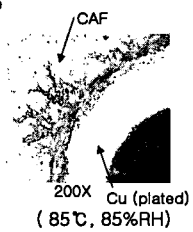
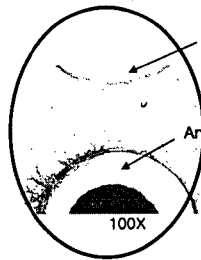
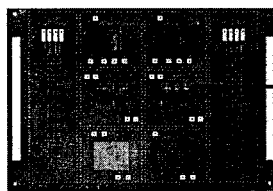
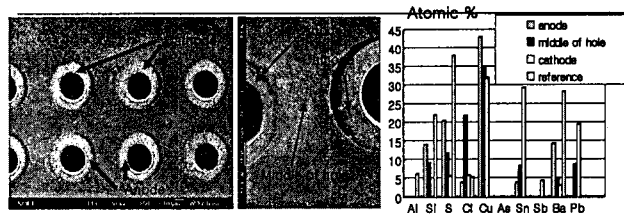
SWITCH MODULE

TEMP&HUMID CHAMBER

Electrochemical migration

### Electrochemical migration in PCB

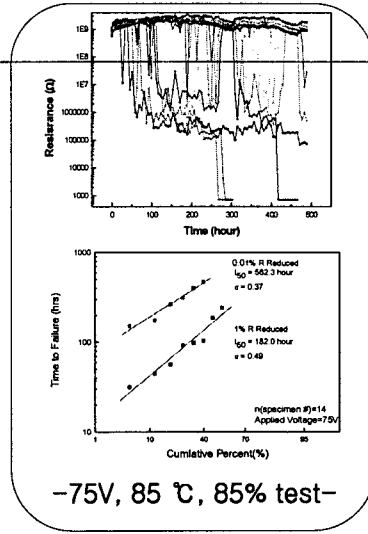
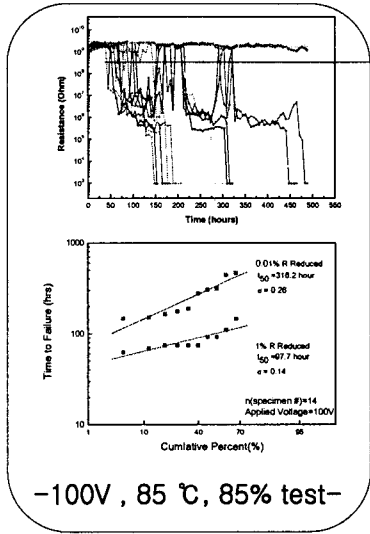
- Printed Circuit Board (PCB)





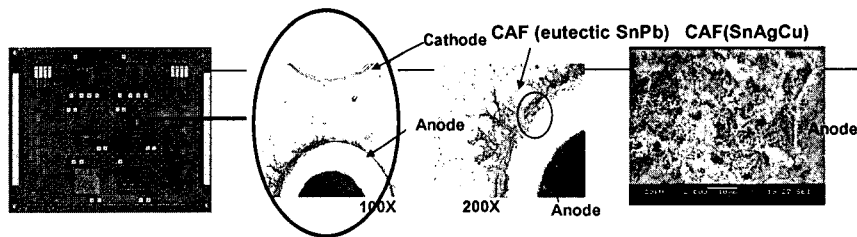
Electrochemical migration

Failure analysis



Electrochemical migration

Corrosion test



ECM에서 CAF가 절연파괴의 주된 원인  
Anode에서 Corrosion 특성과 유사한 거동 보임

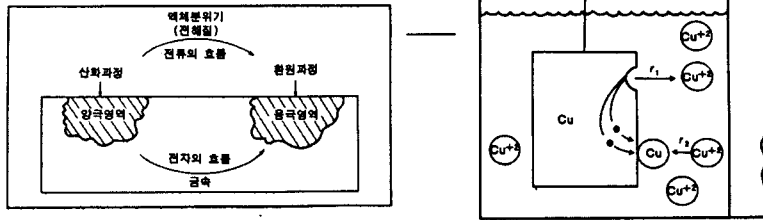
Eutectic SnPb / SnAgCu / Pure Pb / Pure Sn 부식 시험

⇒ 분극 곡선 및 부식속도 측정

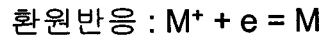
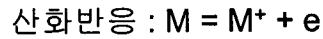
CAF 발생기구 및 소재별 차이 해석

Electrochemical migration

Corrosion phenomena



• 부식 = 산화반응(양극) + 환원반응(음극)

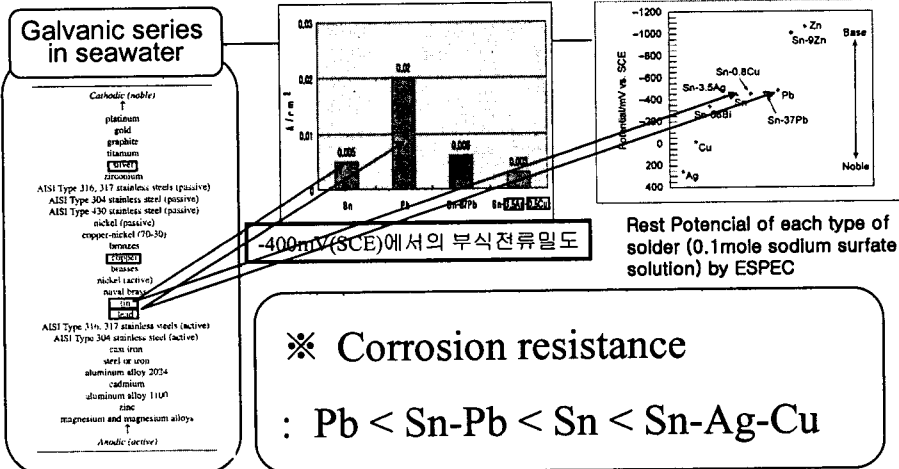


• 부식 발생시 양극과 음극 사이의 전류에 의해 전극전위 변화

→ 전기화학적 분극이 부식속도에 영향

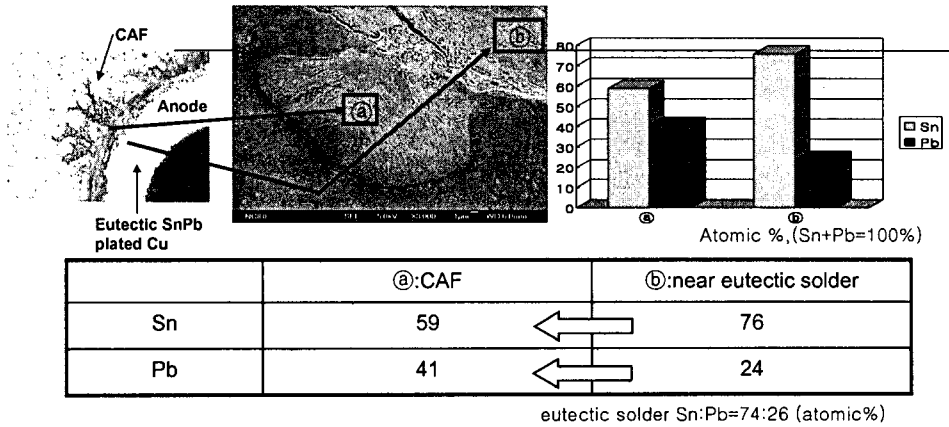
Electrochemical migration

Corrosion resistance : Sn vs. Pb



Electrochemical migration

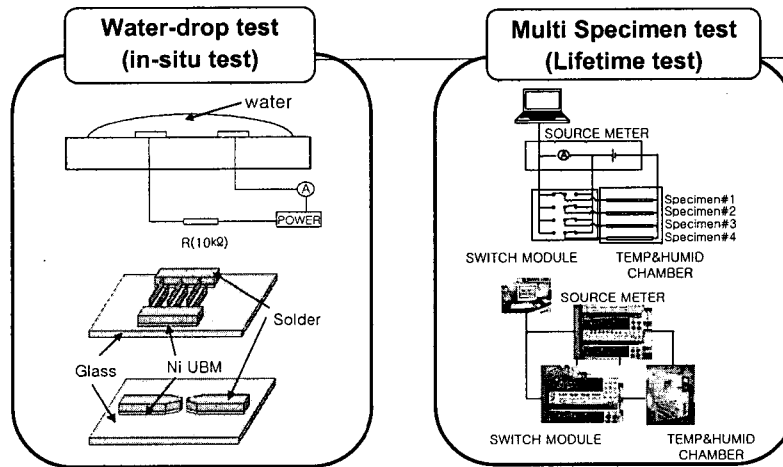
◆ Sn vs. Pb (eutectic SnPb in PCB)



**Pb is more susceptible to CAF than Sn  
⇒ Corrosion characteristics !**

Electrochemical migration

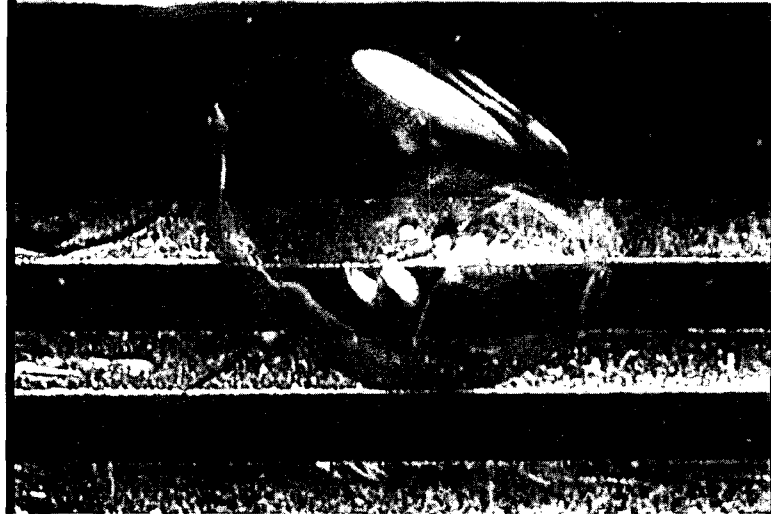
◆ Modeling test with simple structure sample



Electrochemical migration

◇ Preliminary water-drop test

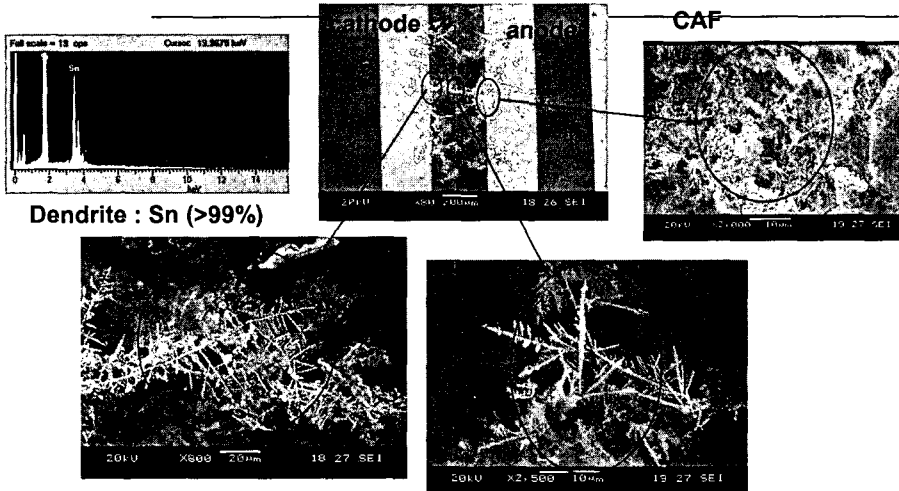
Comb pattern, spacing 300um, SnAgCu, 5V



Electrochemical migration

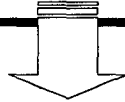
◇ Morphology after water-drop test

Comb pattern, spacing 300um, SnAgCu, 5V



◇ Summary

**Smaller size,  
Large voltage/current,  
high humidity**



**cause a new reliability threat in microelectronic  
packaging**

**Electromigration and Electrochemical Migration**