

History and Current State of Pb-free Soldering Technology in Japan

Sei-ichi Denda

(Nagano Institute of Technology/Japan)

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IMAPS Asia
Nagano Institute of Technology

S.Denda

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History of Pb-free solder development in Japan (1)

1994: (June), JIPC Pb-free solder research group started.

1996. Matsushita Photo Phase Drive SnAgBi Test Production (A)

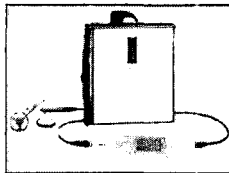
1997: (Dec.), JIPC Report published. (B)

1997 NEC PDA Mobile Gear Test Production, Sn-Ag Sn-Bi (C)

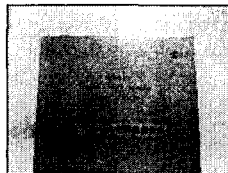
1998: JEIDA and JIEP Pb-free solder application roadmaps.

1998: (Oct.) JEIDA and JIPC research project started.

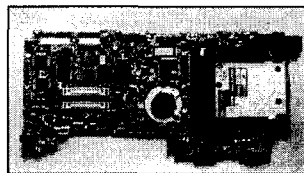
1998: Matsushita MD Player Mass Production



(A)



(B)



(C)



History of Pb-free solder development in Japan (2)

1999: (Jan.), NEDO Project started JEIDA/EIAJ in cooperation.

1999: (Oct.) Pb-free Solder Roadmap 2000 issued.

1999, Hitachi, Refrigerator

1999, NEC, Note PC, Lavie, Versa Pro, Sn-Zn, N2 reflow (D).

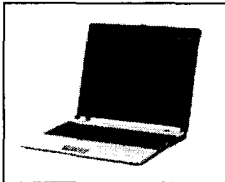
2000: JIEP Low-temp Pb-free project. Report published (E).

2000: JEITA recommended standard Sn-3Ag-0.5Cu

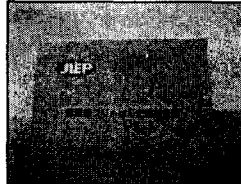
2001: Sony, VTR, DCR TRV-30, Sn-2.5Ag-0.5Cu-1Bi (F).

2003: JIS Draft making started.

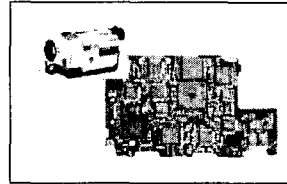
(2004: Apr. WEEE and RoHS final draft.)



(D)



(E)



(F)

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European Union Directive

WEEE: Waste Electrical and Electronic Equipment,
Aug.2005 effective

RoHS: Restriction of the use of certain hazardous
substances in electrical and electronic
equipment, July 2006 effective

Hazardous substance: Mercury, Cadmium, Lead,
Hexavalent Chromium, PBB, PBDE.

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Present state related to Pb-free solder technology (1)

- Pb-free application to the products have been completed in major electronics companies.
- Small factories or companies can't invest for new Pb-free equipments. They want solders suitable to conventional manufacturing line.
- Standard solder is Sn-3Ag-0.5Cu but other composition solders are being used.



Present state related to Pb-free solder technology (2)

- Low temperature solder is becoming popular.
- Pb-free hasn't necessarily completed for semiconductor devices and components.
- Solder of different compositions are being used for flow soldering.
- Soldering fluxes are under development in solder venders

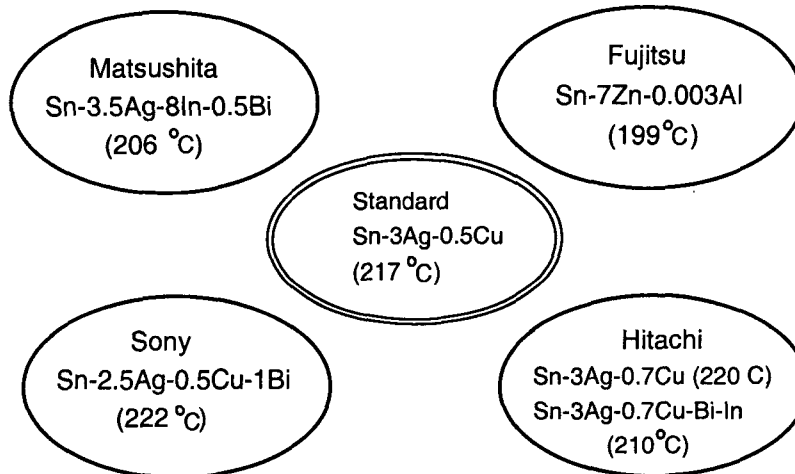


Schedule for Pb-free solder application to all of the products in major Japanese electronics manufacturers.

Company	Limit
Matsushita	2002/12
Sony	2003/3
Hitachi	2002/3
NEC	2002/10
Toshiba	2004/3
Fujitsu	2002/12
Mitsubishi	2005/12
Canon	2004/12
Oki	2003/3
Epson	2004/3



Pb-free solders of different composition in use

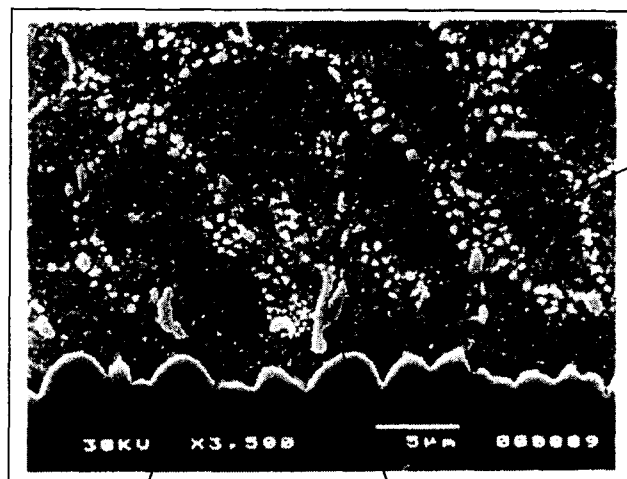


Notices for standard Pb-free solder (1)

- Standard solder (JEITA recommend),
Sn-3Ag-0.5Cu (weight %)
- Melting point 217-219 °C , 36 °C higher
than eutectic Sn-Pb solder.
- Reflow peak temperature should be 235 °C.
- Small temperature tolerance between peak
reflow and maximum component temperature.

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Typical Sn-Ag-Cu solder texture



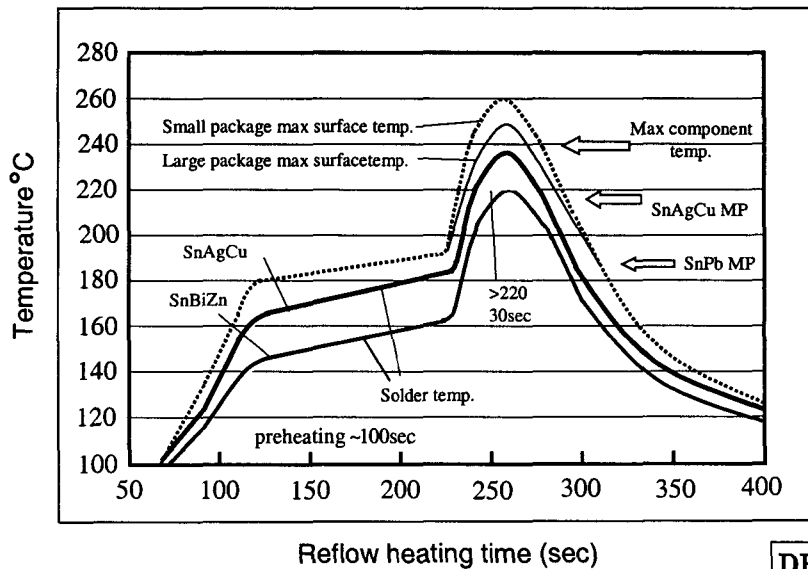
Ag₃Sn

Cu₆Sn₅

CuSn
compound

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Reflow temperature profile for Pb-free solder

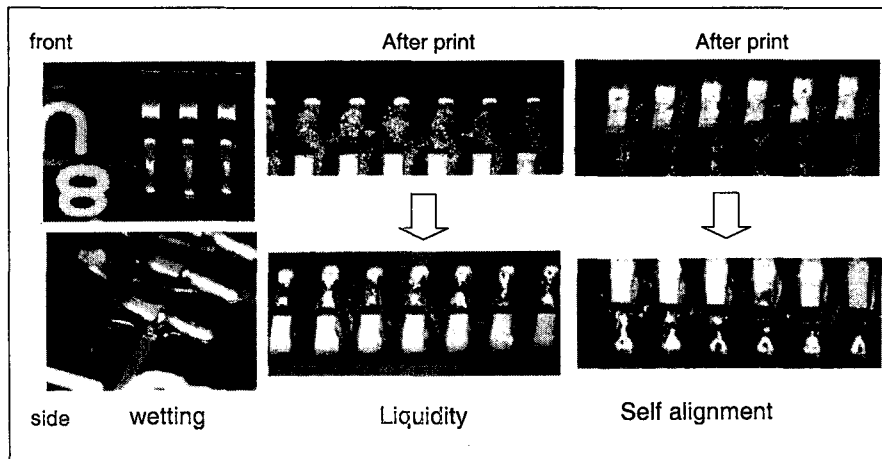


Notices for standard Pb-free solder (2)

- Pb-free solder is harder, worse in wetting and better self alignment than Sn-Pb solder.
- Chemically active to other metals.
- Possibly cause flow solder bath damage.
- Containing Bi in solder and using Pb dipped package lead may cause wiring pattern peel off and damage.

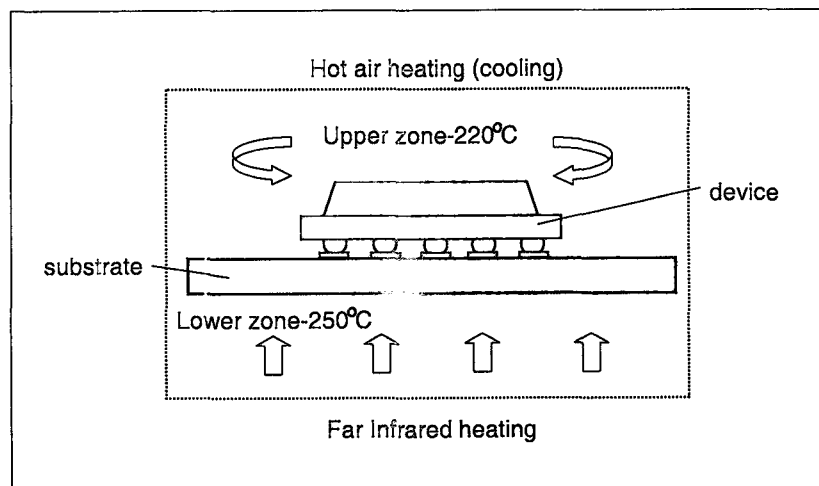
Typical Pb-free reflow soldering

Sn-3Ag-0.5Cu solder paste



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Graded heating reflow furnace



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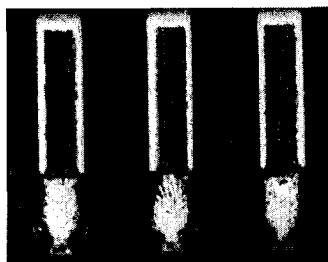
Low temperature Pb-free solder

- Under active development in JIEP and JEITA.
- Applicable to conventional soldering equipment.
- N₂ reflow atmosphere was needed for solder containing Zn so far .
- Max temperature of Intel CPU is 220°C
- Typical composition, Sn-8Zn-3Bi (197 °C)
- NEC, Sharp, Hitachi are using for note PC substrate.
- Air reflow solder paste is developed recently.

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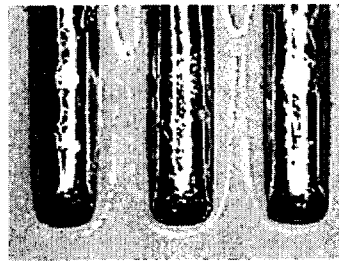
Low temperature, air reflow Pb-free solder paste

Matsushita and Senju



Sn-8Zn-3Bi 197 °C
Reflow 210 °C, flux
activates at reflow.

Sony and Nihon Genma



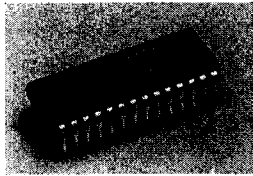
Sn-9Zn 199 °C reflow
over 200 °C, 30sec
peak 225 °C.



Conventional solder
paste, N₂ reflow.

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Pb-free plan for semiconductor devices



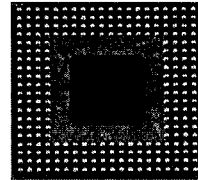
Pin insertion
(DIP)

SnPb plating \Rightarrow SnBi
plating, SnCu plating
SnPb solder dip \Rightarrow
SnCu dip



Surface mount
(QFP, SOP)

SnPb plating \Rightarrow
SnBi plating, SnAg
plating
SnPb solder dip
 \Rightarrow SnCu dip



Area array
(BGA, CSP)

SnPb ball \Rightarrow
SnAgCu ball

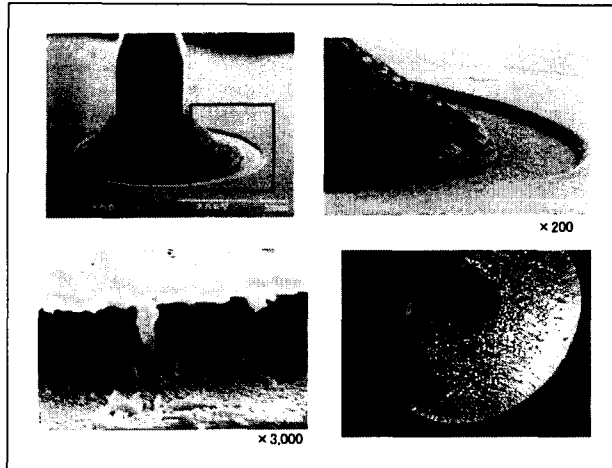
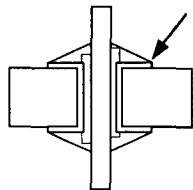
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Pb-free flow soldering

- Flow soldering is made at higher temperature (250-260°C) in short time (few seconds), and larger volume of solder can exist.
- Pb-free solder containing Ni (227°C) is popular.
- Standard solder is also used.
- Solder land lift off and Cu wiring erosion observed.
- Solder bath erosion problem took place.
- New stainless bath and a surface coating are being tested.

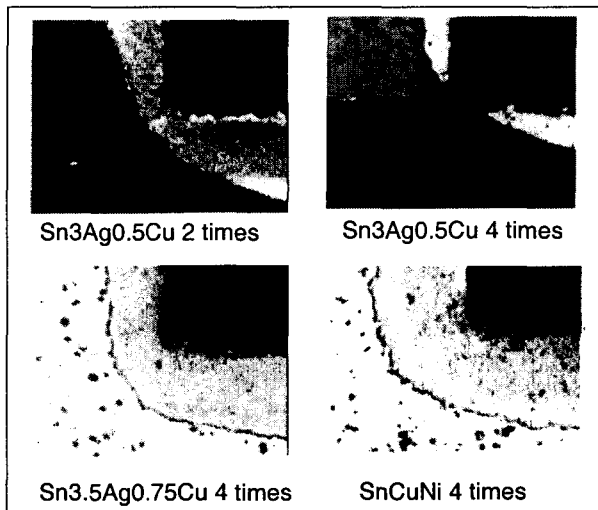
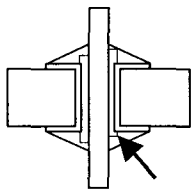
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Solder land lift off phenomenon



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Cu throughhole land erosion in flow soldering



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PRESENTATION

Flow soldering bath

Surface mount (reflow soldering) increases, partial flow soldering is needed.

Pb-free solder is less fluidity. From spout out to static pressure.

Solder. Sn-3Ag-0.5Cu, Sn-0.7Cu-Ni (22Z. N can be a barrier for bath erosion.

Countermeasure for bath erosion: Surface nitritization, change in stainless steel (SUS316).

spout

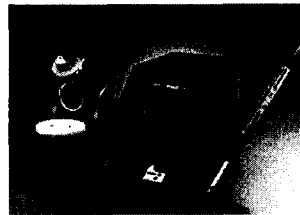


static

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Soldering iron for Pb-free solder

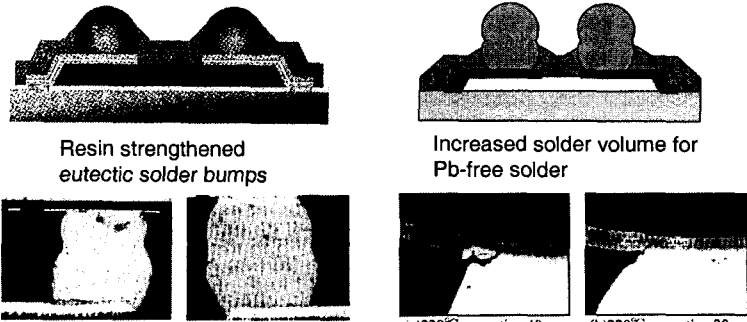
- Iron tips can be damaged due to high solder temperature.
- Fine tips of pure iron coated by Chromium.
- High speed setup, 320-380 °C in 3 sec.
- Precise temperature control, $\pm 1^{\circ}\text{C}$.
- High frequency heating.
- Nitrogen blowing.



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CSP with Pb-free solder bumps

Reliability problem with Pb-free solder



Resin strengthened eutectic solder bumps

Increased solder volume for Pb-free solder

Printed Bump B/M Bump

Cross section

(a) 220°C over time 40sec. (b) 220°C over time 20sec.

Cu wiring erosion depends on solder reflow time.

Tanaka, ICEP 2003 p263

