

# An Efficient Semaphore Implementation Scheme for an Event

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**Abstract:** In this paper, we present a novel efficient semaphore implementation scheme which diminishes completion time of high priority tasks and improves reliability of a system. The real-time system is constrained to complete their tasks in time. Especially, the task of a hard real-time system must meet its deadline under unfavorable conditions. In this paper, the number and sort of the locked semaphores, when an event occurred, decide whether the context switch should occur or not, so higher priority tasks diminish in their completion time. The experimental results show that the proposed method gives performance improvements in finish time of high priority tasks of about 11% over the Zuberi.

## 1. Introduction

Real-time system has time constraint such as deadline, release time, period, etc. Especially, Hard real-time system must complete a task within its deadline.[1] Real-time systems today are much smaller and simpler systems such as in automatic control, cellular phones, and home electronics(camcorders, TVs, and VCRs).

When tasks are willing to share resources, semaphore is implemented mutually exclusively to synchronize relative tasks.[2][3][4][5][6][7] Because semaphore system calls are invoked every time a task releases or resumes out of waiting stage, it becomes essential that the Real-Time OS provide efficient, low-overhead semaphores.

Most researches in the area of reducing the semaphore overheads have inefficient characteristics in time schedules. Semaphore implementations of Zuberi[4] result in priority inversion. Then, a higher priority tasks wait for lower priority task's releasing the semaphore.

In this paper we devise a mechanism to reduce the completion time of the higher priority of tasks. We apply this time reduction technique when an event occurs.

## 2. Conventional Semaphore Schemes

If the semaphore happens to be already locked by some other task, the task making the semaphore lock system call is put on a wait queue and is blocked. It is unblocked due to the semaphore release operation and it then proceeds to reserve the semaphore for itself.

If the caller is blocked by a semaphore, priority inheritance takes place under which the current lock holder task's priority is increased to that of the caller task. This is needed to avoid unbounded priority inversion.

Zuberi[4] proposed an efficient scheme of semaphore Implementation for small-Memory embedded systems

which saves one context switch per semaphore lock operation in most circumstances.

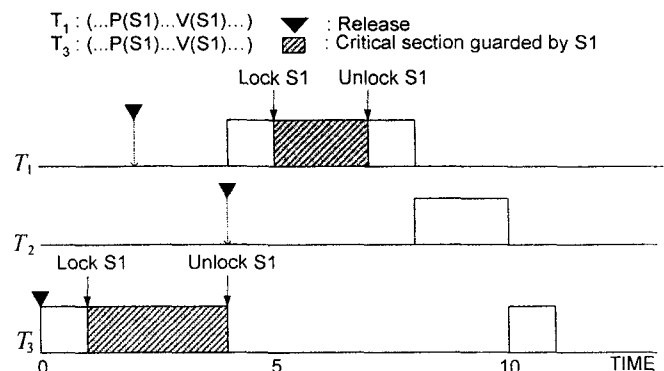


Figure 1. Zuberi's semaphore scheme(1)

Zuberi's scheme is as follows(Figure 1): at first, T<sub>3</sub> is released; T<sub>3</sub> acquires semaphore S<sub>1</sub>; when T<sub>1</sub> with code which intends to lock semaphore S<sub>1</sub> is released, the OS checks if S<sub>1</sub> is necessary for T<sub>1</sub> or not; If S<sub>1</sub> is necessary, then T<sub>3</sub> keeps executing instead of context switch to T<sub>1</sub>; context switch to T<sub>1</sub> is made after unlocking S<sub>1</sub>. As a result, on context switch is eliminated.

First of all, notice that if T<sub>1</sub> attempt to lock only S<sub>1</sub> semaphore, then the semaphore lock scheme is efficient due to one less context switch. In fact, for this case, the completion time is not delayed.

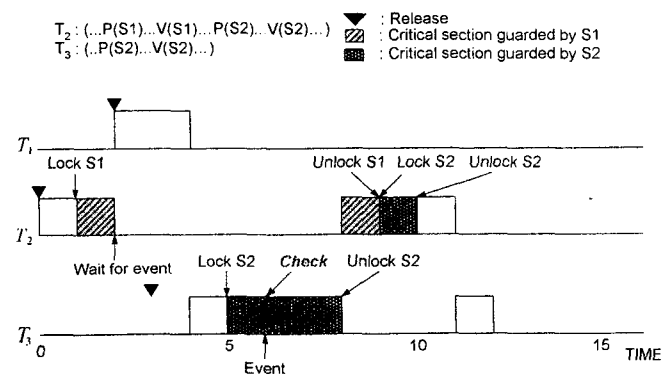


Figure 2. Zuberi's semaphore scheme(2)

The situation is very different when T<sub>1</sub> is willing to lock two or more semaphore including S<sub>1</sub>. Figure 2 shows a typical scenario for this situation. T<sub>2</sub> is released; T<sub>2</sub> acquires semaphore S<sub>1</sub>; T<sub>2</sub> blocks to wait for an event such as a message arrival; some other tasks execute; T<sub>1</sub> is

released; when T1 attempts to lock S1, T1 is blocked, and T3 is executed; T3 acquires semaphore S2; when the event waited for by T2 arrives, the OS checks if S2 is necessary for T2 or not; As S2 is necessary, T3 keeps executing instead of context switch to T2; context switch to T2 is made after unlocking S2; when S2 is unlocked, T1 is executed; As a result, T1 is completed later than traditional semaphore scheme.

### 3. Proposed Semaphore Scheme

We observe that zuberi's scheme is responsible for late completion time of higher priority tasks. So, we propose a novel semaphore implementation scheme which reduce completion time of higher priority tasks rather than Zuberi's scheme.

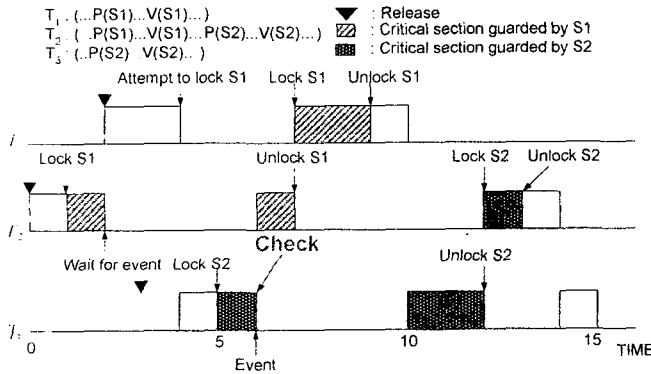


Figure 3. Proposed semaphore scheme

the proposed scheme is as follows(Figure 3): T2 is released; T2 acquires semaphore S1; T2 blocks to wait for an event such as a message arrival; some other tasks execute; T1 is released; when T1 attempts to lock S1, T1 is blocked, and T3 is executed; T3 acquires semaphore S2; when the event waited for by T2 arrives, the OS checks some cases as depicted in Figure 4; only if S2 is necessary for T2 and the number of semaphores necessary for T2 is only one. T3 keeps executing instead of context switch to T2: Otherwise, context switch is made right here to T2; So, if S2 is necessary for T2 and any other semaphore is not necessary for T2. T1 is completed relatively earlier than Zuberi's method.

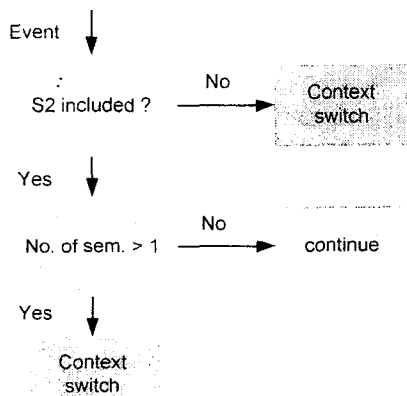


Figure 4. Flow chart of proposed scheme

### 4. Experimental Results

To measure the improvements in performance resulting from our new semaphore implementation scheme, we simulate it under SUN SPARC60. The time interval between each task block is pseudo random.

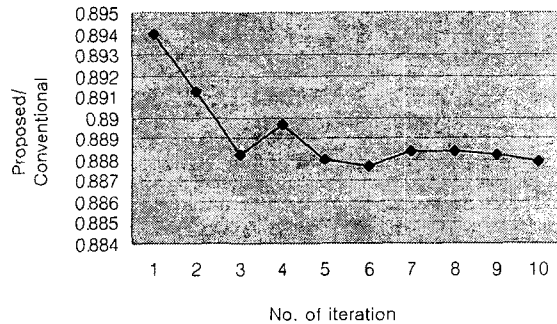


Figure 5. Experimental results

The experimental results in the Figure 5 show that the proposed method gives performance improvements in completion time of high priority tasks of about 11 % over Zuberi's scheme.

### 5. Conclusions and Future Works

In this paper, we presented a new efficient semaphore implementation scheme which reduces completion time of high priority tasks by about 11% and improves reliability of a system.

Future works include the advantages and disadvantages of extending our scheme to multi-processors. In the future, we plan to investigate adopting the proposed scheme to multi-processor systems.

### References

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