

[Nano device]

SEMICONDUCTOR DIELECTRIC INTERFACES AND DEFECT PASSIVATION

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The “heart” of the semiconductor revolution has been the control of the interface between the semiconductor and the gate dielectric. This talk will consider three components of this famous problem. 1) The structure of the silicon/silicon dioxide interface and its role in future silicon technology; 2) Progress in the control of the wide band-gap SiC/SiO₂ interface, enabling high power/high temperature MOSFETs¹ and 3) The use of hydrogen for defect passivation and the role of hydrogen vibrational dynamics in governing device reliability and longevity.²

[References]

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2. M. Budde, G. Lupke, C.Parks-Cheney, N.H.Tolk and L.C. Feldman, *Phys. Rev. Lett.*, 85, 1452 (2000).