SiH₄ Soak Effects for Optimization of Tungsten Plug Deposition on TiN Barrier Metal

Sang-Yong Kim¹⁾, Yong-Jin Seo²⁾, Woo-Sun Lee³⁾, Hun-Sang Chung³⁾, Chang-II Kim4), Eui-Goo Chang4), Yong-Ho Chung5)

Abstract

The SiH₄ soak step is widely used during the CVD Tungsten(W) plug deposition process on the Ti/TiN barrier metal to prevent the WF6 attack to the underlayer metal. We tried to reduce or skip the time of SiH₄ soak process to optimize W-plug deposition process on Via. The electrical characteristics including Via resistance and the structure of W film are affected according to SiH4 soak time. The elimination possibility of SiH₄ soak process was confirmed in the case of that the CVD W film grows on the stable Ti/TiN underlayer.

Key Words: SiH₄ Soak, CVD(chemical vapor deposition), WF₆ attack, W-plug, TiN barrier metal.

1. Introduction

In the CVD Tungsten(W) plug deposition process into contact and Via on the Ti/TiN barrier metal, WF₆ can react with Al, Si, Ti. WF₆ can be reduced by the underlayer Al and Ti through WF₆ + 2Al \rightarrow W + 2AlF₃ or 2WF₆ + 3Ti \rightarrow 2W + 3TiF₄ on Via when the underlayer TiN are unstably formed. In order to minimize the incubation time, W deposition with a nucleation layer formed by the reaction of WF6 by SiH4 is applied. The nucleation layer has a high bulk resistivity, poor step coverage, and increased surface roughness compared with the reduction process. We compared the Via Rc and the W film structure according to SiH₄ soak time, and evaluated the exclusion possibility of the SiH₄ soak step on Via contact hole of 0.33 µm size.

With the TEOS based IMD(inter-metal dielectric) on bare wafer, CVD W was deposited on TiN/Ti barrier metal to confirm the effect of reaction suppression between WF₆ and Ti. We electrical characteristics evaluated of resistance(Rs), Rs non-uniformity(NU), resistivity and reflectance after deposition of W on TEOS. The Via Rc data was achieved on $0.33 \mu m$ size. W CMP process was applied to remove W on IMD except Via, and metal interconnects structure is Ti/TiN/AlCu/top TiN. W structure was analyzed by SEM and EDS.

3. Results and Discussion

The SiH₄ soak time in the CVD W deposition process was splited, and it was displayed in table 1. The Rs, resistivity and reflectance values are decreased by the drop of SiH4 soak time, and the no SiH₄ soak condition shows best result. We think the Si in SiH₄ react with WF₆, and the compound of SiH₄ increase Rs. Reflectance

^{2.} Experiments

¹⁾ ANAM Semiconductor INC.

²⁾ Dept. of Electrical Eng., Daebul Univ.

³⁾ Dept. of Electrical Eng., Chosun Univ. 4) School of E&E Eng., Chungang Univ.

⁵⁾ Seo Kang College

non-uniformity(Ref. NU) increase according to decrease of SiH_4 soak time. The surface morphology of W nucleation layer were compared by SEM in Fig. 1.

Table 1. The comparison of Rs and reflectance data,

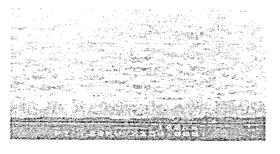
| Soak | Rs | Rs NU | Reflectance | Ref. NU | Resistivity |
|----------|-------|-------|-------------|---------|----------------------|
| Time | (Ω/□) | (%) | (%Si) | (%) | $(\mu \Omega^{-cm})$ |
| No | 2.63 | 2.31 | 122.65 | 0.87 | 11.81 |
| 5 (sec) | 3.29 | 2.75 | 126.19 | 0.35 | 14.82 |
| 10 (sec) | 3.31 | 2.68 | 127.43 | 0.33 | 14.89 |
| 15 (sec) | 3.29 | 2.90 | 127.60 | 0.23 | 14.80 |



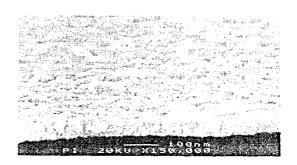
(a) No SiH₄ Soak



(b) SiH₄ Soak Time: 5 sec

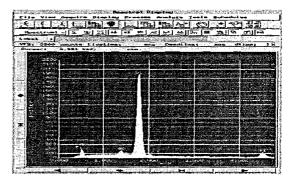


(c) SiH₄ Soak Time: 10 sec

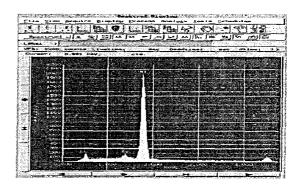


(d) SiH₄ Soak Time: 15 sec

Fig. 1. The surface structure comparison of W nucleation film by SEM.



(a) No SiH4 Soak



(b) SiH₄ Soak Time: 5 sec

Fig. 2. The surface element analysis of the W nucleation film by EDS.

The shrinkage of SiH_4 soak time increase grain size of W film and reduce reflectance and resistance. We confirmed the cross sectional structure by SEM, and the damage of Ti and TiN layer were suppressed in all conditions

including none SiH₄ soak time. TiF₃ formation after W film deposition on Ti/TiN barrier films was confirmed by the surface analysis of the W nucleation film. There aren't any F peak on all samples like Fig. 2. It means that WF₆ attact to Ti/TiN layer in spite of soak skip dose not appear in the case of the barrier structure that are safely deposited. SiH₄ soak skip condition has lower and safe Via Rc profile, and it is showed in Fig. 3.

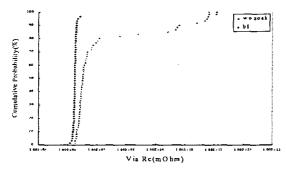


Fig. 3. Normalized Via Rc as a function of SiH₄ Soak time.

4. Conclusion

 SiH_4 soak process is used to suppress WF_6 attack into barrier metal. But, the condition of no SiH_4 soak during W film deposition process has superior electrical characteristic and film structure in this test. So, it is confirmed the throughput improvement and Via Rc progress are possible by optimization of the barrier metal structure. We can decrease the process time about 10% by skip of the soak step.

참고 문헌

- [1] S. Govindarajan et al., "Towards void free W plug fill for sub 0.25 micron DRAM Applications", VMIC., pp. 340 ~ 345, 1999.
- [2] C. Chang and S. Sze, ULSI TECHNOLOGY, pp. 386~389.
- [3] V. Rana et al., "Tungsten and other refractory metals for VLSI application II", MRS., p. 187, 1987.