Improvement of Input Current Waveform for

Soft-Switching Boost DCM Converter

with Unity Power Factor

K. Taniguchi, T. Watanabe, T. Morizane, N. Kimura
Osaka Institute of Technology
5-16-1 Omiya, Asahi-ku OSAKA 535-8585 JAPAN
FAX 81-6-957-213 TEL 81-6-954-4231 E-Mail:taniguchi@ee.oit.ac.jp

Hyun-Woo Lee Kyungnam University 449, Wolyoung-dong, **Masan, Korea**

ABSTRACT-In this paper, a soft-switching discontinuous mode (DCM) power factor corrected (PFC) converter is analyzed by applying the double Fourier series expansion. It is found that the fundamental component and higher-order harmonics included in the input current waveform are obtained by the Fourier series expansion of the mean value of the inductor current. From the theoretical analysis, a new method removing the distortion of the input current waveform is proposed. In spite of an open loop system, the proposed method makes a great improvement of the total harmonic distortion even if the ratio of output voltage to input voltage is very low.

1. INTRODUCTION

PWM power factor correction (PFC) techniques have received great attention in recent years. In most cases, PWM-PFC converters are constructed by a diode rectifier and an active power circuit such as a boost or a buck-boost chopper [1-2]. In Fig.1, the converter is a discontinuous conduction mode (DCM) operation in order to obtain some merits of simpler control such as fixed switching frequency, without current sensor and synchronization control circuit. The DCM operation provides for a simple control but it must be turned off at the maximum value of the current. The soft switching is promising in the improvement of the turn-off characteristics and stress relief of the components.

We have proposed a soft-switching boost PFC converter as shown in Fig.2 [3]. In the soft-switching converter, the switching device is replaced by the loss-less snubber circuit to achieve the zero voltage switching (ZVS) at the maximum inductor current.

In both converter shown in Fig. 1 of hard-switching converter and in Fig.2 of soft-switching converter, the distortion in the input current increases with the low ratio of output voltage to input voltage. On the other hand, the high ratio of output voltage to input voltage causes the cost up of the converter because of requirement of the high voltage devices.

To improve the input current waveform of the soft-switching converter, a new duty factor control method is proposed in this paper. A soft-switching discontinuous mode (DCM) power factor corrected (PFC) converter is analyzed by ap-

plying the double Fourier series expansion. From the theoretical analysis, a new method removing the distortion of the input current waveform is proposed. In spite of an open loop system, the proposed method makes a great improvement of the total harmonic distortion even if the ratio of output voltage to input voltage is very low.

2. OPERATIONAL MODE OF SOFT-SWITCHING PFC CONVERTER

Fig.3 shows an inductor current waveform for one switching cycle. Operation of soft-switching PFC converter consists of 5 modes.

A mode analysis is performed for input AC voltage of

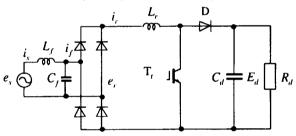


Fig. 1 Conventional hard-switching DCM converter.

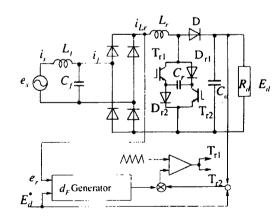


Fig.2 Circuit configuration of soft-switching DCM converter.

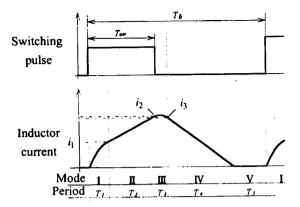


Fig.3 Reactor current waveforms.

 $e_x = E_x \sin \omega_x t$, $\omega_x = 2\pi f_x$ and output voltage from diode rectifier of $e_r = |e_s| = |E_s \sin \omega_s t|$. In initial condition, T_{r1} , T_{r2} are off, he inductor current is zero and capacitor C_r is charged to voltage E_d . Analyzed results of the commutation capacitor voltage v_{Cr} and the commutation reactor current i_{Lr} are shown below.

MODE I $(T_1:t_0 \sim t_1)$ When T_{r1} and T_{r2} are turned on at the same time, the commutation capacitor begins to discharge. Then

$$v_{Cr} = (e_r + E_d)\cos\omega_r t - e_r \qquad \dots (1)$$

$$i_{Lr} = \frac{e_r + E_d}{X_r} \sin \omega_r t \qquad ...(2)$$

where,
$$X_r = \sqrt{L_r / C_r}$$
, $\omega_r = 1 / \sqrt{L_r C_r}$.

This mode ends when $v_c = 0$ at time $t = T_1$. Duration of this mode T_1 is nearly equal to $\pi / 2\omega_r$. Then, the commutation reactor current $i_1 = i_{Lr}(T_1)$ is

$$i_1 \equiv \frac{e_r + E_d}{X_r} \qquad ...(3)$$

MODE II $(T_2: t_1 \sim t_2)$

The commutation reactor current i_{i_r} increases linearly as follows,

$$i_{Lr} = (e_r / L_r)t + i_1$$
 ...(4)

 $i_{Lr} = (e_r / L_r)t + i_l$ In this period, $v_{Cr} = 0$. Mode II ends where Mode II ends when T_{r1} and T_{r2} are turned off at the same time. Then, the duration of this mode T_2 is as follows,

$$T_2 = T_{ON} - T_1$$
 ...(5)

At time $t=T_2$, the commutation reactor current i_2 is as

$$i_2 = \frac{e_r}{L_r} T_2 + i_1 \cong \frac{e_r}{L_r} \left(T_{ON} - \frac{\pi}{2\omega_r} \right) + \frac{e_r + E_d}{X_r} = I_{p2} ...(6)$$

MODE III $(T_3:t_2\sim t_3)$

When T_{r1} and T_{r2} are turned off at the same time, the commutation capacitor begins to be charged. Mode III ends when diode D is turned on at $v_{Cr} = E_{dr}$. At time $t = t_{gr}$, the commutation reactor current i_3 is nearly equal to I_{n2} .

MODE IV $(T_i: t_i \sim t_j)$

When diode D begins to conduct, the current flowing into the reactor L begins to flow load side, then reactor current decreases linearly. In this mode, $v_c = E_d$ is kept. Mode IV ends when diode D is turned off at $i_{ir}=0$.

MODE V
$$(T_s: t_a \sim t_n)$$

In mode V, as the reactor current i_{1r} is zero and T_{r1} , T_{r2} are off, $v_{Cr} = E_d$, $i_{Lr} = 0$ are kept.

One switching period ends at this time and next cycle begin when switching devices T_{r1} , T_{r2} are turned on.

3. ANALYSIS OF INPUT CURRENT WAVEFORM

Since the ratio of an AC source frequency and a switching frequency is generally incommensurable, the PWM input current waveform becomes a non-periodic function. The harmonic analysis of such a waveform can be carried out by using the double Fourier series [4-5]. The input current waveform i, is given by

$$i_{x} = \sum_{m=0}^{+\infty} \sum_{n=0}^{+\infty} K_{mn} e^{i(mx+nx)}$$

$$(7)$$

$$K_{mn} = \frac{1}{(2\pi)^2} \int_0^{2\pi} \int_0^{2\pi} i_r(x, y) e^{-y(mx+ny)} dx dy$$
 (8)

where i_r is a inductor current, ω_h is a switching angular frequency, ω_s is an AC source angular frequency, $x = \omega_b t$,

$$y = \omega_1 t$$
, m=0,1,2,..., and n=0,1,2,...

As a result, the frequency components included in the PWM output waveform are a fundamental component higher-order harmonic, and the sidebands of a carrier frequency. If the frequency ratio becomes large, the sidebands move into the high-frequency region apart from the fundamental frequency. Therefore the sidebands can be removed by the input filter easily. The input current waveform without sidebands is given by

$$i_{x} = \sum_{n=0}^{+\infty} K_{0n} e^{jny} \tag{9}$$

$$K_{0n} = \frac{1}{2\pi} \int_0^{2\pi} \left\{ \frac{1}{2\pi} \int_0^{2\pi} i_r(x, y) dx \right\} e^{-jnv} dy$$

$$= \frac{1}{2\pi} \int_{0}^{2\pi} \{I_{r}(y)\} e^{-jny} dy \tag{10}$$

where
$$I_r(y) = \frac{1}{2\pi} \int_0^{2\pi} i_r(x, y) dx$$
 (11)

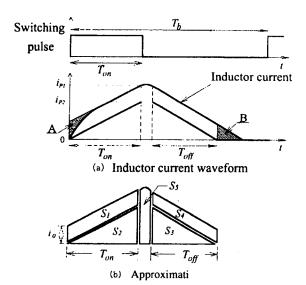


Fig.4 Approximation of inductor current waveform.

 I_r is the mean value of the inductor current i_r . Eq. (10) indicates that the fundamental component and higher-order harmonics included in the input current waveform are obtained by the Fourier series expansion of the mean value of the inductor current $i_{.}$.

Fig. 4 shows an approximation of inductor current waveform shown in Fig.3. An approximation of area A = area B shown in Fig. 4 (a) can be expressed an inductor current waveform as shown in Fig. 4 (b).

Therefore, the mean value I_r of the inductor current i_r is

$$I_{r} = (S_{1} + S_{2} + S_{3} + S_{4} + S_{5}) / T_{b}$$

$$= \frac{1}{X_{r}} \left\{ \left(1 - \frac{\pi}{2} \right) d_{F} + \frac{d_{F}^{2}}{2L_{r}f_{b}} \right\} \frac{e_{r}}{1 - (e_{r} / E_{d})}$$

$$+ \frac{1}{X_{r}} \left(d_{F} + X_{r}C_{r}f_{b} \right) \frac{E_{d}}{1 - (e_{r} / E_{d})}$$
(12)

where $i_{P2} = (e_r / L_r) T_{on}$, $i_0 = i_{P1} - i_{P2}$, $T_{on} = \{e_r / (E_d - e_r)\} T_{off}, S_5 = C_r E_d.$

4. IMPROVEMENT OF INPUT CURRENT WAVEFORM

If
$$d_F >> X_r C_r f_b$$
, then $e_r \left\{ 1 - (e_r / E_d) \right\}^{-1} \cong e_r$,

$$\therefore I_r = \frac{\{1 - (\pi/2)\}d_F}{X_r} e_r + \frac{d_F}{X_r \{1 - (e_r/E_d)\}} E_d = I_r'$$
 (13)

Both eqs (12) and (13) are obtained maximum values at $\omega_s t = \pi / 2$. Fig. 5 shows the errors of I_r for I_r' at

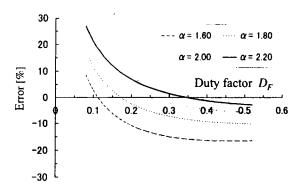


Fig.5 Errors of I_r for I'_r at $\omega_c t = \pi/2$.

 $\omega_{c}t = \pi/2$. The errors are small comparatively.

An improvement of the input current waveform can be derived by applying the theoretical results. A fundamental component and higher-order harmonics included in the input current waveform are obtained by the Fourier series expansion of the mean value of the inductor current. Namely, it is implied that the input current becomes pure sinusoidal waveform when the mean value of the inductor current is completely proportional to the input voltage waveform. Therefore,

$$\frac{1}{\pi} \int_0^{\pi} \frac{d_F E_d}{X_r \left\{ 1 - (e_r / E_d) \right\}} d\omega_s t \propto \frac{1}{\pi} \int_0^{\pi} \frac{e_r}{X_r} d\omega_s t$$

$$\therefore \frac{d_F}{1 - (e_r / E_d)} = A e_r \tag{14}$$

(14)

From (20), duty function d_F can be obtained by

$$d_F = D_F \left(1 - \frac{1}{\alpha} \left| \sin \omega_x t \right| \right) \sin \omega_x t$$
 (15)

where $0 \le (D_F = A) \le 1$ and A is constant.

Fig. 6 shows a waveform of d_F from (15). Top area of the inductor current at boost ratio of $\alpha = 2.00 (E_d = 565V)$ is flatness and the waveform at $\alpha = 1.237 (E_d = 350V)$ has double peak. The input current waveform is improved by controlling the duty factor according to (15).

5. CHARACTERISTICS OF PROPOSED d_F CONTROL

Principal parameters for simulation are listed in Table 1. Comparison of the THD between the conventional constant d_F control, the soft-switching constant d_F control and the proposed soft-switching d_F control are shown in Fig. 7. Input power is 1.2kW. In wide variation of duty factor, the proposed method establishes a great improvement of THD. Many harmonics included in the input current waveform of the proposed method can be decreased.

Table 1 Principal parameters for simulation.

Source Voltage (Max)	E_s	200√2 [V]
Source Frequency	f_s	60 [Hz]
Switching Frequency	f_b	20 [kHz]
Smoothing Capacity	C_d	1000 [μF]
Resonant Capacitor	C,	0.1 [μF]
Resonant Inductor	L_r	100 [μH]

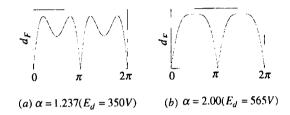


Fig.6 d_F Modulating signal.

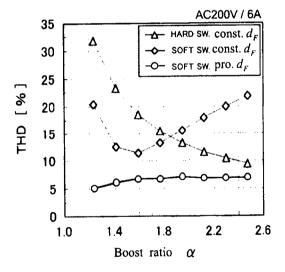


Fig.7 Comparision of THD.

Fig. 8 shows variation of THD for input power at $\alpha = 1.237$ [$E_d = 350V$]. Maximum power is 1.3kW for the conventional, 1.6kW for the soft-switching constant d_F control and 1.9kW for the proposed soft-switching d_F control. The proposed d_F control achieves both improvement of input current waveform and power range.

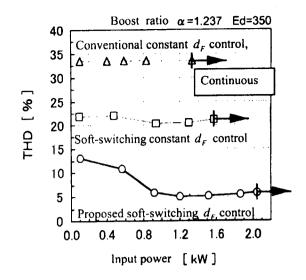


Fig 8 THD vs. input power.

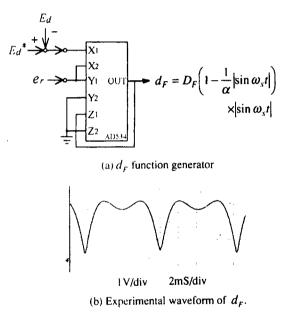
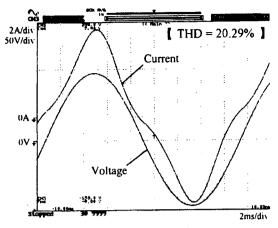


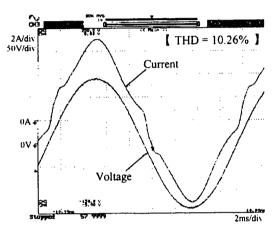
Fig 9 Generation of d_F modulating signal.

6. EXPERIMENTAL RESULTS

Many characteristics of the proposed soft-switching PFC converter ware confirmed by experimental results. The input current waveform can be improved by controlling the duty factor according to eq (15) instead of constant d_F control. A circuit to control the duty factor according to Eq. (15) can be



(a) Conventional constant d_F control.



(b) Soft-switching constant d_F control.

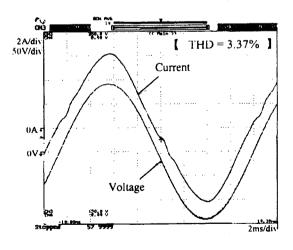


Fig. 10 Examples of input voltage and current waveforms ($\alpha = 1.4$).

realized by using one multiplier-divider ICs (AD534). The generation circuit of d_F modulating signal is very simple as shown in Fig.9 (a). Fig.9 (b) shows an experimental waveforms of d_F modulating signal..

Fig. 10 shows an example of the experimental input voltage and current waveforms for the conventional constant d_F control, soft-switching constant d_F control and the proposed d_F control for soft-switching PFC converter. Experimental waveforms are obtained for the case of the input voltage V_S =100V rms, the input frequency f_s =60Hz, switching frequency f_b =20kHz. L_f =2.42mH, C_f =9.34 μ F, L_r =120 μ H, C_d =1mF, R_d =100 Ω and α =1.4. The proposed control method establishes a great improvement of THD in spite of an open loop system.

7. CONCLUSIONS

To improve the input current waveform of the soft-switching converter, a new duty factor control method has been proposed in this paper. A soft-switching discontinuous mode (DCM) power factor corrected (PFC) converter is analyzed by applying the double Fourier series expansion. It is found that the fundamental component and higher-order harmonics included in the input current waveform are obtained by the Fourier series expansion of the mean value of the inductor current. From the theoretical analysis, a new method removing the distortion of the input current waveform is proposed. In spite of an open loop system, the proposed method makes a great improvement of the total harmonic distortion even if the ratio of output voltage to input voltage is very low. Proposed soft-switching boost PFC converter achieves the zero voltage switching (ZVS) at the maximum current.

REFERENCES

[1] A.R.Prasad, P.D.Ziogas, and S.Manias, "An Active Power Fact or Correction Technique For Three-Phase Diode Rectifiers," IEEE PESC'89, 1989, pp.58-66.

[2]J.W.Kolar, H.Ertl, F.C. Zach" A Novel Three-Phase Single-Switch Discontinuous-Mode AC-DC Buck-Boost Converter with High-Quality Input Current Waveforms and Isolated Output", IEEE Trans. Power Electron., vol. IE-9, No.2, pp160-172, March (1994)

[3] Taniguchi, Nishiyama, Kimura, "A Soft-Switching Converter with High Power Factor using Loss-Less Snubber," Tran. IEE in Japan, vol. 115-D, No. 1, 1995, pp. 84-85.

[4] Taniguchi, Ogino and Irie"PWM Technique for MOSFET Inverter", IEEE Trans. Power Electronics, Vol. PE-3, No.3, pp 328/334 (1988-7)

[5] Taniguchi "Characteristic Analysis and Improvement of Input Current Waveform for Boost DCM Converter with Unity Power Factor," Tran IEE in Japan, vol. 121-D, No. 3, 2001, pp. 302-307.