

An Improved Stationary Frame-based Digital Current Control Scheme for a PM Synchronous Motor

Kyeong-Hwa Kim and Myung-Joong Youn
Department of Electrical Engineering
Korea Advanced Institute of Science and Technology
373-1, Kusong-Dong, Yusong-Gu, Taejeon, 305-701, Korea

Abstract - An improved stationary frame-based digital current control technique for a permanent magnet (PM) synchronous motor is presented. Generally, the stationary frame current controller is known to provide the advantage of a simple implementation. However, there are some unavoidable limitations such as a steady-state error and a phase delay in the steady-state. On the other hand, in the synchronous frame current regulator, the regulated currents are dc quantities and a zero steady-state error can be obtained through the integral control. However, the need to transform the signals between the stationary and synchronous frames makes the implementation of a synchronous frame regulator complex. Although the PI controller in the stationary frame gives a steady-state error and a phase delay, the control performance can be greatly improved by employing the exact decoupling control inputs for the back EMF, resulting in an ideal steady-state control characteristics irrespective of an operating condition as in the synchronous PI decoupling controller. However, its steady-state response may be degraded due to the inexact cancellation inputs under the parameter variations. To improve the control performance in the stationary frame, the disturbance is estimated using the time delay control. The proposed scheme is implemented on a PM synchronous motor using DSP TMS320C31 and the effectiveness is verified through the comparative simulations and experiments.

Key Words: Current control, stationary reference frame, PM synchronous motor, disturbance estimation, time delay control.

I. INTRODUCTION

According to the reference frame in which a current controller is designed, the current control schemes for a voltage source inverter-fed PM synchronous motor drive system can be classified as two categories, namely, the stationary frame controller [1]-[4] and synchronous frame controller [5]-[10]. The hysteresis and ramp comparison controllers can be categorized into the stationary frame current controller. The synchronous PI decoupling regulator is designed in the synchronously rotating frame. On the other hand, the predictive control can be used in either reference frame. Generally, the stationary frame controller is known to provide the advantage of a simple implementation. However, there are some unavoidable limitations such as a steady-state current error and a phase delay in the steady-state since P or PI controller can not exactly track the sinusoidally varying reference and the back EMF acts as a disturbance in the current control loop [4], [5]. On the contrary, in the synchronous frame current regulator, the regulated currents are dc quantities. Thus, a zero steady-state error can be obtained through the use of an integral control. However, the need to transform the signals between the stationary and synchronous reference frames makes the implementation of a synchronous frame-based current regulator complex. Hence, a regulator with a zero steady-state error in the stationary frame would have

an implementation advantage. First such an attempt is found in [4]. Although a zero steady-state error can be obtained by this control scheme, the transient performance seems to be unsatisfactory and the parameter variations have not been considered. Also, this scheme requires the speed informations for the regulator design. In view of the transient performance, however, it is more efficient to use the PI decoupling or predictive control using the speed informations [2], [3]. This paper presents an improved stationary frame-based digital current control scheme for a PM synchronous motor. Although the PI controller in the stationary frame gives a steady-state error and a phase delay, the control performance can be greatly improved by employing the exact decoupling control inputs for the back EMF and the PI control with a high bandwidth, resulting in an ideal steady-state control characteristics irrespective of operating conditions as in the synchronous PI decoupling controller. However, its steady-state response may be degraded due to the inexact cancellation inputs under the parameter variations such as the flux linkage, stator inductance, and stator resistance. In particular, the inaccuracy of the back EMF influences primarily on the control performance. This is more serious at high speed operation. In [10], to improve the control performance in the presence of the parameter variations, the time delay control has been applied to the synchronous frame current controller. This scheme can be an effective way for a control of systems with unknown dynamics and uncertainties since this algorithm does not require an explicit dynamic model nor does it depend on the estimation of specific parameters [11]. In this paper, this scheme will be employed for the estimation of the disturbance caused by the parameter variations to improve the steady-state control performance in the stationary frame-based digital current controller. The whole control system is realized by DSP TMS320C31 for a PM synchronous motor driven by a three-phase voltage-fed PWM inverter.

II. MODELING OF A PM SYNCHRONOUS MOTOR AND STATIONARY FRAME CURRENT CONTROL

The stator voltage equations of a PM synchronous motor in the stationary reference frame are described as follows:

$$v_{qs} = R_s i_{qs} + L_s \frac{di_{qs}}{dt} + e_{qs} \quad (1)$$

$$v_{ds} = R_s i_{ds} + L_s \frac{di_{ds}}{dt} + e_{ds} \quad (2)$$

where R_s is the stator resistance, L_s is the stator inductance, and e_{qs} and e_{ds} represent the back EMF's. These can be expressed as

$$e_{qs} = \lambda_m \omega_r \cos \theta_r \quad (3)$$

$$e_{ds} = -\lambda_m \omega_r \sin \theta_r \quad (4)$$

where ω_r is the electrical rotor angular velocity, θ_r is the electrical rotor angular position, and λ_m is the flux linkage. The current references in the stationary frame can be obtained from those in the synchronous frame using the relation as follows:

$$\begin{bmatrix} i_{qs}^* \\ i_{ds}^* \end{bmatrix} = \mathbf{K}(\theta_r) \begin{bmatrix} i_{qe}^* \\ i_{de}^* \end{bmatrix}, \quad \mathbf{K}(\theta_r) = \begin{pmatrix} \cos \theta_r & \sin \theta_r \\ -\sin \theta_r & \cos \theta_r \end{pmatrix} \quad (5)$$

where subscript "s" denotes the quantities in the stationary reference frame, subscript "e" denotes the quantities in the synchronous reference frame, and the symbol "*" denotes the reference quantities. From the measurements of the phase currents, the current values in the stationary frame can be obtained as follows:

$$i_{qs} = i_{as} \quad (6)$$

$$i_{ds} = (i_{cs} - i_{bs}) / \sqrt{3} \quad (7)$$

where i_{as} , i_{bs} , and i_{cs} denote the phase currents, respectively. Using the stationary frame PI controller for a current regulation, a steady-state current error and a phase delay are present in the phase currents since the back EMF's act as disturbances and the current references are varied sinusoidally. To improve the control performance, the control input voltages can be obtained using the PI decoupling control as follows:

$$v_{qs}^* = u_{qs} + e_{qs} \quad (8)$$

$$v_{ds}^* = u_{ds} + e_{ds} \quad (9)$$

$$\text{where } u_{qs} = (K_p + K_I/s) \cdot (i_{qs}^* - i_{qs}) \quad (10)$$

$$u_{ds} = (K_p + K_I/s) \cdot (i_{ds}^* - i_{ds}) \quad (11)$$

s is Laplace operator, and K_p and K_I are the proportional and integral gains, respectively. Then, the transfer function from i_{qs}^* to i_{qs} in the q -axis component can be expressed as

$$G_c(s) = \frac{i_{qs}(s)}{i_{qs}^*(s)} = \frac{K_p s + K_I}{L_s s^2 + (K_p + R_s)s + K_I} \quad (12)$$

If K_I is selected as $K_I = K_p R_s / L_s$, (12) reduces to

$$G_c(s) = \frac{\omega_c}{s + \omega_c} \quad (13)$$

where ω_c is the controller bandwidth expressed as $\omega_c = K_p / L_s$. The transfer function and controller bandwidth can be similarly obtained for the d -axis component of the stator current.

III. DISTURBANCE ESTIMATION USING TIME DELAY CONTROL

From (1) and (2), the discrete-time equation of a PM synchronous motor can be obtained as follows:

$$v_s(k) = \mathbf{A} i_s(k) + \frac{L_{so}}{T} \mathbf{I} \cdot [i_s(k+1) - i_s(k)] + e_{so}(k) \quad (14)$$

where $v_s = [v_{qs} \quad v_{ds}]^T$, $i_s = [i_{qs} \quad i_{ds}]^T$, $\mathbf{A} = R_s \mathbf{I}$

$$e_s = \begin{bmatrix} e_{qs} \\ e_{ds} \end{bmatrix} = \begin{pmatrix} \lambda_m \omega_r(k) \cos[\theta_r(k)] \\ -\lambda_m \omega_r(k) \sin[\theta_r(k)] \end{pmatrix}, \quad \mathbf{I} = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}$$

and T is a sampling period. Using the nominal parameters, (14) can be rewritten as follows:

$$v_s(k) = \mathbf{A}_o i_s(k) + \frac{L_{so}}{T} \mathbf{I} \cdot [i_s(k+1) - i_s(k)] + e_{so}(k) + f_s(k) \quad (15)$$

where $f_s = [f_q \quad f_d]^T$, $\mathbf{A}_o = R_{so} \mathbf{I}$

$$e_{so} = \begin{pmatrix} e_{qso} \\ e_{dso} \end{pmatrix} = \begin{pmatrix} \lambda_{mo} \omega_r(k) \cos[\theta_r(k)] \\ -\lambda_{mo} \omega_r(k) \sin[\theta_r(k)] \end{pmatrix}$$

subscript "o" denotes the nominal value, and $f_s(k)$ represents the disturbance vector caused by the parameter variations. This can be expressed as

$$f_s(k) = \Delta \mathbf{A} i_s(k) + \frac{\Delta L_s}{T} \mathbf{I} \cdot [i_s(k+1) - i_s(k)] + \Delta e_s(k) \quad (16)$$

where $\Delta \mathbf{A} = \Delta R_s \mathbf{I}$, $\Delta e_s(k) = \begin{pmatrix} \Delta \lambda_m \omega_r(k) \cos[\theta_r(k)] \\ -\Delta \lambda_m \omega_r(k) \sin[\theta_r(k)] \end{pmatrix}$

$$\Delta R_s = R_s - R_{so}, \quad \Delta L_s = L_s - L_{so}, \quad \text{and } \Delta \lambda_m = \lambda_m - \lambda_{mo}.$$

In the time delay control, to obtain the estimates for the disturbance $f_s(k)$, it is considered that the value of $f_s(k)$ at the present time k is very close to that at $(k-L)$ -step in the past for a small time delay L as follows [10], [11]:

$$f_s(k) \cong f_s(k-L). \quad (17)$$

Using (15) and (17), the simple estimates for $f_s(k)$ can be derived as follows [10]:

$$\hat{f}_s(k) \cong f_s(k-L) = v_s(k-L) - \mathbf{A}_o i_s(k-L) - \frac{L_{so}}{T} \mathbf{I} \cdot [i_s(k-L+1) - i_s(k-L)] - e_{so}(k-L) \quad (18)$$

where $\hat{f}_s = [\hat{f}_q \quad \hat{f}_d]^T$ and the symbol " $\hat{\cdot}$ " denotes the estimated quantities. Since the numerical differentiation of the measured currents is needed for the estimation of $f_s(k)$, the high frequency noise in stator currents may be amplified. To reduce this noise, a first order low pass filter $G_f(s) = a/s + a$ is implemented by a digital manner using the bilinear transform within a DSP [10], [12]. Then, the discrete-time transfer function of $G_f(s)$ can be obtained as

$$G_f(z) = \frac{\hat{f}_{qf}(z)}{\hat{f}_q(z)} = \frac{aT(1+z^{-1})}{(2+aT) - (2-aT)z^{-1}}. \quad (19)$$

Using (19), the filtered estimate for the q -axis disturbance can be obtained in a difference equation as follows:

$$\hat{f}_{qf}(k) = \frac{2-aT}{2+aT} \hat{f}_{qf}(k-1) + \frac{aT}{2+aT} [\hat{f}_q(k) + \hat{f}_q(k-1)]. \quad (20)$$

Similarly, the filtered estimate for the d -axis disturbance can be obtained. Fig. 1 shows the proposed disturbance estimation scheme using the time delay control. The measured currents and voltages are delayed by L step and these delayed signals are used for the disturbance estimation. Since this can be accomplished by only using the time delay of L step, some calculations, and low pass filter, the computational load of the controller is relatively small.

IV. STATIONARY FRAME DIGITAL CURRENT CONTROL WITH FEEDFORWARD COMPENSATION

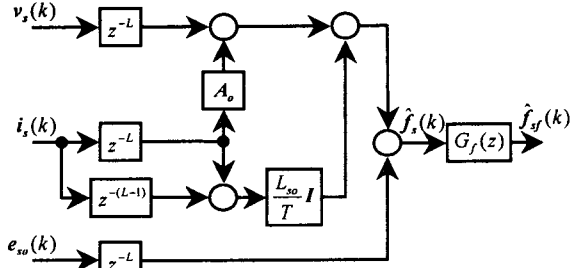


Fig. 1 Disturbance estimation using time delay control approach

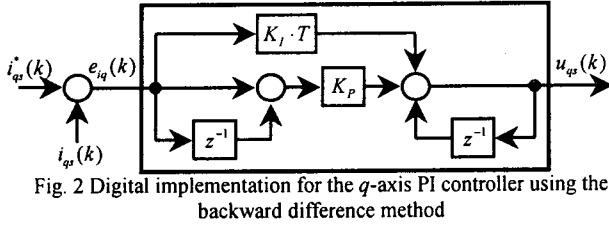


Fig. 2 Digital implementation for the q -axis PI controller using the backward difference method

By employing the estimated disturbances for a feedforward control, the reference voltage in the proposed stationary frame-based current regulator can be calculated as follows:

$$\mathbf{v}_s^*(k) = \mathbf{u}_s(k) + \mathbf{e}_{so}(k) + \hat{\mathbf{f}}_{sf}(k) \quad (21)$$

where $\hat{\mathbf{f}}_{sf} = [\hat{f}_{df} \ \hat{f}_{df}]^T$, $\mathbf{u}_s = [u_{qs} \ u_{ds}]^T$, and u_{qs} and u_{ds} represent the PI controllers in (10) and (11), respectively. For the digital implementation, these controllers are discretized using the backward difference as follows [12]:

$$u_{qs}(k) = u_{qs}(k-1) + K_p[e_{iq}(k) - e_{iq}(k-1)] + K_i T e_{iq}(k) \quad (22)$$

$$u_{ds}(k) = u_{ds}(k-1) + K_p[e_{id}(k) - e_{id}(k-1)] + K_i T e_{id}(k) \quad (23)$$

where $e_{iq}(k) = i_{qs}^*(k) - i_{qs}(k)$ and $e_{id}(k) = i_{ds}^*(k) - i_{ds}(k)$. Then, the reference voltage is composed of the conventional PI decoupling control part and disturbance compensation part. Fig. 2 shows the digital implementation for the q -axis PI controller using the backward difference.

V. SIMULATIONS AND EXPERIMENTAL RESULTS

For the performance comparison with the proposed control scheme, the stationary frame PI control and PI decoupling control are employed. The overall block diagram for the proposed current control scheme is shown in Fig. 3. The overall system consists of a proposed current controller, a disturbance estimator using the time delay, a PWM inverter, and a PM synchronous motor. For the current control algorithm, the stationary frame PI decoupling control scheme with a feedforward disturbance compensation is used. During the operations, the disturbances caused by the parameter variations are estimated by using the time delay control and used for the feedforward control. The computed reference voltages are transformed to obtain the reference phase voltages using the relations as follows:

$$v_{as}^* = v_{qs}^* \quad (24)$$

$$v_{bs}^* = -(v_{qs}^* + \sqrt{3}v_{ds}^*)/2 \quad (25)$$

$$v_{cs}^* = -(v_{qs}^* - \sqrt{3}v_{ds}^*)/2. \quad (26)$$

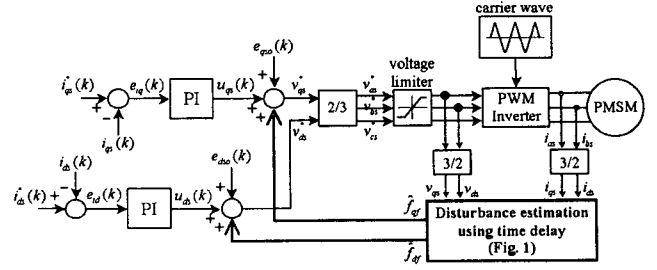


Fig. 3 Overall block diagram for the proposed current control scheme

These voltages are limited so that the conduction times of each switch cannot be in excess of the sampling time during large transient periods such as a starting and a sudden load change. The reference phase voltages are generated in the form of step waves at the positive peak of the carrier wave and are compared with the carrier wave to produce the drive signals for the inverter switches. Also, these reference phase voltages are fed into the disturbance estimator through the voltage limiter to be used for the feedback voltage for the disturbance estimation. The whole control algorithm is implemented using DSP TMS320C31 with a clock frequency of 40 MHz. The sampling period is set to 150 μ sec both in the simulations and experiments. The PM synchronous motor is driven by a three-phase PWM inverter employing the intelligent power module with a switching frequency of 6.67 kHz. The current references in the stationary reference frame are obtained using (5) and the rotor position from an RDC. The phase currents are detected by the Hall-effect devices and transformed to the stationary currents using (6) and (7). The nominal parameters of a PM synchronous motor are listed in Table 1.

Table 1 Specifications of a PM synchronous motor.

Rated power	400 W	Rated speed	3000 rpm
Rated torque	1.274 Nm	Number of poles	4
Magnetic flux	0.16 Wb	Stator resistance	3.0 Ω
Stator inductance	5 mH	Moment of inertia	1.54×10^{-4} Nm \cdot s 2

Fig. 4 shows the simulation results for the performance comparison of the stationary frame current controllers. The q -axis and d -axis current references are given as $i_{qe}^* = 2$ [A] and $i_{de}^* = 0$, respectively, and the motor is operated at 1500 [rpm]. The gains of the PI current controllers for the q -axis and d -axis are selected as $K_p = 20$ and $K_i = 12000$, respectively, so that the controller bandwidth is determined as $\omega_c = 4000$ [rad/sec]. Fig. 4(a) shows the current responses for the PI controller. It is shown that a steady-state current error and a phase delay are clearly present in the phase current responses. Fig. 4(b) shows the current responses for the PI decoupling controller. In the nominal conditions of $\mathbf{f}_s(k) = \mathbf{0}$, it is shown that the PI decoupling controller in the stationary frame can give an ideal response irrespective of operating condition. Fig. 4(c) shows the current responses for the PI decoupling controller under the parameter variations ($\Delta\lambda_m = -0.5\lambda_{m0}$, $\Delta L_s = 1.0L_{s0}$, and $\Delta R_s = 1.0R_{s0}$). Even though an ideal characteristics can be obtained for the nominal parameter values, the steady-state current error is

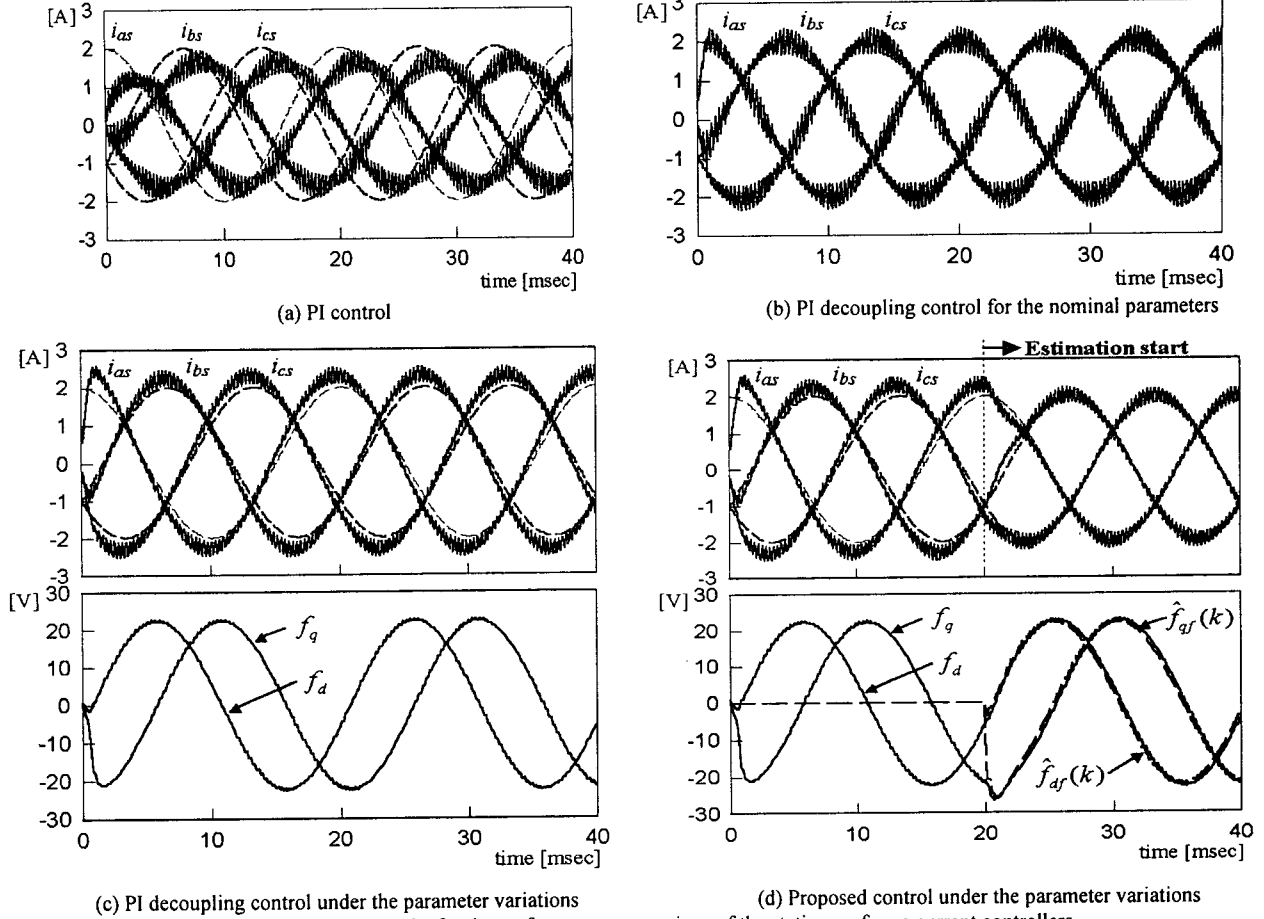


Fig. 4 Simulation results for the performance comparison of the stationary frame current controllers

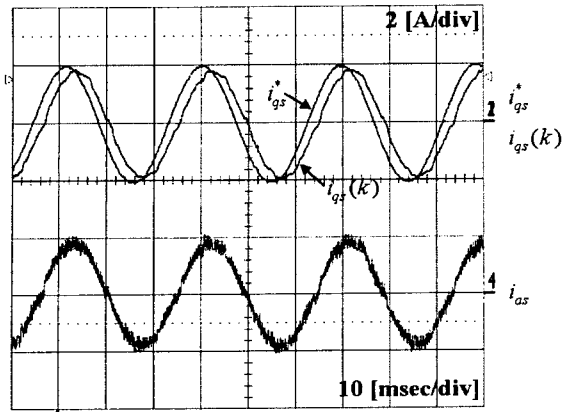
observed in the phase currents under the parameter variations. This is caused by the sinusoidally-varying disturbance voltages due to the parameter variations as can be shown in Fig. 4(c). This degradation is expected to be more serious at high speed operation because the magnitude of the disturbance is proportional to an operating speed. Fig. 4(d) shows the simulation result for the proposed control scheme under the same parameter variations as Fig. 4(c). Before the estimation algorithm starts at $t=20$ [msec], the steady-state errors are observed in the phase currents. However, as soon as the estimation algorithm starts at $t=20$ [msec], the steady-state current errors and phase delays are quickly removed within a few milliseconds. This can be explained by the estimation of the disturbance voltages as shown in this figure. For the design of the time-delayed disturbance estimator, $L=1$ is chosen. Also, $\alpha=2000$ is selected for the cut-off frequency of the low pass filter, which corresponds to the cut-off frequency of 318 Hz. Since the maximum electrical frequency of the used PM synchronous motor is 100 Hz and the inverter switching frequency is 6.67 kHz, the high frequency noise caused by inverter switching can be well suppressed without affecting any influences on the fundamental current component.

Fig. 5 shows the experimental results of the stationary frame current controllers for the performance comparison. The q -axis and d -axis current references are given as $i_{qe}^* = 2$ [A] and $i_{de}^* = 0$, respectively. Also, the experiments are carried out under the same conditions as the simulations.

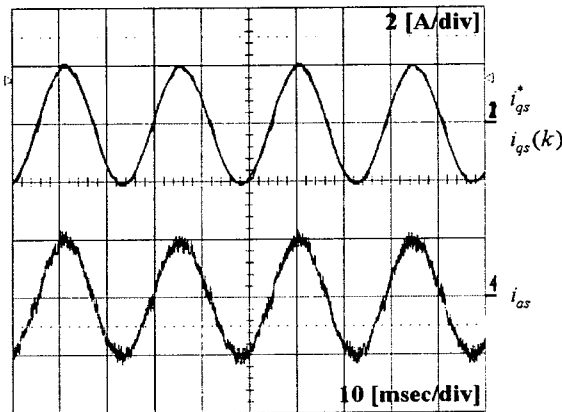
Fig. 5(a) shows the result for the PI controller. Figs. 5(b) and (c) show the control performance for the PI decoupling controller under the nominal parameters and under $\Delta\lambda_m = -0.5\lambda_m$, respectively. Fig. 5(d) shows the current responses and estimated disturbances for the proposed control scheme and the all experimental results are shown to be well coincided with the simulation results.

VI. CONCLUSIONS

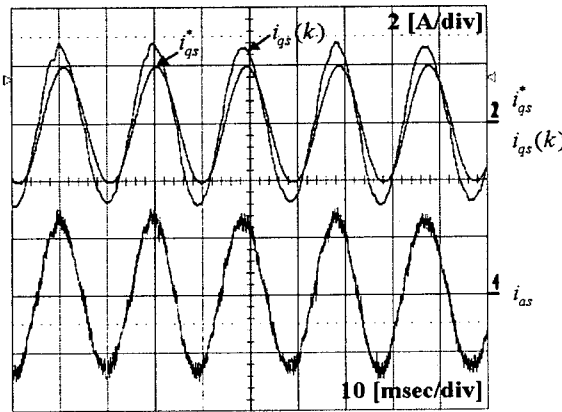
An improved stationary frame-based digital current control scheme for a PM synchronous motor using the time delay control approach is presented. Although the PI controller in the stationary frame gives a steady-state error and a phase delay, the control performance can be greatly improved by employing the exact decoupling control inputs for the back EMF and the PI control with a high bandwidth, resulting in an ideal steady-state control characteristics irrespective of operating conditions as in the synchronous PI decoupling controller. However, its steady-state response may be degraded due to the inexact cancellation inputs under the inexact motor modeling or some parameter variations such as the flux linkage, stator inductance, and stator resistance. This is more serious at high speed under the flux linkage variation since the disturbance is proportional to an operating speed. To improve the current control performance in the stationary frame, the disturbance caused by the parameter variations is estimated by using a



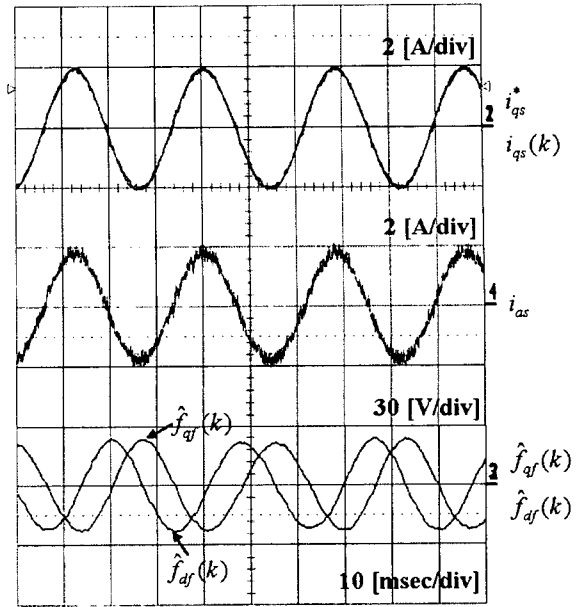
(a) PI control



(b) PI decoupling control for the nominal parameters



(c) PI decoupling control under $\Delta\lambda_m = -0.5\lambda_{m0}$



(d) Proposed control under $\Delta\lambda_m = -0.5\lambda_{m0}$

Fig. 5 Experimental results for the performance comparison

time delay control and the estimated disturbance is used for the computation of the reference voltages by a feedforward manner. The design procedures are straightforward and the current control performance is not affected by the variation of the motor parameters and operating conditions. The whole control algorithm is implemented using DSP TMS320C31 for a PM synchronous motor driven by a three-phase voltage-fed PWM inverter and the effectiveness is verified through the comparative simulations and experiments. As a result, an ideal and a stable control performance can be obtained even in the stationary reference frame.

REFERENCES

- [1] D. M. Brod and D. W. Novotny, "Current control of VSI-PWM inverters," *IEEE Trans. Ind. Applicat.*, vol. 21, no. 4, pp. 562-570, May/June 1985.
- [2] H. L. Huy and L. A. Dessaint, "An adaptive current control scheme for PWM synchronous motor drives: Analysis and simulation," *IEEE Trans. Power Electron.*, vol. 4, no. 4, pp. 486-495, Oct. 1989.
- [3] L. Ben-Brahim and A. Kawamura, "Digital control of induction motor current with deadbeat response using predictive state observer," *IEEE Trans. Power Electron.*, vol. 7, no. 3, pp. 551-559, Jul. 1992.
- [4] D. N. Zmood, D. G. Holmes, and G. H. Bode, "Frequency-domain analysis of three-phase linear current regulators," *IEEE Trans. Ind. Applicat.*, vol. 37, no. 2, pp. 601-610, Mar./Apr. 2001.
- [5] T. M. Rowan and R. J. Kerkman, "A new synchronous current regulator and an analysis of current-regulated PWM inverters," *IEEE Trans. Ind. Applicat.*, vol. 22, no. 4, pp. 678-690, Jul./Aug. 1986.
- [6] D. S. Oh, K. Y. Cho, and M. J. Youn, "A discretized current control technique with delayed input voltage feedback for a voltage-fed PWM inverter," *IEEE Trans. Power Electron.*, vol. 7, no. 2, pp. 364-373, Apr. 1992.
- [7] D. C. Lee, S. K. Sul, and M. H. Park, "High performance current regulator for a field-oriented controlled induction motor drive," *IEEE Trans. Ind. Applicat.*, vol. 30, no. 5, pp. 1247-1257, Sep./Oct. 1994.
- [8] L. Zhang, R. Norman, and W. Shepherd, "Long-range predictive control of current regulated PWM for induction motor drives using the synchronous reference frame," *IEEE Trans. Contr. Syst. Technol.*, vol. 5, no. 1, pp. 119-126, Jan. 1997.
- [9] K. H. Kim, I. C. Baik, and M. J. Youn, "An improved digital current control of a PM synchronous motor with a simple feedforward disturbance compensation scheme," in *Proc. IEEE Int. Conf. PESC'98*, 1998, pp. 101-107.
- [10] K. H. Kim and M. J. Youn, "A simple and robust digital current control technique of a PM synchronous motor using time delay control approach," *IEEE Trans. Power Electron.*, vol. 16, no. 1, pp. 72-82, Jan. 2001.
- [11] K. Youcef-Toumi and O. Ito, "A time delay controller for systems with unknown dynamics," *ASME J. Dyn. Sys., Meas., Contr.*, vol. 112, pp. 133-142, Mar. 1990.
- [12] K. Ogata, *Discrete-time Control Systems*. Prentice-Hall, 1987.
- [13] *TMS320C30 Assembly Language Tools User's Guide*, Texas Instrument, 1990.