

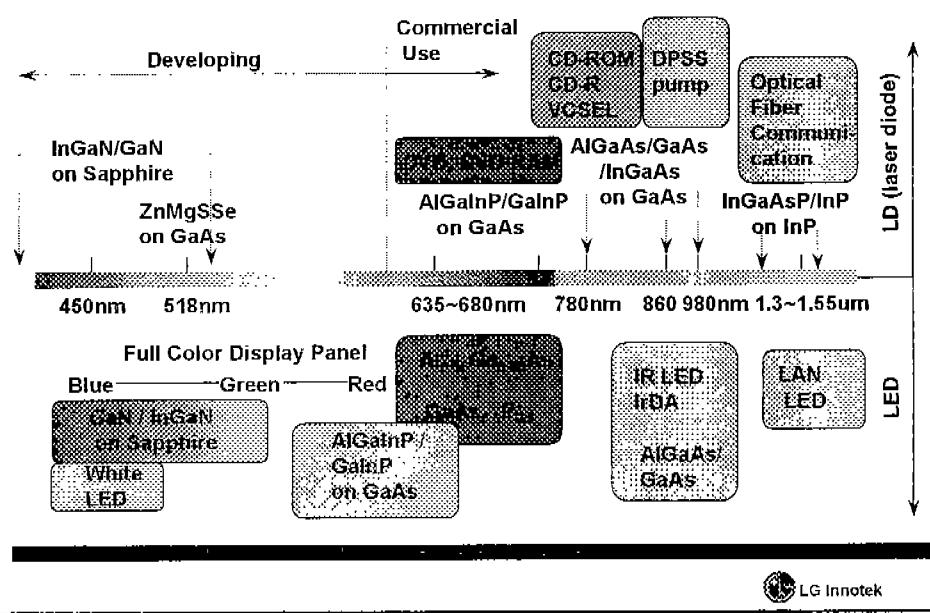
광소자용 반도체 소재 및 공정

(Material and Process Issues in Laser Diode and LED)

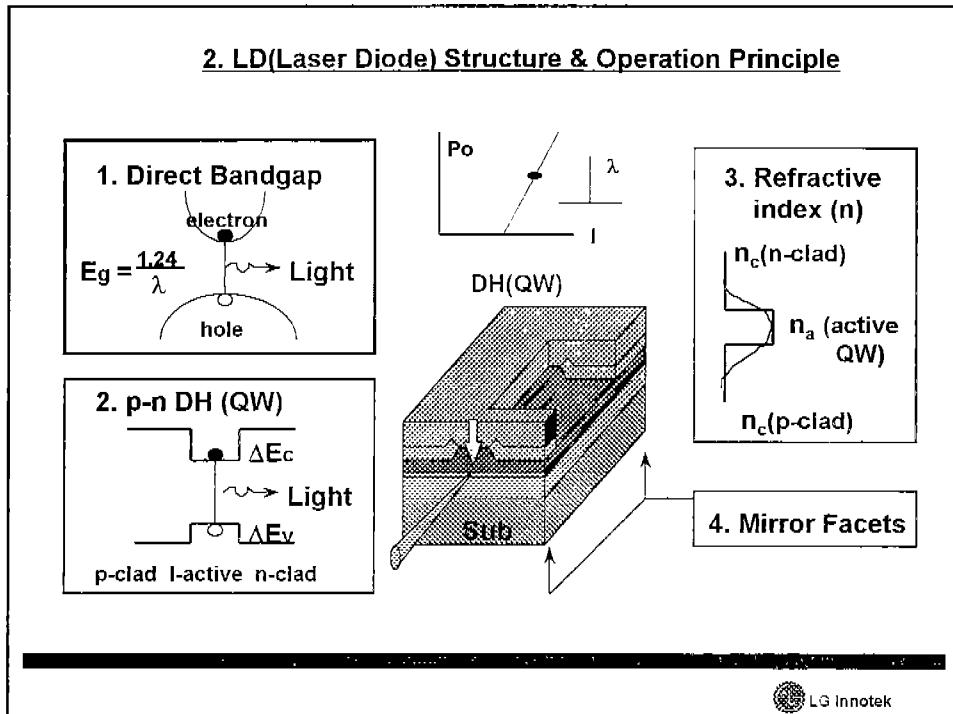
Contents

- 1. Compound Semiconductor & Optical Components**
- 2. LD(Laser Diode) Structure & Operation Principle**
- 3. Laser Diode Fabrication Process and Equipment**
- 4. Technical Trend in Laser Diode Package**

1. Compound Semiconductor & Optical Components



2. LD(Laser Diode) Structure & Operation Principle



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3. Laser Diode Fabrication Process and Equipment

LD Chip Fabrication Process			
Process			
Equipment	MOCVD/LP W/S, OVEN	ALIGNER, E-BEAM, LAPPING MACHINE	SCRIBER, SPUTTER
Comments	1. Crystal Growth - NH Growth - CVD Selective Growth	2. Electrode Formation & Lapping	3. Chip Bar Breaking & Facet Coating
			4. Chip Breaking

LD Assembly and Testing		
Process		
Equipment	CHIP TESTER	WIRE BONDER, DIP BONDER, WELDER
Comments	1. Chip I-I Testing	2. LD/MPD Bonding & Wiring
		3. Burn-in test & final test

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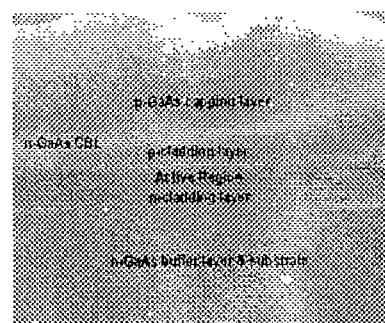
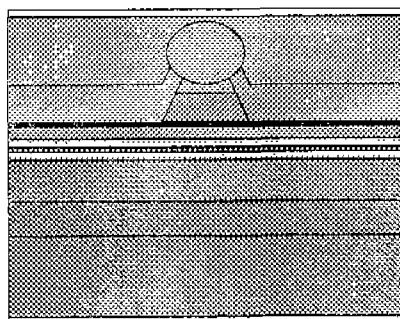
MOCVD 1st(DH) Growth Structure for 650nm LD

Layer	Material	thickness	Dop.
p+ Cap	GaAs	0.5μm	2x10 ¹⁹
p-buffer	In _{0.5} Ga _{0.5} P	0.1μm	2x10 ¹⁸
2nd p-Clad	In _{0.8} (Ga _{0.3} Al _{0.7}) _{0.5} P	0.85 - 0.9μm	1x10 ¹⁹
p-EVL	0.3% In _x Ga _{1-x} P	70Å	1x10 ¹⁹
1st p-Clad	In _{0.8} (Ga _{0.27} Al _{0.73}) _{0.5} P	0.15 - 0.25μm	6-7x10 ¹⁷
SCH	In _{0.8} (Ga _{0.48} Al _{0.52}) _{0.5} P	0.1- 0.12μm	1x10 ¹⁹
4QW Active (strain compensated structure)	0.8% compressively strained In _x Ga _{1-x} P well, 0.3% tensilely strained In _x (Ga _{0.48} Al _{0.52}) _{1-x} P barrier	45Å 60Å	
SCH	In _{0.5} (Ga _{0.48} Al _{0.52}) _{0.5} P	0.1- 0.12μm	
n-Clad	In _{0.5} (Ga _{0.3} Al _{0.7}) _{0.5} P	0.8-1.0μm	1x10 ¹⁹
n+-Buffer	GaAs	0.8μm	2x10 ¹⁸

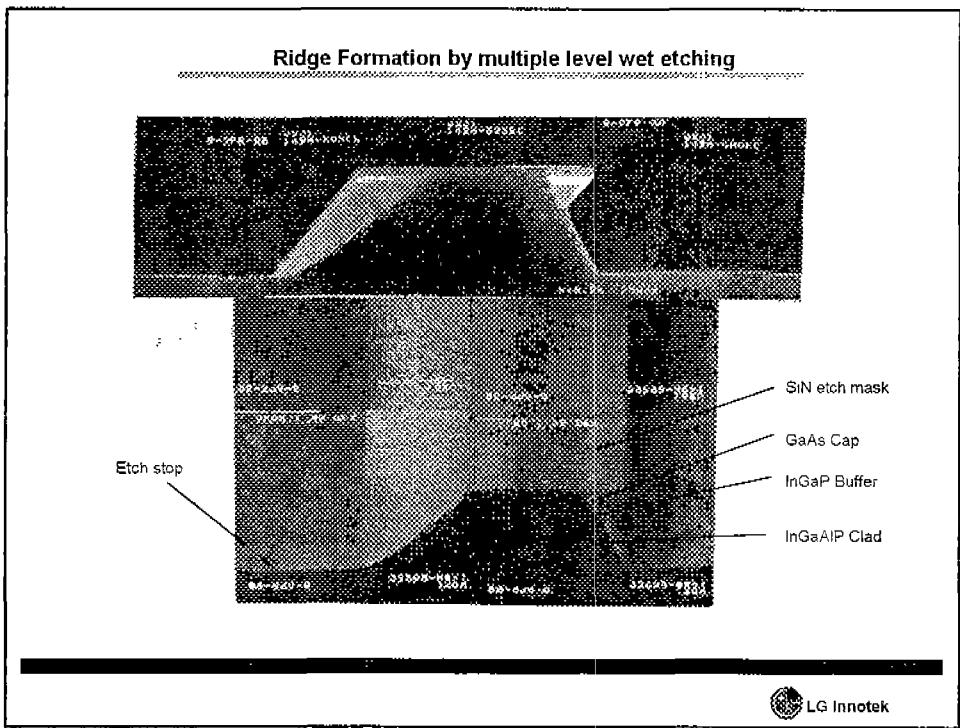
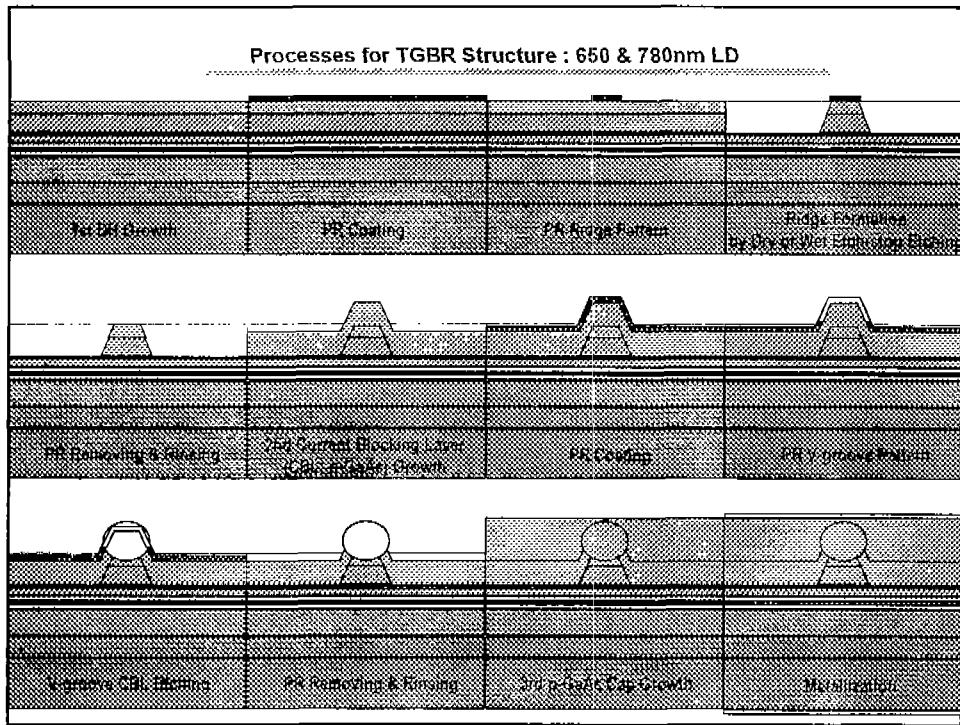
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Total Growth Buried Ridge Structure

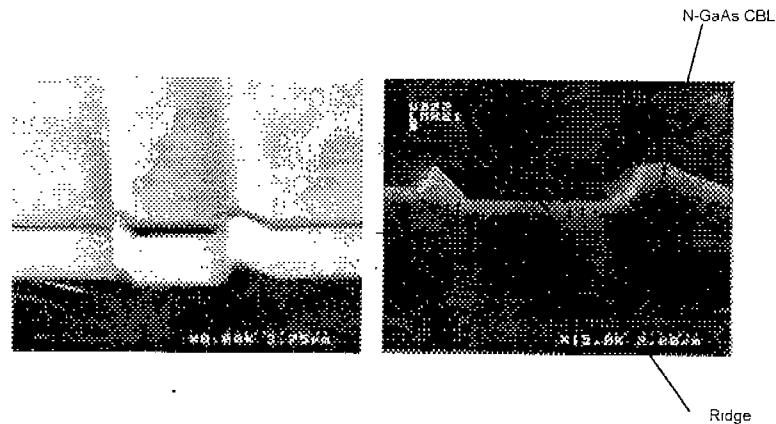
SEM Characterization



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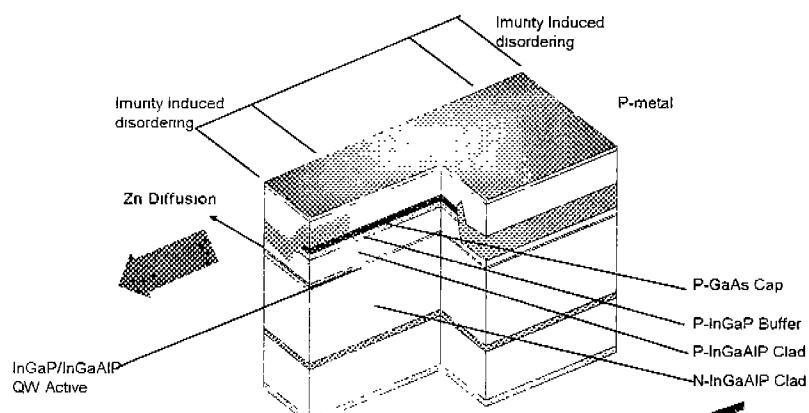


Selective growth of GaAs Current Blocking Layer

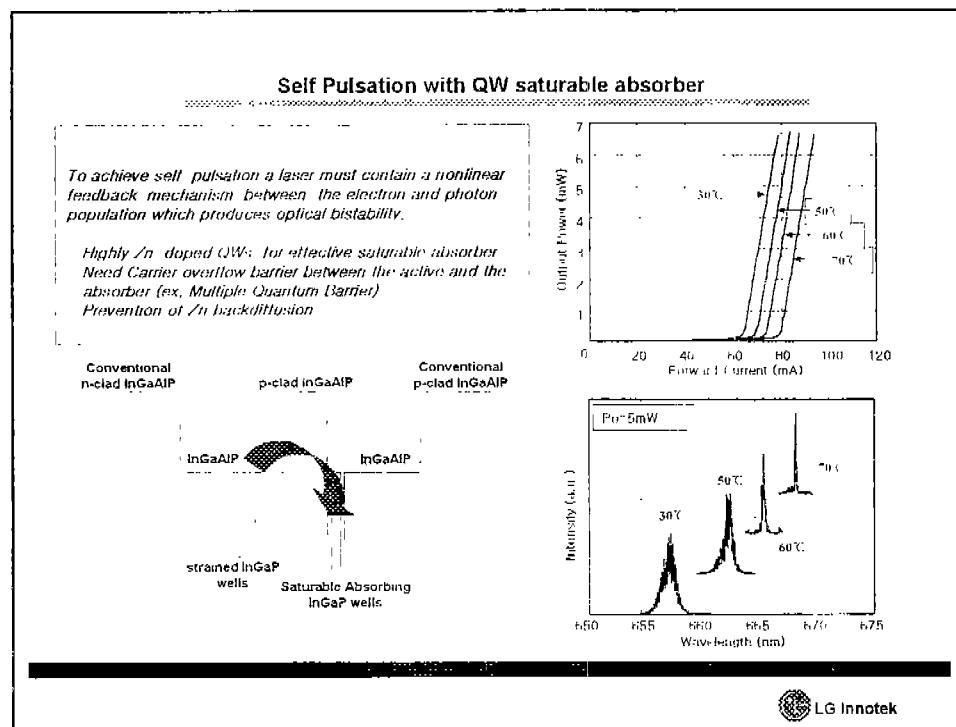
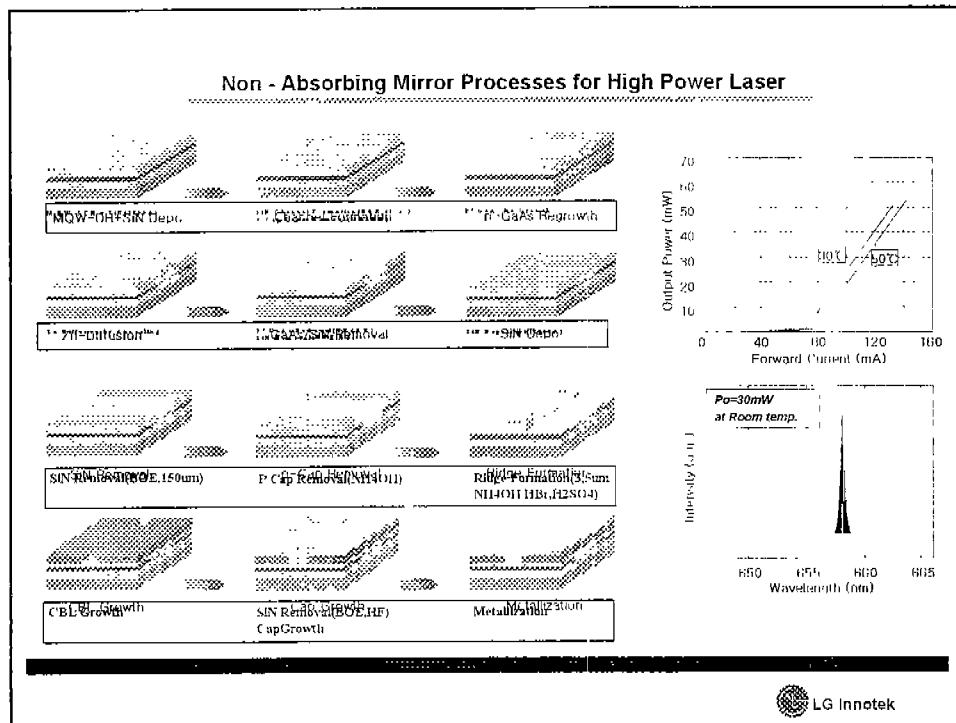


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Schematic of DVD-RAM LD with Non-Absorbing Mirror



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4. Technical Trend in Laser Diode Package

